



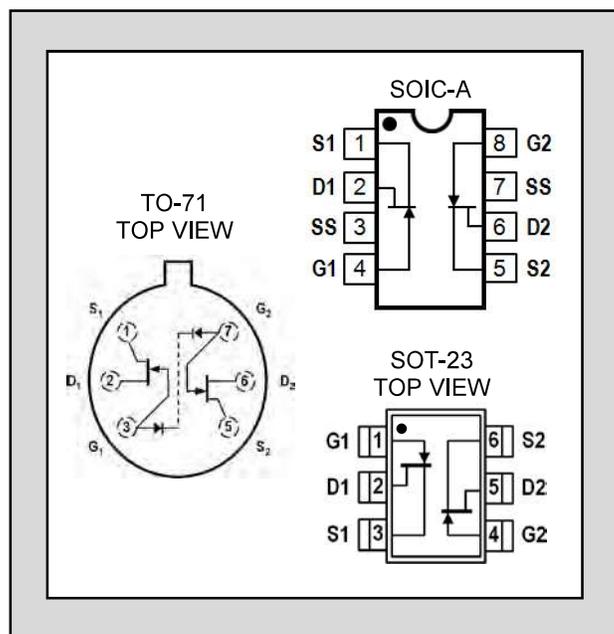
Over 30 Years of Quality Through Innovation

# LSK589

LOW NOISE, LOW CAPACITANCE  
MONOLITHIC DUAL  
N-CHANNEL JFET

FEATURES	
ULTRA LOW NOISE	$e_n = 4.0 \text{ nV}/\sqrt{\text{Hz}}$
LOW INPUT CAPACITANCE	$C_{iss} = 5\text{pF}$
HIGH TRANSCONDUCTANCE	$G_{fs} \geq 4000\mu\text{S}$

ABSOLUTE MAXIMUM RATINGS <sup>1</sup> @ 25 °C (unless otherwise stated)	
<b>Maximum Temperatures</b>	
Storage Temperature	-55 to +150°C
Junction Operating Temperature	-55 to +150°C
<b>Maximum Power Dissipation, TA = 25°C</b>	
Continuous Power Dissipation, per side <sup>4</sup>	250mW
Power Dissipation, total <sup>5</sup>	500mW
<b>Maximum Currents</b>	
Gate Forward Current	$I_{G(F)} = 50\text{mA}$
<b>Maximum Voltages</b>	
Gate to Source	$V_{GS0} = 25\text{V}$
Gate to Drain	$V_{GDO} = 25\text{V}$



### MATCHING CHARACTERISTICS @ 25°C (unless otherwise stated)

SYMBOL	CHARACTERISTIC	MIN	TYP	MAX	UNITS	CONDITIONS
$ V_{GS1} - V_{GS2} $	Differential Gate to Source Cutoff Voltage			20	mV	$V_{DS} = 10\text{V}, I_D = 5\text{mA}$
$\frac{I_{DSS1}}{I_{DSS2}}$	Gate to Source Saturation Current Ratio	0.9		1.0		$V_{DS} = 10\text{V}, V_{GS} = 0\text{V}$ (Note 2)
CMRR	<b>COMMON MODE REJECTION RATIO</b> $-20 \log  \Delta V_{GS1-2}/\Delta V_{DS} $	85			dB	$V_{DG} = 5\text{V to } 10\text{V}, I_D = 5\text{mA}$

SYMBOL	CHARACTERISTIC	MIN	TYP	MAX	UNITS	CONDITIONS
$e_n$	Noise Voltage		7		$\text{nV}/\sqrt{\text{Hz}}$	$V_{DS} = 10\text{V}, I_D = 5\text{mA}, f = 100\text{Hz}$
$e_n$	Noise Voltage		4		$\text{nV}/\sqrt{\text{Hz}}$	$V_{DS} = 10\text{V}, I_D = 5\text{mA}, f = 10\text{kHz}$
$C_{ISS}$	Common Source Input Capacitance			5	pF	$V_{DS} = 10\text{V}, I_D = 5\text{mA}, f = 1\text{MHz}$
$C_{RSS}$	Common Source Reverse Transfer Capacitance			1.2	pF	

