



P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary (Typ. @ V_{GS} = -4.5V, T_A = +25°C)

V _{DSS}	R _{DS(on)}	Qg	Q_{gd}	I _D
-20V	40mΩ	2.3nC	0.4nC	-4.1A

Description

This new generation MOSFET is designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Battery Management
- Load Switch
- Battery Protection

Features

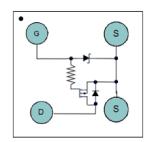
- LD-MOS Technology with the Lowest Figure of Merit:
 - $R_{DS(on)} = 40 m\Omega$ to Minimize On-State Losses
 - Q_g = 2.3nC for Ultra-Fast Switching
- V_{gs(th)} = -0.8V typ. for a Low Turn-On Potential
- CSP with Footprint 1.0mm x 1.0mm
- Height = 0.62mm for Low Profile
- ESD = 3kV HBM Protection of Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Mechanical Data

- Case: U-WLB1010-4
- Terminal Connections: See Diagram Below
- Weight: 0.0018 grams (Approximate)

U-WLB1010-4





Top View Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2047UCB4-7	U-WLB1010-4	3.000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

U-WLB1010-4



DW = Product Type Marking Code YM = Date Code Marking Y = Year (ex: Z = 2012) M = Month (ex: 9 = September)

Date Code Key

Year	201	2	2013		2014	20	15	2016		2017	2	2018
Code	Z		Α		В	(2	D		Е		F
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V _{DSS}	-20	V		
Gate-Source Voltage			V_{GSS}	-6	V
Continuous Drain Current (Note 5) V _{GS} = -4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	l _D	-4.1 -3.2	А
Continuous Drain Current (Note 5) V _{GS} = -2.5V	I _D	-3.6 -2.8	А		
Pulsed Drain Current (Note 6)	I _{DM}	16	A		

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	P _D	1.0	W
Thermal Resistance, Junction to Ambient @ T _A = +25°C (Note 7)	$R_{\theta JA}$	127	°C/W
Thermal Resistance, Junction to Case @ T _C = +25°C (Note 7)	R _{θJC}	25.8	°C/W
Power Dissipation (Note 5)	P _D	1.66	W
Thermal Resistance, Junction to Ambient @ T _A = +25°C (Note 5)	R _{0JA}	77	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

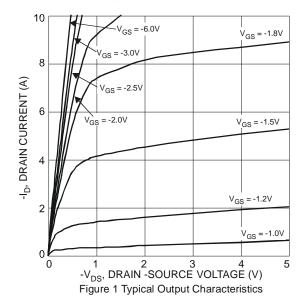
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

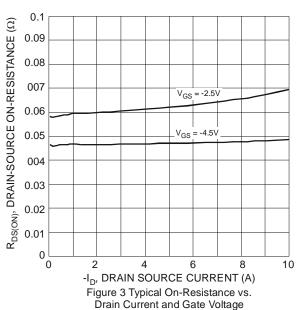
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)	•					•
Drain-Source Breakdown Voltage	BV _{DSS}	-20	1	_	V	$V_{GS} = 0V, I_D = -250\mu A$
Gate-Source Breakdown Voltage	BV _{GSS}	-6.0	_	_	V	$V_{DS} = 0V, I_{G} = -250\mu A$
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	_	_	-1	μA	$V_{DS} = -16V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	1	-	-100	nA	$V_{GS} = -6V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.4	-0.8	-1.2	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$
Static Drain-Source On-Resistance	D-s/s/s	l	40	47	mΩ	$V_{GS} = -4.5V, I_{D} = -1A$
Static Dialif-Source Off-Nesistance	R _{DS(ON)}	1	53	60	11152	$V_{GS} = -2.5V, I_D = -1A$
Forward Transfer Admittance	Y _{fs}	l	3.7	_	S	$V_{DS} = -10V, I_{D} = -1A$
Diode Forward Voltage	V_{SD}	_	-0.7	-1.0	V	$V_{GS} = 0V$, $I_S = -1A$
Reverse Recovery Charge	Q _{rr}	_	3.07	_	nC	$V_{DD} = -10V, I_F = -1A,$
Reverse Recovery Time	t _{rr}	_	13.14	_	ns	di/dt =100A/µs
DYNAMIC CHARACTERISTICS (Note 9)						•
Input Capacitance	Ciss	_	218	_		101/11/101/11
Output Capacitance	Coss	_	116	_	pF	$V_{DS} = -10V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	11	_		I = 1.0WH1Z
Total Gate Charge	Qg	_	2.3	_		
Gate-Source Charge	Q _{gs}	_	0.2	_	nC	$V_{GS} = -4.5V, V_{DS} = -10V,$
Gate-Drain Charge	Q _{gd}	_	0.4	_	IIC	$I_D = -1A$
Gate Charge at Vth	Q _{g(th)}	_	0.2	_		
Turn-On Delay Time	t _{D(on)}	_	7.9	_		
Turn-On Rise Time	t _r	_	10.7	_]	$V_{DS} = -10V, V_{GS} = -2.5V,$
Turn-Off Delay Time	t _{D(off)}	_	48	_	ns	$R_G = 20\Omega$, $I_D = -1A$
Turn-Off Fall Time	t _f	_	38	_	1	

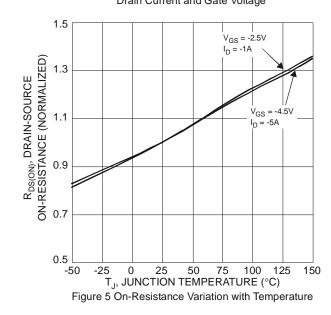
Notes:

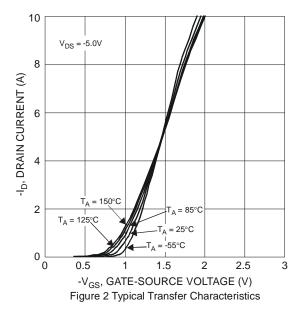
- 5. Device mounted on FR4 material with 1-inch² (6.45-cm²), 2-oz. (0.071-mm thick) Cu.
- Repetitive rating, pulse width limited by junction temperature.
 Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
- 8. Short duration pulse test used to minimize self-heating effect.
- 9. Guaranteed by design. Not subject to production testing.

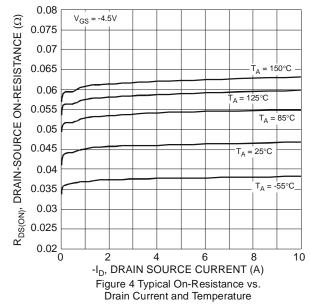


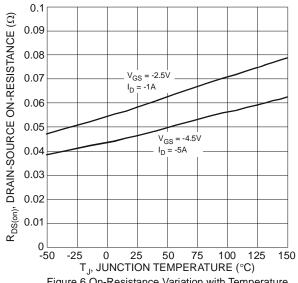














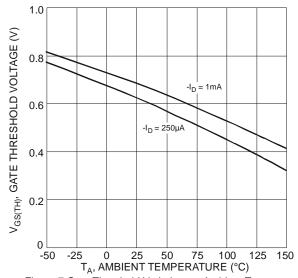


Figure 7 Gate Threshold Variation vs. Ambient Temperature

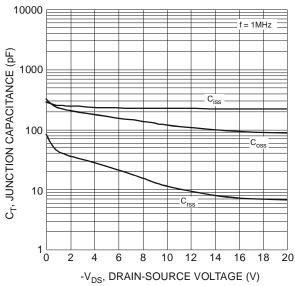
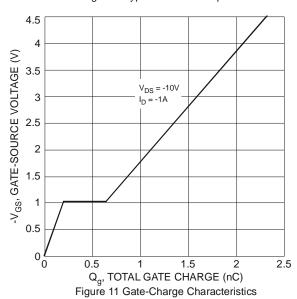
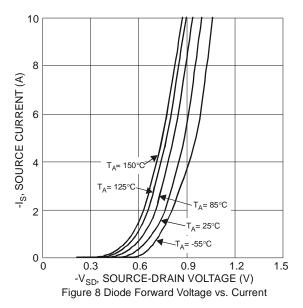


Figure 9 Typical Junction Capacitance





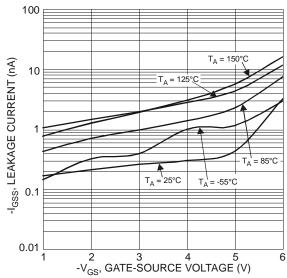


Figure 10 Typical Gate-Source Leakage Current vs. Voltage

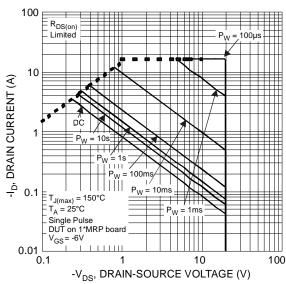
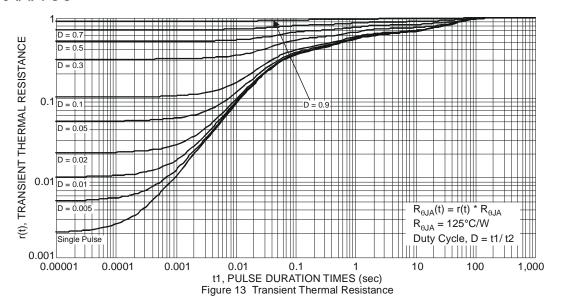


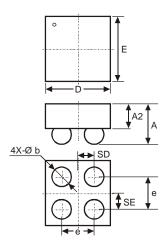
Figure 12 SOA, Safe Operation Area





Package Outline Dimensions

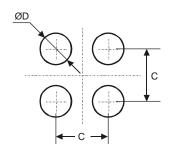
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



U-WLB1010-4							
Dim	Min	Max	Тур				
D	0.95	1.05	1.00				
Е	0.95	0.95 1.05 1.00					
Α	A – 0.62 –						
A2	_	_	0.38				
b	0.25	0.35	0.30				
е	e – – 0.50						
SD	SD – 0.25						
SE	_	_	0.25				
All Dimensions in mm							

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)			
С	0.50			
D	0.25			



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