



## **Product Summary**

BV <sub>DSS</sub>	RDS(ON) Max	Ι <sub>D</sub> T <sub>A</sub> = +25°C
-20V	$35m\Omega @ V_{GS} = -4.5V$	-6.0A
	45mΩ @ V <sub>GS</sub> = -2.5V	-5.2A

# **Description and Applications**

This MOSFET is designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- **DC-DC** Converters
- Motor Control
- Power Management Functions
- Analog Switch

# ESD PROTECTED TO 3kV



TSOT26



Top View

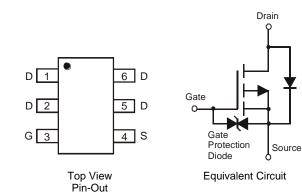
-20V P-CHANNEL ENHANCEMENT MODE MOSFET

# **Features and Benefits**

- Low Input Capacitance
- Low On-Resistance
- Fast Switching Speed
- ESD Protected up to 3kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

# **Mechanical Data**

- Case: TSOT26
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 @3
- Weight: 0.013 grams (Approximate)



## Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2035UVT-7	TSOT26	3,000/Tape & Reel
DMP2035UVT-13	TSOT26	10,000/Tape & Reel

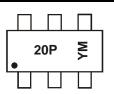
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead\_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + CI) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# Marking Information



20P = Product Type Marking Code YM = Date Code Marking Y or  $\overline{Y}$  = Year (ex: Y = 2011)

M = Month (ex: 9 = September)

### Date Code Key

Notes:

Date Code Re	≠y											
Year	2011	~	20	016	2017	2018	3	2019	2020	202	21	2022
Code	Y	~		D	Е	F		G	Н			J
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V <sub>DSS</sub>	-20	V	
Gate-Source Voltage		V <sub>GSS</sub>	±12	V	
		T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	ID	-6.0 -4.8	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = -4.5V	t<10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-7.2 -5.7	A
	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-5.2 -4.1	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = -2.5V	t<10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	۱ <sub>D</sub>	-6.2 -4.9	А
Maximum Continuous Body Diode Forward Curren	t (Note 6)	ls	-2.0	А	
Pulsed Drain Current (10µs Pulse, Duty Cycle = 19	I <sub>DM</sub>	-24	А		

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	PD	1.2	W	
Thermal Desistance, Junction to Ambient (Note 5)	Steady State	P	106	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{ heta}JA$	74	C/W
Total Power Dissipation (Note 6)		PD	2.0	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	65	
mermai Resistance, Junction to Ambient (Note 6)	t<10s	κ <sub>θ</sub> ja	R <sub>0JA</sub> 46	
Thermal Resistance, Junction to Case (Note 6)	Steady State	$R_{\theta JC}$	11.8	
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	• <b>,</b> •		- 71-		•	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20			V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_		-1	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	_		±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note7)						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.4	-0.7	-1.5	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$
Gate Threshold Voltage Temperature Coefficient	$_{\triangle}V_{GS(TH)}/_{\triangle}T_{J}$	_	2.5	_	mV/°C	$I_D = -250\mu A$ ,Referenced to +25°C
			23	35		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4.0A
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	30	45	mΩ	V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -4.0A
			41	62		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -2.0A
Forward Transfer Admittance	Y <sub>fs</sub>	_	18	_	S	V <sub>DS</sub> = -5V, I <sub>D</sub> = -5.5A
Diode Forward Voltage (Note 6)	V <sub>SD</sub>	_	-0.7	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		1,610	2,400		
Output Capacitance	C <sub>oss</sub>		157	210	pF	$V_{DS} = -10V$ , $V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	Crss		145	200		
Gate Resistance	R <sub>G</sub>	_	9.4	14.1	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge	Qg	_	15.4	23.1		
Gate-Source Charge	Q <sub>gs</sub>	_	2.5		nC	$V_{DS} = -10V, V_{GS} = -4.5V$ $I_{D} = -4A$
Gate-Drain Charge	Q <sub>gd</sub>	_	3.3			ID = -4A
Turn-On Delay Time	t <sub>D(ON)</sub>	_	17	33		
Turn-On Rise Time	t <sub>R</sub>	_	12	19	ns	$V_{GS} = -4.5V, V_{DS} = -10V, R_G = 6\Omega$
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	94	150	115	$I_D = -1A, R_L = 10\Omega$
Turn-Off Fall Time	t <sub>F</sub>	_	42	64		
Reverse Recovery Time	t <sub>RR</sub>	_	14	25	ns	$I_{-} = 4.50$ di/dt=1000/uS
Reverse Recovery Charge	Q <sub>RR</sub>		4	8	nC	I <sub>F</sub> =-4.5A, di/dt=100A/μS

 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. Notes:

7. Short duration pulse test used to minimize self-heating effect.

8. Guaranteed by design. Not subject to product testing.





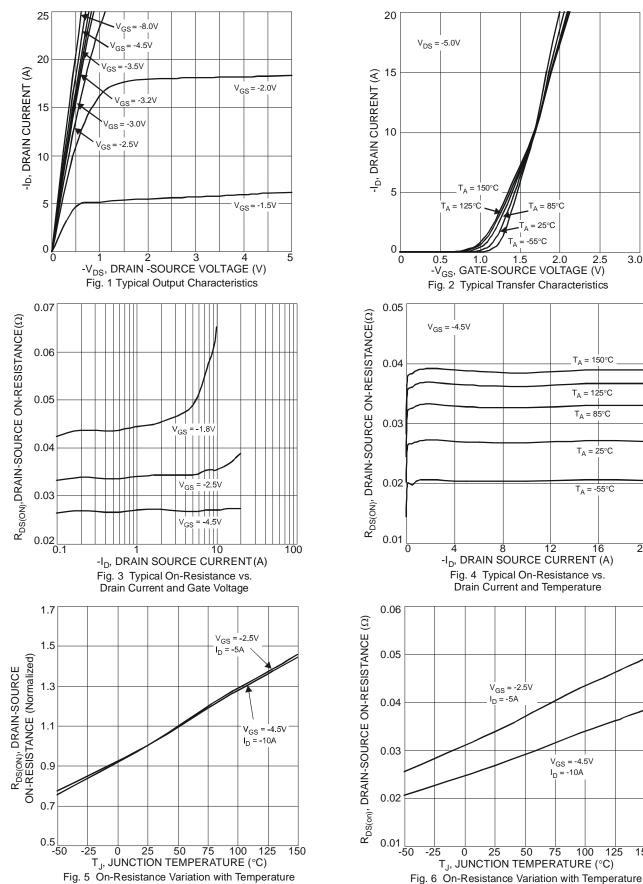
3.0

20

2.5

 $T_A = 25^{\circ}C$ 

16



DMP2035UVT Document number: DS35190 Rev. 7 - 2 150



1.2

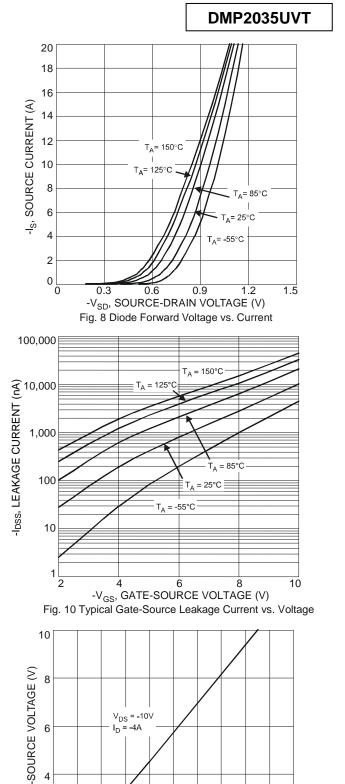
1.0

0.8

0.6

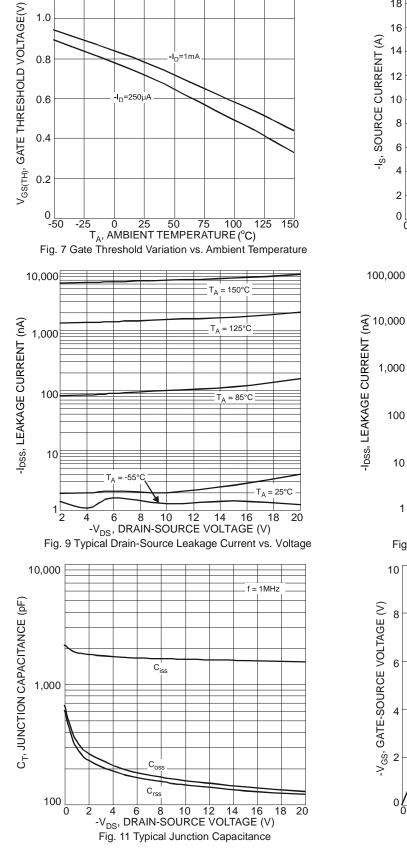
0.4

0.2



8 12 16 20 24 28 32 Q<sub>g</sub>, TOTAL GATE CHARGE (nC)

Fig. 12 Gate-Charge Characteristics



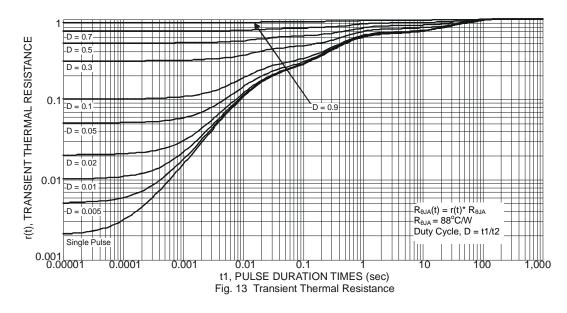
=1mA

l<sub>D</sub>=250μA

36 40

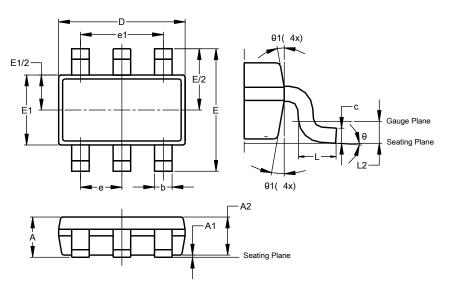
DMP2035UVT Document number: DS35190 Rev. 7 - 2





# **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.



	TSOT26							
Dim	Min	Max	Тур					
Α	- 1.00		-					
A1	0.010	0.100	-					
A2	0.840	0.900	-					
D	2.800	3.000	2.900					
Е	2	2.800 BSC						
E1	1.500	1.700	1.600					
b	0.300	0.450	-					
С	0.120	0.200	-					
е	0.950 BSC							
e1	1.900 BSC							
L	0.30	0.50	-					
L2	0.250 BSC							
θ	0°	8°	4°					
θ1	4°	12°	-					
A	II Dimen	sions in	mm					

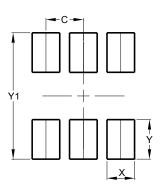
## TSOT26



# Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

TSOT26



Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.199

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