

High Temperature Silicon Carbide Power Schottky Diode

V_{RRM} = 1200 V $I_{F (Tc=25^{\circ}C)}$ = 30 A Q_{C} = 58 nC

Features

- 1200 V Schottky rectifier
- 250 °C maximum operating temperature
- · Electrically isolated base-plate
- Zero reverse recovery charge
- Superior surge current capability
- Positive temperature coefficient of V_F
- Temperature independent switching behavior
- Lowest figure of merit Q_C/I_F
- Available screened to Mil-PRF-19500

Advantages

- High temperature operation
- Improved circuit efficiency (Lower overall cost)
- · Low switching losses
- · Ease of paralleling devices without thermal runaway
- Smaller heat sink requirements
- Industry's lowest reverse recovery charge
- Industry's lowest device capacitance
- Ideal for output switching of power supplies
- Best in class reverse leakage current at operating temperature

Package

RoHS Compliant



TO - 257 (Isolated Base-plate Hermetic Package)

Applications

- Down Hole Oil Drilling, Geothermal Instrumentation
- High Temperature DC/DC Converters
- · High Temperature Motor and Servo Drives
- High Temperature Inverters
- High Temperature Actuator Control
- Military Power Supplies

Maximum Ratings at T_i = 250 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Repetitive peak reverse voltage	V_{RRM}		1200	V
Continuous forward current	I _F	T _C = 25 °C	30	Α
Continuous forward current	I _F	T _C ≤ 225 °C	9.4	Α
RMS forward current	I _{F(RMS)}	T _C ≤ 225 °C	16	Α
Surge non-repetitive forward current, Half Sine Wave	$I_{F,SM}$	T_C = 25 °C, t_P = 10 ms	65	Α
Non-repetitive peak forward current	$I_{F,max}$	T _C = 25 °C, t _P = 10 μs	280	Α
l ² t value	∫i² dt	$T_{\rm C}$ = 25 °C, $t_{\rm P}$ = 10 ms	20	A^2S
Power dissipation	P _{tot}	T _C = 25 °C	230	W
Operating and storage temperature	T_{j} , T_{stg}		-55 to 250	°C

Electrical Characteristics at T_j = 250 °C, unless otherwise specified

Downworks w	Comple ed	Conditions —		Values		1114	
Parameter	Symbol			min.	typ.	max.	Unit
Diode forward voltage	\/	I _F = 10 A, T _j = 25 °C		1.6		V	
	V_{F}	I _F = 10 A, T _j = 210 °C			2.3		V
Reverse current	1	V _R = 1200 V, T _j = 25 °C		1	20	μΑ	
	I _R	$V_R = 1200 \text{ V}, T_j = 250 ^{\circ}\text{C}$		55	300		
Total capacitive charge	$Q_{\rm C}$	V _R = 400			58		nC
	Q C	$I_F \le I_{F,MAX}$ - $dI_F/dt = 200 A/\mu s$	V _R = 960 V		95		110
Switching time	+	T _i = 210 °C	V _R = 400 V		< 49		ne
	t _s	., 2.0 0	V _R = 960 V	\ 49	~ 49		ns
Total capacitance		$V_R = 1 \text{ V, f} = 1 \text{ MHz, T}_j = 25 \text{ °C}$		884		pF	
	С	$V_R = 400 \text{ V}, f = 1 \text{ MHz}, T_j = 25 ^{\circ}\text{C}$		79			
		$V_R = 1000 \text{ V}, f = 1 \text{ MHz}, T_i = 25 ^{\circ}\text{C}$		63			

Thermal Characteristics

Thermal resistance, junction - case	R_{thJC}	1.08	°C/W
Mechanical Properties			

Μ

Mounting torque

Nm

0.6



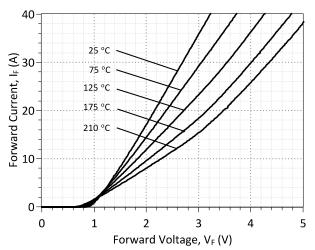


Figure 1: Typical Forward Characteristics

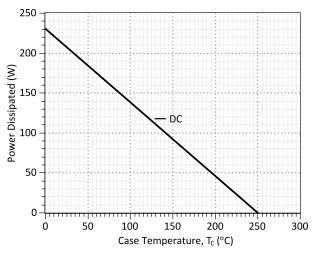


Figure 3: Power Derating Curve

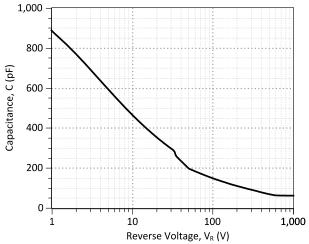


Figure 5: Typical Junction Capacitance vs Reverse Voltage Characteristics

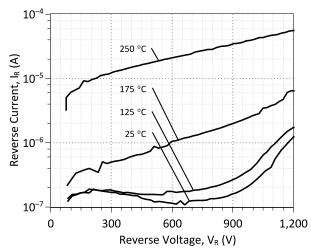
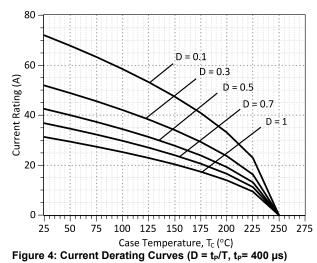


Figure 2: Typical Reverse Characteristics



(Considering worst case Z_{th} conditions)

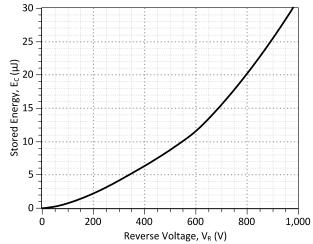


Figure 6: Typical Switching Energy vs Reverse Voltage Characteristics



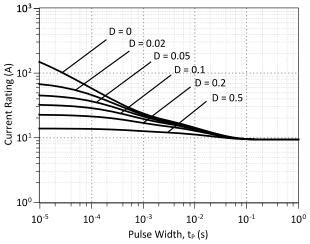


Figure 7: Current vs Pulse Duration Curves at T_c = 225 °C

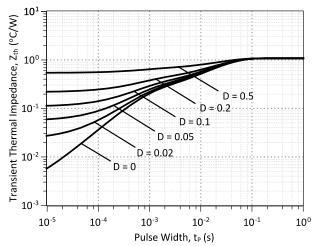
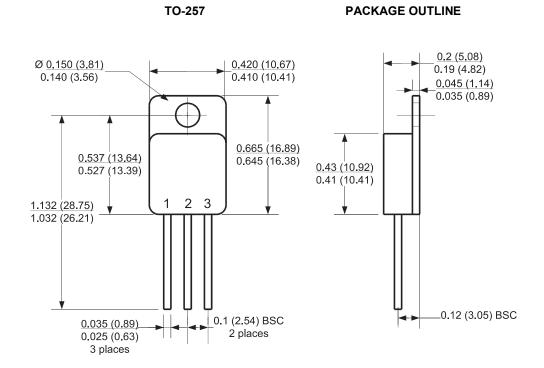


Figure 8: Transient Thermal Impedance

Package Dimensions:



- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS





Revision History					
Date	Revision	Comments	Supersedes		
2014/08/26	1	Updated Electrical Characteristics			
2012/04/24	0	Initial release			

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SPICE Model Parameters

This is a secure document. Copy this code from the SPICE model PDF file on our website into a SPICE software program for simulation of the 1N8028-GA.

```
MODEL OF GeneSiC Semiconductor Inc.
    $Revision: 1.0
                               $
    $Date: 05-SEP-2013
    GeneSiC Semiconductor Inc.
    43670 Trade Center Place Ste. 155
    Dulles, VA 20166
    COPYRIGHT (C) 2013 GeneSiC Semiconductor Inc.
     ALL RIGHTS RESERVED
* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
* PARTICULAR PURPOSE."
* Models accurate up to 2 times rated drain current.
* Start of 1N8028-GA SPICE Model
.SUBCKT 1N8028 ANODE KATHODE
D1 ANODE KATHODE 1N8028 25C; Call the Schottky Diode Model
D2 ANODE KATHODE 1N8028 PIN; Call the PiN Diode Model
.MODEL 1N8028 25C D
      1.74E-13
+ IS
                                     0.05105
                          RS
+ TRS1
         0.005
                         TRS2
                                     1.68E-5
         1.2637323
                         IKF
                                     1.884319
+ EG
         1.2
                         XTI
                                     3
+ CJO
         1.15E-09
                         VJ
                                    0.44
+ M
         1.5
                          FC
                                    0.5
         1.00E-10
+ TT
                         BV
                                    1200
+ IBV
         1.00E-03
                          VPK
                                    1200
+ IAVE
          20
                          TYPE
                                    SiC Schottky
      GeneSiC Semiconductor
+ MFG
.MODEL 1N8028 PIN D
+ IS 5.15E-15
                         RS
                                     0.2
+ N
         3.1605
                         IKF
                                    0.00055844
         3.23
+ EG
                         XTI
                                     3
+ FC
         0.5
                                    0
                         TT
+ BV
         1200
                                    1.00E-03
                         IBV
+ VPK
          1200
                          IAVE
                                     20
         SiC PiN
+ TYPE
.ENDS
```

* End of 1N8028-GA SPICE Model