

High Temperature Silicon Carbide Power Schottky Diode

V_{RRM} = 1200 V $I_{F (Tc=25^{\circ}C)}$ = 8 A Q_{C} = 17 nC

Features

- 1200 V Schottky rectifier
- 250 °C maximum operating temperature
- Electrically isolated base-plate
- Zero reverse recovery charge
- Superior surge current capability
- Positive temperature coefficient of V_F
- Temperature independent switching behavior
- Lowest figure of merit Q_C/I_F
- Available screened to Mil-PRF-19500

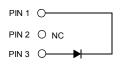
Advantages

- High temperature operation
- Improved circuit efficiency (Lower overall cost)
- · Low switching losses
- Ease of paralleling devices without thermal runaway
- Smaller heat sink requirements
- Industry's lowest reverse recovery charge
- Industry's lowest device capacitance
- Ideal for output switching of power supplies
- Best in class reverse leakage current at operating temperature

Package

RoHS Compliant





TO - 257 (Isolated Base-plate Hermetic Package)

Applications

- Down Hole Oil Drilling, Geothermal Instrumentation
- High Temperature DC/DC Converters
- High Temperature Motor and Servo Drives
- High Temperature Inverters
- High Temperature Actuator Control
- Military Power Supplies

Maximum Ratings at T_i = 250 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Repetitive peak reverse voltage	V_{RRM}		1200	V
Continuous forward current	I _F	T _C = 25 °C	8	Α
Continuous forward current	I _F	T _C ≤ 225 °C	2.5	Α
RMS forward current	I _{F(RMS)}	T _C ≤ 225 °C	4.3	Α
Surge non-repetitive forward current, Half Sine Wave	$I_{F,SM}$	T_C = 25 °C, t_P = 10 ms	30	Α
Non-repetitive peak forward current	$I_{F,max}$	$T_{\rm C}$ = 25 °C, $t_{\rm P}$ = 10 μ s	120	Α
l ² t value	∫i² dt	$T_{\rm C}$ = 25 °C, $t_{\rm P}$ = 10 ms	5	A ² S
Power dissipation	P _{tot}	T _C = 25 °C	66	W
Operating and storage temperature	T_j , T_stg		-55 to 250	°C

Electrical Characteristics at T_j = 250 °C, unless otherwise specified

Douare store	0	Conditions		Values		1114	
Parameter	Symbol			min.	typ.	max.	Unit
Diode forward voltage	V _F	$I_F = 2.5 \text{ A}, T_j =$	I _F = 2.5 A, T _j = 25 °C		1.6		V
	٧F	I _F = 2.5 A, T _j = 250 °C		2.8		V	
Reverse current	1	$V_R = 1200 \text{ V}, T_j = 25 ^{\circ}\text{C}$		1	10	μΑ	
	I _R	$V_R = 1200 \text{ V}, T_j = 250 ^{\circ}\text{C}$			25		200
Total capacitive charge	Q_{C}	V _R = 400			17		nC
		$I_F \le I_{F,MAX}$	V _R = 960 V		29		
Switching time	4	dI _F /dt = 200 A/µs T _i = 210 °C	V _R = 400 V		< 25		no
	t _s	., 210 0	$V_{R} = 960 \text{ V}$		\ Z5	\ 2 5	ns
Total capacitance	С	V _R = 1 V, f = 1 MHz, T _j = 25 °C		237			
		$V_R = 400 \text{ V}, f = 1 \text{ MHz}, T_j = 25 ^{\circ}\text{C}$		25		pF	
		V _R = 1000 V, f = 1 MH	= 1000 V, f = 1 MHz, T _i = 25 °C		20		

Thermal Characteristics

Thermal resistance, junction - case	R _{thJC}	3.4	°C/W

Mechanical Properties

Mounting torque	M	0.6	Nm

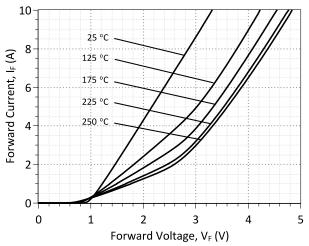


Figure 1: Typical Forward Characteristics

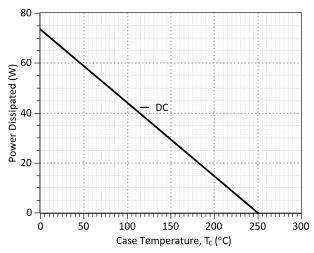


Figure 3: Power Derating Curve

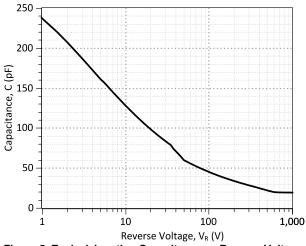


Figure 5: Typical Junction Capacitance vs Reverse Voltage Characteristics

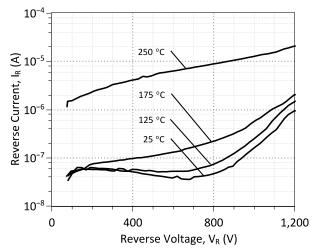
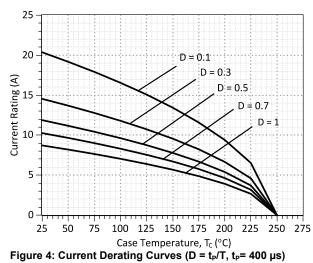


Figure 2: Typical Reverse Characteristics



(Considering worst case Z_{th} conditions)

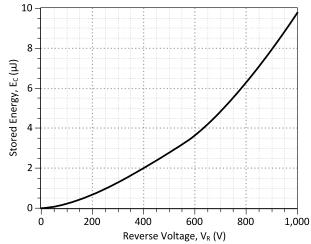


Figure 6: Typical Switching Energy vs Reverse Voltage Characteristics



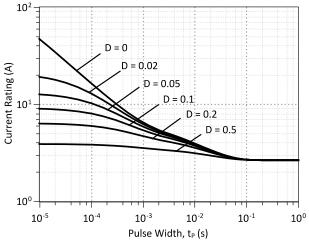


Figure 7: Current vs Pulse Duration Curves at T_C = 225 °C

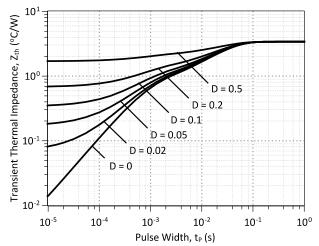
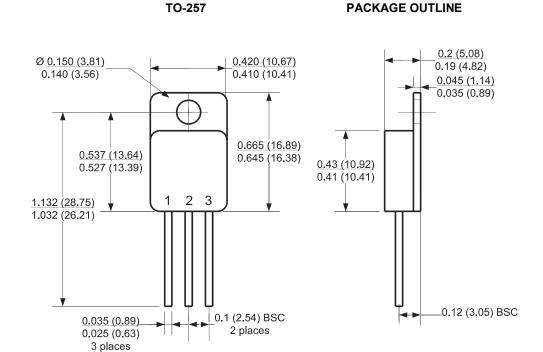


Figure 8: Transient Thermal Impedance

Package Dimensions:



- 1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.
 2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS





Revision History						
Date	Revision	Comments	Supersedes			
2014/08/26	1	Updated Electrical Characteristics				
2012/04/24	0	Initial release				

Published by GeneSiC Semiconductor, Inc. 43670 Trade Center Place Suite 155 Dulles, VA 20166

GeneSiC Semiconductor, Inc. reserves right to make changes to the product specifications and data in this document without notice.

GeneSiC disclaims all and any warranty and liability arising out of use or application of any product. No license, express or implied to any intellectual property rights is granted by this document.

Unless otherwise expressly indicated, GeneSiC products are not designed, tested or authorized for use in life-saving, medical, aircraft navigation, communication, air traffic control and weapons systems, nor in applications where their failure may result in death, personal injury and/or property damage.



SPICE Model Parameters

This is a secure document. Copy this code from the SPICE model PDF file on our website into a SPICE software program for simulation of the 1N8026-GA.

```
MODEL OF GeneSiC Semiconductor Inc.
     $Revision: 1.0
     $Date: 05-SEP-2013
     GeneSiC Semiconductor Inc.
     43670 Trade Center Place Ste. 155
    Dulles, VA 20166
    COPYRIGHT (C) 2013 GeneSiC Semiconductor Inc.
     ALL RIGHTS RESERVED
* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
* PARTICULAR PURPOSE."
* Models accurate up to 2 times rated drain current.
* Start of 1N8026-GA SPICE Model
.SUBCKT 1N8026 ANODE KATHODE
R1 ANODE INT R=((TEMP-24)*0.0021); Temperature Dependant Resistor
D1 INT KATHODE 1N8026 25C; Call the 25C Diode Model
D2 ANODE KATHODE 1N80\overline{2}6 PIN; Call the PiN Diode Model
.MODEL 1N8026 25C D
        4.45E-15
+ IS
                                     0.206
                          RS
         1.18144
                                     112.92
+ N
                          IKF
+ EG
         1.2
                          XTI
+ CJO
         3.00E-10
                                     0.419
                         VJ
+ M
         1.6
                          FC
                                     0.5
        1.00E-10
+ TT
                                     1200
                          BV
         1.00E-03
                                    1200
+ IBV
                         VPK
+ IAVE
                                     SiC Schottky
                          TYPE
      GeneSiC Semiconductor
+ MFG
.MODEL 1N8026 PIN D
         2.93E-12
+ IS
                          RS
                                    0.35326
+ N
          4.6113
                                    0.0043236
                          IKF
+ EG
         3.23
                         XTI
                                    60
+ FC
          0.5
                         TT
+ BV
                                    1.00E-03
         1200
                          IBV
+ VPK
         1200
                          IAVE
                                     2.5
+ TYPE
        SiC PiN
.ENDS
```

* End of 1N8026-GA SPICE Model