

BFR380L3

NPN Silicon RF Transistor

- High current capability and low noise figure for wide dynamic range
- Low voltage operation
- Ideal for low phase noise oscillators up to 3.5 GHz
- Low noise figure: 1.1 dB at 1.8 GHz
- Pb-free (RoHS compliant) package
- Qualified according AEC Q101



ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Туре	Marking	Pin Configuration			Package	
BFR380L3	FC	1 = B	2 = E	3 = C	TSLP-3-1	

Maximum Ratings at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V _{CEO}	6	V
Collector-emitter voltage	V _{CES}	15	
Collector-base voltage	V _{CBO}	15	
Emitter-base voltage	V _{EBO}	2	
Collector current	I _C	80	mA
Base current	I _B	14	
Total power dissipation ¹⁾	P _{tot}	380	mW
<i>T</i> _S ≤ 96°C			
Junction temperature	TJ	150	°C
Storage temperature	T _{Stg}	-55 150	
Thermal Resistance			

Parameter	Symbol	Value	Unit
Junction - soldering point ²⁾	R _{thJS}	≤ 140	K/W

 $^{1}T_{S}$ is measured on the collector lead at the soldering point to the pcb

²For calculation of R_{thJA} please refer to Application Note AN077 Thermal Resistance



Parameter	Symbol		Values	√alues	
		min.	typ.	max.]
DC Characteristics					
Collector-emitter breakdown voltage	V _{(BR)CEO}	6	9	-	V
$I_{\rm C}$ = 1 mA, $I_{\rm B}$ = 0					
Collector-emitter cutoff current	I _{CES}				nA
$V_{CE} = 5 V, V_{BE} = 0$		-	1	30	
$V_{\rm CE}$ = 15 V, $V_{\rm BE}$ = 0		-	-	1000	
Collector-base cutoff current	I _{CBO}	-	-	30	
$V_{\rm CB} = 5 \text{V}, I_{\rm E} = 0$					
Emitter-base cutoff current	I _{EBO}	-	10	500	
$V_{\rm EB} = 1 \text{V}, I_{\rm C} = 0$					
DC current gain	h _{FE}	90	120	160	-
$I_{\rm C}$ = 40 mA, $V_{\rm CE}$ = 3 V, pulse measured					

Electrical Characteristics at $T_A = 25^{\circ}$ C, unless otherwise specified



Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random sampling	g)	1		1	1
Transition frequency	f _T	11	14	-	GHz
$I_{\rm C}$ = 40 mA, $V_{\rm CE}$ = 3 V, f = 1 GHz					
Collector-base capacitance	C _{cb}	-	0.45	0.8	pF
$V_{\rm CB} = 5 \text{ V}, f = 1 \text{ MHz}, V_{\rm BE} = 0$,					
emitter grounded					
Collector emitter capacitance	C _{ce}	-	0.18	-	
$V_{CE} = 5 \text{ V}, f = 1 \text{ MHz}, V_{BE} = 0$,					
base grounded					
Emitter-base capacitance	C _{eb}	-	1	-	
$V_{\rm EB}$ = 0.5 V, f = 1 MHz, $V_{\rm CB}$ = 0 ,					
collector grounded					
Minimum Noise figure	NF _{min}	0.5	1.1	2.1	dB
$I_{\rm C}$ = 8 mA, $V_{\rm CE}$ = 3 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,					
<i>f</i> = 1.8 GHz					
Power gain, maximum available ¹⁾	G _{ma}				
$I_{\rm C}$ = 40 mA, $V_{\rm CE}$ = 3 V, $Z_{\rm S}$ = $Z_{\rm Sopt,}$ $Z_{\rm L}$ = $Z_{\rm Lopt,}$					
<i>f</i> = 1.8 GHz		11.5	14	16.5	
<i>f</i> = 3 GHz		7.5	10	12.5	
Transducer gain	S _{21e} ²				dB
$I_{\rm C}$ = 40 mA, $V_{\rm CE}$ = 3 V, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω ,					
<i>f</i> = 1.8 GHz		9.5	11.5	13.5	
<i>f</i> = 3 GHz		5.5	7.5	9.5	
Third order intercept point at output ²⁾	IP ₃	-	29.5	-	dBm
V _{CE} = 3 V, <i>I</i> _C = 40 mA, <i>f</i> = 1.8 GHz,					
$Z_{\rm S} = Z_{\rm L} = 50\Omega$					
1dB compression point at output	P _{-1dB}				
<i>I</i> _C = 40 mA, <i>V</i> _{CE} = 3V, <i>f</i> = 1.8 GHz					
$Z_{\rm S} = Z_{\rm L} = 50\Omega$		-	16	-	
$Z_{\rm S} = Z_{\rm Sopt,} \ Z_{\rm L} = Z_{\rm Lopt}$		-	19.5	-	

Electrical Characteristics at $T_A = 25^{\circ}$ C, unless otherwise specified

 ${}^{1}G_{\text{ma}} = |S_{21e} / S_{12e}| \ (k - (k^{2} - 1)^{1/2})$

 2 IP3 value depends on termination of all intermodulation frequency components. Termination used for this measurement is 50 Ω from 0.1 MHz to 6 GHz



SPICE Parameter

For the SPICE model as well as for the S-parameters (including noise parameters) please refer to our internet website <u>www.infineon.com/rf.models</u>.

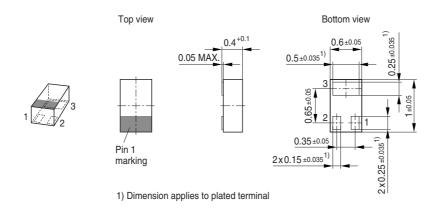
Please consult our website and download the latest versions before actually starting your design.

You find the BFR380L3 SPICE model in the internet in MWO- and ADS- format which you can import into these circuit simulation tools very quickly and conveniently. The simulation data have been generated and verified using typical devices.

The BFR380L3 SPICE model reflects the typical DC- and RF-performance with high accuracy.

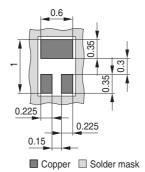


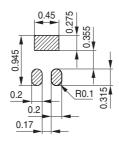




Foot Print

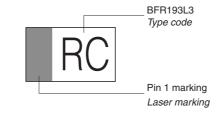
For board assembly information please refer to Infineon website "Packages"





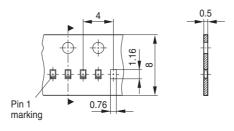
Stencil apertures

Marking Layout (Example)



Standard Packing

Reel ø180 mm = 15.000 Pieces/Reel





Datasheet Revision History: 27 May 2010

This datasheet replaces the revisions from 10 July 2008 and 30 March 2007.

The product itself has not been changed and the device characteristics remain unchanged. Only the product description and information available in the datasheet has been expanded and updated.

Previou	Previous Revisions: 10 July 2008 and 30 March 2007				
Page	Subject (changes since last revision)				
1	Datasheet has final status				
2	Typical values for leakage currents included, values for maximum leakage currents reduced				



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