# MOC3040, MOC3041, MOC3042, MOC3043 MOC3040X, MOC3041X, MOC3042X, MOC3043X



# OPTICALLY COUPLED BILATERAL SWITCH LIGHTACTIVATED ZERO VOLTAGE CROSSING TRIAC



### APPROVALS

• UL recognised, File No. E91231 Package Code " TT "

## 'X'SPECIFICATIONAPPROVALS

- VDE 0884 in 3 available lead form : -
  - STD
  - G form
  - SMD approved to CECC 00802

### DESCRIPTION

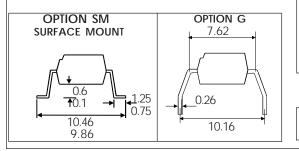
The MOC304\_Series are optically coupled isolators consisting of a Gallium Arsenide infrared emitting diode coupled with a monolithic silicon detector performing the functions of a zero crossing bilateral triac mounted in a standard 6 pin dual-in-line package.

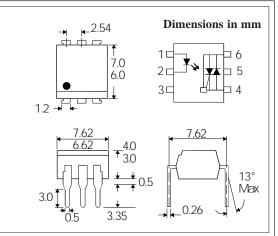
### FEATURES

- Options :-10mm lead spread - add G after part no. Surface mount - add SM after part no. Tape&reel - add SMT&R after part no.
  - High Isolation Voltage  $(5.3 kV_{RMS}, 7.5 kV_{PK})$
- Zero Voltage Crossing
- 400V Peak Blocking Voltage
- All electrical parameters 100% tested
- Custom electrical selections available

### APPLICATIONS

- CRTs
- Power Triac Driver
- Motors
- Consumer appliances
- Printers





# ABSOLUTE MAXIMUM RATINGS (25 °C unless otherwise noted)

Storage Temperature55°C-+150°C
Operating Temperature $-40^{\circ}C - +100^{\circ}C$
Lead Soldering Temperature 260°C
(1.6mm from case for 10 seconds)

### INPUTDIODE

Forward Current	50mA
Reverse Voltage	6V
Power Dissipation	120mW
(derate linearly 1.41mW/°C above 25°	C)

### **OUTPUT PHOTO TRIAC**

Off-State Output Terminal Voltage	400V
Peak Repetitive Surge Current	
(PW=100µs, 120pps)	1A
Power Dissipation	150mW
(derate linearly $1.76 \text{mW}/^{\circ}\text{C}$ above $25^{\circ}\text{C}$ )	

### **POWER DISSIPATION**

Total Power Dissipation \_\_\_\_\_ 250mW (derate linearly  $2.94mW/^{0}C$  above  $25^{0}C$ )

## ISOCOM COMPONENTS 2004 LTD

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	PARAMETER	MIN	ТҮР	MAX	UNITS	TEST CONDITION
Input	Forward Voltage $(V_F)$ Reverse Current $(I_R)$		1.2	1.4 10	V μA	I <sub>F</sub> =20mA V <sub>R</sub> =6V
Output	Peak Off-state Current ( $I_{DRM}$ ) Peak Blocking Voltage ( $V_{DRM}$ ) On-state Voltage ( $V_{TM}$ )	400		500 3.0	nA V V	$V_{DRM} = 400V \text{ (note 1)}$ $I_{DRM} = 500nA$ $I_{TM} = 100mA \text{ (peak)}$
	Critical rate of rise of off-state Voltage ( dv/dt )	600	1500		V/µs	
Coupled	Input Current to Trigger (I <sub>FT</sub> )(note 2) MOC3040 MOC3041 MOC3042 MOC3043			30 15 10 5	mA mA mA mA	$V_{TM} = 3V (note 2)$
	Holding Current , either direction ( $I_H$ ) Input to Output Isolation Voltage $V_{ISO}$	5300 7500	400		μΑ V <sub>RMS</sub> V <sub>PK</sub>	See note 3 See note 3
Zero Crossing Charact- -eristic	Inhibit Voltage ( $V_{IH}$ ) Leakage in Inhibited State ( $I_S$ )			20 500	V μA	$I_{F}$ = Rated $I_{FT}$ MT1-MT2 Voltage above which device will not trigger $I_{F}$ = Rated $I_{FT}$ $V_{DRM}$ = Rated $V_{DRM}$
						$v_{\text{DRM}} = \text{Kateu} v_{\text{DRM}}$ Off-state

# ELECTRICAL CHARACTERISTICS ( $T_A = 25^{\circ}C$ Unless otherwise noted)

Note 1. Test voltage must be applied within dv/dt rating. Note 2. Guaranteed to trigger at an I<sub>F</sub> value less than or equal to max. I<sub>FT</sub>, recommended I<sub>F</sub> lies between Rated I<sub>FT</sub> and absolute max. I<sub>F</sub>. Note 3. Measured with input leads shorted together and output leads shorted together.