Multilayer ceramic capacitors are available in a variety of physical sizes and configurations, including leaded devices and surface mounted chips. Leaded styles include molded and conformally coated parts with axial and radial leads. However, the basic capacitor element is similar for all styles. It is called a chip and consists of formulated dielectric materials which have been cast into thin layers, interspersed with metal electrodes alternately exposed on opposite edges of the laminated structure. The entire structure is fired at high temperature to produce a monolithic block which provides high capacitance values in a small physical volume. After firing, conductive terminations are applied to opposite ends of the chip to make contact with the exposed electrodes. Termination materials and methods vary depending on the intended use.

TEMPERATURE CHARACTERISTICS

Ceramic dielectric materials can be formulated with a wide range of characteristics. The EIA standard for ceramic dielectric capacitors (RS-198) divides ceramic dielectrics into the following classes:

Class I: Temperature compensating capacitors, suitable for resonant circuit application or other applications where high Q and stability of capacitance characteristics are required. Class I capacitors have predictable temperature coefficients and are not affected by voltage, frequency or time. They are made from materials which are not ferro-electric, yielding superior stability but low volumetric efficiency. Class I capacitors are the most stable type available, but have the lowest volumetric efficiency.

Class II: Stable capacitors, suitable for bypass or coupling applications or frequency discriminating circuits where Q and stability of capacitance characteristics are not of major importance. Class II capacitors have temperature characteristics of ± 15% or less. They are made from materials which are ferro-electric, yielding higher volumetric efficiency but less stability. Class II capacitors are affected by temperature, voltage, frequency and time.

Class III: General purpose capacitors, suitable for by-pass coupling or other applications in which dielectric losses, high insulation resistance and stability of capacitance characteristics are of little or no importance. Class III capacitors are similar to Class II capacitors except for temperature characteristics, which are greater than ± 15%. Class III capacitors have the highest volumetric efficiency and poorest stability of any type.

KEMET leaded ceramic capacitors are offered in the three most popular temperature characteristics:
C0G: Class I, with a temperature coefficient of 0 ± 30 ppm per degree C over an operating temperature range of - 55°C to + 125°C (Also known as "NP0").
X7R: Class II, with a maximum capacitance change of ± 15% over an operating temperature range of - 55°C to + 125°C.
Z5U: Class III, with a maximum capacitance change of + 22% - 56% over an operating temperature range of + 10°C to + 85°C.

Specified electrical limits for these three temperature characteristics are shown in Table 1.

SPECIFIED ELECTRICAL LIMITS

Parameter	Temperature Characteristics		
	C0G	X7R	Z5U

Dissipation Factor: Measured at following conditions.
  C0G – 1 kHz and 1 vrms if capacitance >1000pF
      1 MHz and 1 vrms if capacitance ≤ 1000 pF
  X7R – 1 kHz and 1 vrms* or if extended cap range 0.5 vrms
  Z5U – 1 kHz and 0.5 vrms
	0.10%	2.5%
		(3.5% @ 25V)
			4.0%

Dielectric Stength: 2.5 times rated DC voltage.	Pass Subsequent IR Test		
Insulation Resistance (IR): At rated DC voltage, whichever of the two is smaller	1,000 MΩ–µF or 100 GΩ	1,000 MΩ–µF or 100 GΩ	1,000 MΩ–µF or 10 GΩ

Temperature Characteristics: Range, °C
              Capacitance Change without
              DC voltage
	-55 to +125
	0 ± 30 ppm/°C
		-55 to +125
		± 15%
			+ 10 to +85
			+22%,-56%

* MHz and 1 vrms if capacitance ≤ 100 pF on military product.

Table I

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Application Notes

ELECTRICAL CHARACTERISTICS

The fundamental electrical properties of multilayer ceramic capacitors are as follows:

Polarity: Multilayer ceramic capacitors are not polar, and may be used with DC voltage applied in either direction.

Rated Voltage: This term refers to the maximum continuous DC working voltage permissible across the entire operating temperature range. Multilayer ceramic capacitors are not extremely sensitive to voltage, and brief applications of voltage above rated will not result in immediate failure. However, reliability will be reduced by exposure to sustained voltages above rated.

Capacitance: The standard unit of capacitance is the farad. For practical capacitors, it is usually expressed in microfarads (10^{-6} farad), nanofarads (10^{-9} farad), or picofarads (10^{-12} farad). Standard measurement conditions are as follows:

Class I (up to 1,000 pF):	1MHz and 1.2 VRMS maximum.
Class I (over 1,000 pF):	1kHz and 1.2 VRMS maximum.
Class II:	1 kHz and 1.0 ± 0.2 VRMS.
Class III:	1 kHz and 0.5 ± 0.1 VRMS.

Like all other practical capacitors, multilayer ceramic capacitors also have resistance and inductance. A simplified schematic for the equivalent circuit is shown in Figure 1. Other significant electrical characteristics resulting from these additional properties are as follows:

Figure 1
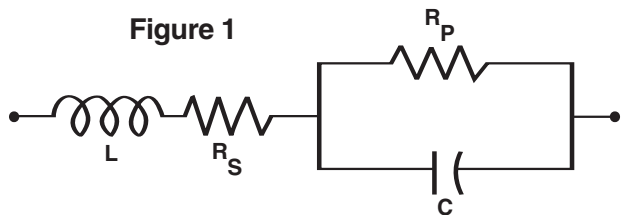
C = Capacitance R_S = Equivalent Series Resistance (ESR)

L = Inductance R_P = Insulation Resistance (IR)

Impedance: Since the parallel resistance (Rp) is normally very high, the total impedance of the capacitor is:

$$Z = \sqrt{R_S^2 + (X_C - X_L)^2}$$

Where Z = Total Impedance

RS = Equivalent Series Resistance

X_C = Capacitive Reactance $= \dfrac{1}{2\pi fC}$

X_L = Inductive Reactance $= 2\pi fL$

The variation of a capacitor's impedance with frequency determines its effectiveness in many applications.

Dissipation Factor: Dissipation Factor (DF) is a measure of the losses in a capacitor under AC application. It is the ratio of the equivalent series resistance to the capacitive reactance, and is usually expressed in percent. It is usually measured simultaneously with capacitance, and under the same conditions. The vector diagram in Figure 2 illustrates the relationship between DF, ESR, and impedance. The reciprocal of the dissipation factor is called the "Q", or quality factor. For convenience, the "Q" factor is often used for very low values of dissipation factor. DF is sometimes called the "loss tangent" or "tangent δ", as derived from this diagram.
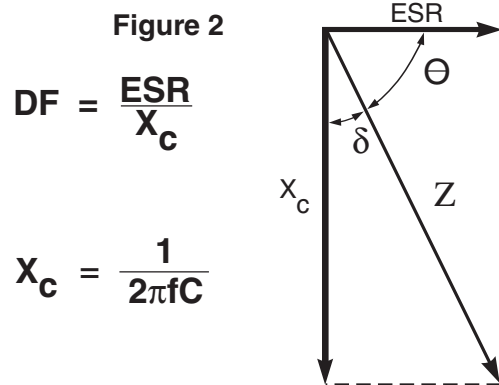
Figure 2

$$DF = \frac{ESR}{X_C}$$

$$X_C = \frac{1}{2\pi fC}$$

Insulation Resistance: Insulation Resistance (IR) is the DC resistance measured across the terminals of a capacitor, represented by the parallel resistance (Rp) shown in Figure 1. For a given dielectric type, electrode area increases with capacitance, resulting in a decrease in the insulation resistance. Consequently, insulation resistance is usually specified as the "RC" (IR x C) product, in terms of ohm-farads or megohm-microfarads. The insulation resistance for a specific capacitance value is determined by dividing this product by the capacitance. However, as the nominal capacitance values become small, the insulation resistance calculated from the RC product reaches values which are impractical. Consequently, IR specifications usually include both a minimum RC product and a maximum limit on the IR calculated from that value. For example, a typical IR specification might read "1,000 megohm-microfarads or 100 gigohms, whichever is less."

Insulation Resistance is the measure of a capacitor to resist the flow of DC leakage current. It is sometimes referred to as "leakage resistance." The DC leakage current may be calculated by dividing the applied voltage by the insulation resistance (Ohm's Law).

Dielectric Withstanding Voltage: Dielectric withstanding voltage (DWV) is the peak voltage which a capacitor is designed to withstand for short periods of time without damage. All KEMET multilayer ceramic capacitors will withstand a test voltage of 2.5 x the rated voltage for 60 seconds.

KEMET specification limits for these characteristics at standard measurement conditions are shown in Table 1 on page 4. Variations in these properties caused by changing conditions of temperature, voltage, frequency, and time are covered in the following sections.

TABLE 1
EIA TEMPERATURE CHARACTERISTIC CODES
FOR CLASS I DIELECTRICS

Significant Figure of Temperature Coefficient		Multiplier Applied to Temperature Coefficient		Tolerance of Temperature Coefficient *	
PPM per Degree C	Letter Symbol	Multi-plier	Number Symbol	PPM per Degree C	Letter Symbol
0.0	C	-1	0	±30	G
0.3	B	-10	1	±60	H
0.9	A	-100	2	±120	J
1.0	M	-1000	3	±250	K
1.5	P	-100000	4	±500	L
2.2	R	+1	5	±1000	M
3.3	S	+10	6	±2500	N
4.7	T	+100	7		
7.5	U	+1000	8		
		+10000	9		

* These symetrical tolerances apply to a two-point measurement of temperature coefficient: one at 25°C and one at 85°C. Some deviation is permitted at lower temperatures. For example, the PPM tolerance for C0G at -55°C is +30 / -72 PPM.

TABLE 2
EIA TEMPERATURE CHARACTERISTIC CODES
FOR CLASS II & III DIELECTRICS

Low Temperature Rating		High Temperature Rating		Maximum Capacitance Shift	
Degree Celcius	Letter Symbol	Degree Celcius	Number Symbol	Percent	Letter Symbol
+10C	Z	+45C	2	±1.0%	A
-30C	Y	+65C	4	±1.5%	B
-55C	X	+85C	5	±2.2%	C
		+105C	6	±3.3%	D
		+125C	7	±4.7%	E
		+150C	8	±7.5%	F
		+200C	9	±10.0%	P
				±15.0%	R
				±22.0%	S
				+22/-33%	T
				+22/-56%	U
				+22/-82%	V

EFFECT OF TEMPERATURE

Figure 3. Temperature °C
Capacitance & DF vs Temperature - C0G

Figure 4. Temperature °C
Capacitance & DF vs Temperature - X7R

Figure 5. Temperature °C
Capacitance & DF vs Temperature - Z5U

EFFECT OF APPLIED VOLTAGE

Figure 6. **AC or DC Volts Applied**
Typical Effects of 1000 Hz AC and DC Voltage Level on Capacitance and Dissipation Factor - X7R
Note: C0G Dielectric capacitance and dissipation factor are stable with voltage.

Figure 7. **AC or DC Volts Applied**
Typical Effects of 1000 Hz AC and DC Voltage Level on Capacitance and Dissipation Factor - Z5U
Note: C0G Dielectric capacitance and dissipation factor are stable with voltage.

Effect of Temperature: Both capacitance and dissipation factor are affected by variations in temperature. The maximum capacitance change with temperature is defined by the temperature characteristic. However, this only defines a "box" bounded by the upper and lower operating temperatures and the minimum and maximum capacitance values. Within this "box", the variation with temperature depends upon the specific dielectric formulation. Typical curves for KEMET capacitors are shown in Figures 3, 4, and 5. These figures also include the typical change in dissipation factor for KEMET capacitors.

Insulation resistance decreases with temperature. Typically, the insulation resistance at maximum rated temperature is 10% of the 25°C value.

Effect of Voltage: Class I ceramic capacitors are not affected by variations in applied AC or DC voltages. For Class II and III ceramic capacitors, variations in voltage affect only the capacitance and dissipation factor. The application of DC voltage higher than 5 vdc reduces both the capacitance and dissipation factor. The application of AC voltages up to 10-20 Vac tends to increase both capacitance and dissipation factor.

At higher AC voltages, both capacitance and dissipation factor begin to decrease.

Typical curves showing the effect of applied AC and DC voltage are shown in Figure 6 for KEMET X7R capacitors and Figure 7 for KEMET Z5U capacitors.

Effect of Frequency: Frequency affects both capacitance and dissipation factor. Typical curves for KEMET multilayer ceramic capacitors are shown in Figures 8 and 9.

The variation of impedance with frequency is an important consideration in the application of multilayer ceramic capacitors. Total impedance of the capacitor is the vector of the capacitive reactance, the inductive reactance, and the ESR, as illustrated in Figure 2. As frequency increases, the capacitive reactance decreases. However, the series inductance (L) shown in Figure 1 produces inductive reactance, which increases with frequency. At some frequency, the impedance ceases to be capacitive and becomes inductive. This point, at the bottom of the V-shaped impedance versus frequency curves, is the self-resonant frequency. At the self-resonant frequency, the reactance is zero, and the impedance consists of the ESR only.

Typical impedance versus frequency curves for KEMET multilayer ceramic capacitors are shown in Figures 10, 11, and 12. These curves apply to KEMET capacitors in chip form, without leads. Lead configuration and lead length have a significant impact on the series inductance. The lead inductance is approximately 10nH/inch, which is large compared to the inductance of the chip. The effect of this additional inductance is a decrease in the self-resonant frequency, and an increase in impedance in the inductive region above the self-resonant frequency.

Effect of Time: The capacitance of Class II and III dielectrics change with time as well as with temperature, voltage and frequency. This change with time is known as "aging." It is caused by gradual realignment of the crystalline structure of the ceramic dielectric material as it is cooled below its Curie temperature, which produces a loss of capacitance with time. The aging process is predictable and follows a logarithmic decay. Typical aging rates for C0G, X7R, and Z5U dielectrics are as follows:

C0G	None
X7R	2.0% per decade of time
Z5U	5.0% per decade of time

Typical aging curves for X7R and Z5U dielectrics are shown in Figure 13.

The aging process is reversible. If the capacitor is heated to a temperature above its Curie point for some period of time, de-aging will occur and the capacitor will regain the capacitance lost during the aging process. The amount of de-aging depends on both the elevated temperature and the length of time at that temperature. Exposure to 150°C for one-half hour or 125°C for two hours is usually sufficient to return the capacitor to its initial value.

Because the capacitance changes rapidly immediately after de-aging, capacitance measurements are usually delayed for at least 10 hours after the de-aging process, which is often referred to as the "last heat." In addition, manufacturers utilize the aging rates to set factory test limits which will bring the capacitance within the specified tolerance at some future time, to allow for customer receipt and use. Typically, the test limits are adjusted so that the capacitance will be within the specified tolerance after either 1,000 hours or 100 days, depending on the manufacturer and the product type.

POWER DISSIPATION

Power dissipation has been empirically determined for two representative KEMET series: C052 and C062. Power dissipation capability for various mounting configurations is shown in Table 3. This table was extracted from Engineering Bulletin F-2013, which provides a more detailed treatment of this subject.

Note that no significant difference was detected between the two sizes in spite of a 2 to 1 surface area ratio. Due to the materials used in the construction of multilayer ceramic capacitors, the power dissipation capability does not depend greatly on the surface area of the capacitor body, but rather on how well heat is conducted out of the capacitor lead wires. Consequently, this power dissipation capability is applicable to other leaded multilayer styles and sizes.

TABLE 3
POWER DISSIPATION CAPABILITY
(Rise in Celsius degrees per Watt)

Mounting Configuration	Power Dissipation of C052 & C062
1.00" leadwires attached to binding post of GR-1615 bridge (excellent heat sink)	90 Celsius degrees rise per Watt ±10%
0.25" leadwires attached to binding post of GR-1615 bridge	55 Celsius degrees rise per Watt ±10%
Capacitor mounted flush to 0.062" glass-epoxy circuit board with small copper traces	77 Celsius degrees rise per Watt ±10%
Capacitor mounted flush to 0.062" glass-epoxy circuit board with four square inches of copper land area as a heat sink	53 Celsius degrees rise per Watt ±10%

As shown in Table 3, the power dissipation capability of the capacitor is very sensitive to the details of its use environment. The temperature rise due to power dissipation should not exceed 20°C. Using that constraint, the maximum permissible power dissipation may be calculated from the data provided in Table 3.

It is often convenient to translate power dissipation capability into a permissible AC voltage rating. Assuming a sinusoidal wave form, the RMS "ripple voltage" may be calculated from the following formula:

$$E = Z \, x \sqrt{\frac{P_{MAX}}{R}}$$

Where   **E = RMS Ripple Voltage (volts)**

            P = Power Dissipation (watts)

            Z = Impedance

            R = ESR

The data necessary to make this calculation is included in Engineering Bulletin F-2013. However, the following criteria must be observed:

1. The temperature rise due to power dissipation should be limited to 20°C.
2. The peak AC voltage plus the DC voltage must not exceed the maximum working voltage of the capacitor.

Provided that these criteria are met, multilayer ceramic capacitors may be operated with AC voltage applied without need for DC bias.

RELIABILITY

A well constructed multilayer ceramic capacitor is extremely reliable and, for all practical purposes, has an infinite life span when used within the maximum voltage and temperature ratings. Capacitor failure may be induced by sustained operation at voltages that exceed the rated DC voltage, voltage spikes or transients that exceed the dielectric withstanding voltage, sustained operation at temperatures above the maximum rated temperature, or the excessive temperature rise due to power dissipation.

Failure rate is usually expressed in terms of percent per 1,000 hours or in FITS (failure per billion hours). Some KEMET series are qualified under U.S. military established reliability specifications MIL-PRF-20, MIL-PRF-123, MIL-PRF-39014, and MIL-PRF-55681. Failure rates as low as 0.001% per 1,000 hours are available for all capacitance / voltage ratings covered by these specifications. These specifications and accompanying Qualified Products List should be consulted for details.

For series not covered by these military specifications, an internal testing program is maintained by KEMET Quality Assurance. Samples from each week's production are subjected to a 2,000 hour accelerated life test at 2 x rated voltage and maximum rated temperature. Based on the results of these tests, the average failure rate for all non-military series covered by this test program is currently 0.06% per 1,000 hours at maximum rated conditions. The failure rate would be much lower at typical use conditions. For example, using MIL-HDBK-217D this failure rate translates to 0.9 FITS at 50% rated voltage and 50°C.

Current failure rate details for specific KEMET multilayer ceramic capacitor series are available on request.

MISAPPLICATION

Ceramic capacitors, like any other capacitors, may fail if they are misapplied. Typical misapplications include exposure to excessive voltage, current or temperature. If the dielectric layer of the capacitor is damaged by misapplication the electrical energy of the circuit can be released as heat, which may damage the circuit board and other components as well.

If potential for misapplication exists, it is recommended that precautions be taken to protect personnel and equipment during initial application of voltage. Commonly used precautions include shielding of personnel and sensing for excessive power drain during board testing.

STORAGE AND HANDLING

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature – reels may soften or warp, and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40° C, and maximum storage humidity not exceed 70% relative humidity. In addition, temperature fluctuations should be minimized to avoid condensation on the parts, and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability, chip stock should be used promptly, preferably within 1.5 years of receipt.

EFFECT OF FREQUENCY

Figure 8. Frequency - Hertz
Capacitance & DF vs Frequency - C0G

Figure 9. Frequency - Hertz
Capacitance & DF vs Frequency - X7R & Z5U

EFFECT OF TIME (hours)

Figure 13. Typical Aging Rates for X7R & Z5U

IMPEDANCE VS FREQUENCY

Figure 10. Impedance vs Frequency for C0G Dielectric

Figure 11. Impedance vs Frequency for X7R Dielectric

Figure 12. Impedance vs Frequency for Z5U Dielectric

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GENERAL SPECIFICATIONS

Working Voltage:
C0G – 50, 100, 200
X7R – 50, 100, 200

Temperature Characteristics:
C0G 0 ±30 PPM / °C from -55°C to +125°C
X7R ± 15% from -55°C to +125°C

Capacitance Tolerance:
C0G ±0.5pF, ±1%, ±2%, ±5%, ±10%, ±20%
     (±0.5pF is tightest tolerance available)
X7R ±10%, ±20%, -0 +100%, +80% / -20%

Construction:
Monolithic block of ceramic dielectric with
Interdigitated internal electrodes, encapsulated
in a molded case, and having axial or radial leads.
Meets flame test requirements of UL Standard 94V-0.

Lead Material:
Axial: Solder coated copper clad steel
Radial: Solder-coated copper standard (100% tin
plated optional)

Solderability:
MIL-STD-202, Method 208, Sn62 solder, 245°C for
5 ±1/2 seconds.

Terminal Strength:
  EIA-198 Method 303, Condition A (2.2 kg)

ELECTRICAL

Capacitance:
Within specified tolerance and when measured with
1 volt rms at 1kHz (1000 pF or less at 1 MHz for C0G).

Dissipation Factor @25°C:
25°C at 1kHz (1000 pF or less at 1 MHz for C0G).
C0G – 0.15% maximum
X7R – 2.5% maximum

Insulation Resistance:
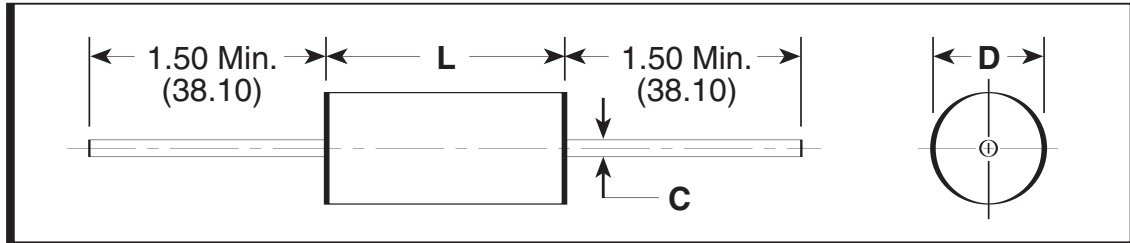After 2 minutes electrification at 25°C and rated voltage
C0G – 100K MΩ or 1000 MΩ – µF, whichever is less.
X7R – 100K MΩ or 1000 MΩ – µF, whichever is less.

Dielectric Withstanding Voltage:
250% of rated voltage for 5 seconds with current limited
to 50 mA at 25°C.

**Ceramic Molded
Axial/Radial - Standard**

KEMET
CHARGED.™

CAPACITOR OUTLINE DRAWINGS — (AXIAL LEADS)

DIMENSIONS — INCHES (MILLIMETERS)

Case Size	Military Equivalent Styles	L Length	D Body diameter	C Lead Diameter
C114	CC75, CCR75 CK12, CKR11	.160 ± .010 (4.06 ± .25)	.090 ± .010 (2.29 ± .25)	.020, +.000, -.003 (.51, +.00, -.08)
C124	CC76, CCR76 CK13, CKR12	.250 ± .010 (6.35 ± .25)	.090 ± .010 (2.29 ± .25)	.020, +.000, -.003 (.51, +.00, -.08)
C192	CC77, CCR77 CK14, CKR14	.390 ± .010 (9.91 ± .25)	.140 ± .010 (3.56 ± .25)	.025, +.004, -.001 (.64, +.10, -.025)
C202	CC78, CCR78 CK15, CKR15	.500 ± .020 (12.70 ± .51)	.250 ± .015 (6.35 ± .38)	.025, +.004, -.001 (.64, +.10, -.025)
C222	CC79, CCR79 CK16, CKR16	.690 ± .030 (17.53 ± .76)	.350 ± .020 (8.89 ± .51)	.025, +.004, -.001 (.64, +.10, -.025)

CAPACITOR OUTLINE DRAWINGS — (RADIAL LEADS)
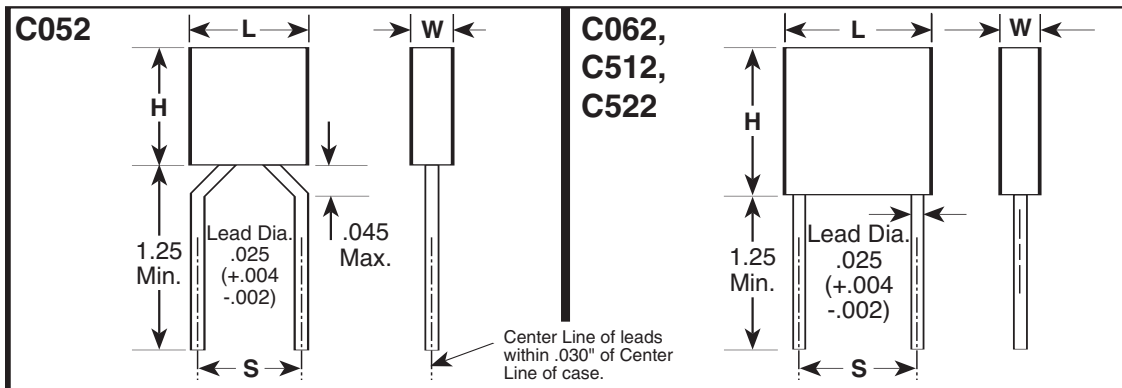
DIMENSIONS — INCHES (MILLIMETERS)

Case Size	Military Equivalent Styles	H Height	L Length	W Width	S Lead Spacing
C052	CC05, CCR05 CK05, CKR05	.190 ± .010 (4.83 ± .25)	.190 ± .010 (4.83 ± .25)	.090 ± .010 (2.29 ± .25)	.200 ± .015 (5.08 ± .38)
C062	CC06, CCR06 CK06, CKR06	.290 ± .010 (7.37 ± .25)	.290 ± .010 (7.37 ± .25)	.090 ± .010 (2.29 ± .25)	.200 ± .015 (5.08 ± .38)
C512	CC07, CCR07	.480 ± .020 (12.19 ± .51)	.480 ± .020 (12.19 ± .51)	.140 ± .010 (3.56 ± .25)	.400 ± .020 (10.16 ± .51)
C522	CC08, CCR08	.480 ± .020 (12.19 ± .51)	.480 ± .020 (12.19 ± .51)	.240 ± .010 (6.10 ± .25)	.400 ± .020 (10.16 ± .51)

For packaging information, see pages 46, 47 and 48.

CERAMIC MOLDED/AXIAL & RADIAL - STANDARD

ORDERING INFORMATION

C 052 C 102 K 2 R 5 T A

CERAMIC

CASE SIZE
See Table Below

SPECIFICATION
C – Standard

CAPACITANCE PICOFARAD CODE
Expressed in picofarads (pF). First two digits represent significant figures. Third digit specifies number of zeros following except 9 indicates division by 10). Examples: 0.1 µF = 100,000 pF = 104 and 9.1 pF = 919. See tables for standard values.

CAPACITANCE TOLERANCE

Standard	Others
M – ±20%	H – ±3%
K – ±10%	G – ±2%
J – ±5%	F – ±1%
	D – ±.5pF

Standard tolerances for each Series are shown in the repetitive parts lists.

Case Sizes	
Radial	**Axial**
C052	C114
C062	C124
C512	C192
C522	C202
	C222

FAILURE RATE
A – Not Applicable

LEAD MATERIAL
C – 60/40 Tin/Lead (SnPb)
T – 100% Tin (Sn)(C052, C062 only)

INTERNAL CONSTRUCTION
5 – Multilayer

TEMPERATURE CHARACTERISTIC

KEMET Designator	EIA Equivalent	Cap. Change with Temp.	
		Temp Range, °C	Measured without DC Bias Voltage
G (Ultra Stable)	C0G (NP0)	-55 to +125°	±30 ppm/°C
R (Stable)	X7R	-55° to +125	±15%

WORKING VOLTAGE (DC)
2 – 200V; 1 – 100V; 5 – 50V

Part Number Example: C052C102K2R5TA (14 digits – no spaces)

AXIAL CAPACITOR MARKING
STANDARD C114C, C124C, C192C, C202C & C222C

KC0G	—— KEMET, Temperature Characteristic
101J	—— Capacitance, Capacitance Tolerance
200V	—— Voltage
0812	—— Date Code

RADIAL CAPACITOR MARKING
C052C & C062C STANDARD MARKING

FRONT

Style ——	C062
Temperature Characteristic ——	X7R
Capacitance, Capacitance Tolerance ——	104K

BACK

100V	—— Voltage
K	—— KEMET
0811	—— Date Code

C512 & C522 STANDARD MARKING

KEMET	—— KEMET
C512X7R	—— SIZE and Temperature Characteristic
105K 50V	—— Capacitance, Capacitance Tolerance, Voltage
0832	—— Date Code

Ceramic Molded Axial/Radial - Standard

RATINGS & PART NUMBER REFERENCE

CAPACITANCE pF	KEMET PART NUMBER
200 VOLT – C114 STANDARD C0G	
1.0	C114C109(1)2G5CA
1.5	C114C159(1)2G5CA
2.2	C114C229(1)2G5CA
2.7	C114C279(1)2G5CA
3.3	C114C339(1)2G5CA
3.9	C114C399(1)2G5CA
4.7	C114C479(1)2G5CA
5.6	C114C569(1)2G5CA
6.8	C114C689(1)2G5CA
8.2	C114C829(1)2G5CA
10.0	C114C100(2)2G5CA
12.0	C114C120(2)2G5CA
15.0	C114C150(2)2G5CA
18.0	C114C180(2)2G5CA
22.0	C114C220(2)2G5CA
27.0	C114C270(3)2G5CA
33.0	C114C330(3)2G5CA
39.0	C114C390(3)2G5CA
47.0	C114C470(3)2G5CA
56.0	C114C560(4)2G5CA
68.0	C114C680(4)2G5CA
82.0	C114C820(4)2G5CA
100.0	C114C101(4)2G5CA
120.0	C114C121(4)2G5CA
150.0	C114C151(4)2G5CA
180.0	C114C181(4)2G5CA
220.0	C114C221(4)2G5CA
270.0	C114C271(4)2G5CA
330.0	C114C331(4)2G5CA
100 VOLT – C114 STANDARD C0G	
82.0	C114C820(4)1G5CA
100.0	C114C101(4)1G5CA
120.0	C114C121(4)1G5CA
150.0	C114C151(4)1G5CA
180.0	C114C181(4)1G5CA
220.0	C114C221(4)1G5CA
270.0	C114C271(4)1G5CA
330.0	C114C331(4)1G5CA
390.0	C114C391(4)1G5CA
470.0	C114C471(4)1G5CA
560.0	C114C561(4)1G5CA
680.0	C114C681(4)1G5CA

NOTE 1: Insert proper symbol for capacitance tolerance as follows:

(1) 1.0 pF to 8.2 pF: D— ±.5 pF
(2) 10.0 pF to 22 pF: J— ±5%, K— ±10%
(3) 27.0 pF to 47 pF: G— ±2%, J— ±5%, K— ±10%
(4) 56.0 pF and up: F— ±1%,G— ±2%, J— ±5%, K— ±10%

CAPACITANCE pF	KEMET PART NUMBER
200 VOLT – C124 STANDARD C0G	
390.0	C124C391(4)2G5CA
470.0	C124C471(4)2G5CA
560.0	C124C561(4)2G5CA
100 VOLT – C124 STANDARD C0G	
820.0	C124C821(4)1G5CA
1,000.0	C124C102(4)1G5CA
200 VOLT – C192 STANDARD C0G	
680.0	C192C681(4)2G5CA
820.0	C192C821(4)2G5CA
1,000.0	C192C102(4)2G5CA
1,200.0	C192C122(4)2G5CA
1,500.0	C192C152(4)2G5CA
1,800.0	C192C182(4)2G5CA
2,200.0	C192C222(4)2G5CA
2,700.0	C192C272(4)2G5CA
3,300.0	C114C332(4)2G5CA
3,900.0	C114C392(4)2G5CA
4,700.0	C114C472(4)2G5CA
100 VOLT – C192 STANDARD C0G	
1,200.0	C114C122(4)1G5CA
1,500.0	C114C152(4)1G5CA
1,800.0	C114C182(4)1G5CA
2,200.0	C114C222(4)1G5CA
2,700.0	C114C272(4)1G5CA
3,300.0	C114C332(4)1G5CA
3,900.0	C114C392(4)1G5CA
4,700.0	C114C472(4)1G5CA
5,600.0	C114C562(4)1G5CA
6,800.0	C114C682(4)1G5CA
8,200.0	C114C822(4)1G5CA

NOTE 1: Insert proper symbol for capacitance tolerance as follows:

(1) 1.0 pF to 8.2 pF: D— ±.5 pF
(2) 10.0 pF to 22 pF: J— ±5%, K— ±10%
(3) 27.0 pF to 47 pF: G— ±2%, J— ±5%, K— ±10%
(4) 56.0 pF and up: F— ±1%,G— ±2%, J— ±5%, K— ±10%

CAPACITANCE pF	KEMET PART NUMBER
200 VOLT – C202 STANDARD C0G	
5,600.0	C202C562(4)2G5CA
6,800.0	C202C682(4)2G5CA
8,200.0	C202C822(4)2G5CA
10,000.0	C202C103(4)2G5CA
12,000.0	C202C123(4)2G5CA
15,000.0	C202C153(4)2G5CA
18,000.0	C202C183(4)2G5CA
22000.0	C202C223(4)2G5CA
100 VOLT – C202 STANDARD C0G	
10,000.0	C202C103(4)1G5CA
12,000.0	C202C123(4)1G5CA
15,000.0	C202C153(4)1G5CA
18,000.0	C202C183(4)1G5CA
22,000.0	C202C223(4)1G5CA
27,000.0	C202C273(4)1G5CA
33,000.0	C202C333(4)1G5CA
200 VOLT – C222 STANDARD C0G	
27,000.0	C222C273(4)2G5CA
33,000.0	C222C333(4)2G5CA
39,000.0	C222C393(4)2G5CA
47,000.0	C222C473(4)2G5CA
100 VOLT – C222 STANDARD C0G	
39,000.0	C222C393(4)1G5CA
47,000.0	C222C473(4)1G5CA
56,000.0	C222C563(4)1G5CA
68,000.0	C222C683(4)1G5CA
82,000.0	C222C823(4)1G5CA
100,000.0	C222C104(4)1G5CA

NOTE 1: Insert proper symbol for capacitance tolerance as follows:

(1) 1.0 pF to 8.2 pF: D— ±.5 pF
(2) 10.0 pF to 22 pF: J— ±5%, K— ±10%
(3) 27.0 pF to 47 pF: G— ±2%, J— ±5%, K— ±10%
(4) 56.0 pF and up: F— ±1%,G— ±2%, J— ±5%, K— ±10%

RATINGS & PART NUMBER REFERENCE

CAPACITANCE pF	KEMET PART NUMBER
200 VOLT – C052 SIZE C0G	
1.0	C052C109(1)2G5CA
1.5	C052C159(1)2G5CA
2.2	C052C229(1)2G5CA
2.7	C052C279(1)2G5CA
3.3	C052C339(1)2G5CA
3.9	C052C399(1)2G5CA
4.7	C052C479(1)2G5CA
5.6	C052C569(1)2G5CA
6.8	C052C689(1)2G5CA
8.2	C052C829(1)2G5CA
10.0	C052C100(2)2G5CA
12.0	C052C120(2)2G5CA
15.0	C052C150(2)2G5CA
18.0	C052C180(2)2G5CA
22.0	C052C220(2)2G5CA
27.0	C052C270(3)2G5CA
33.0	C052C330(3)2G5CA
39.0	C052C390(3)2G5CA
47.0	C052C470(3)2G5CA
56.0	C052C560(4)2G5CA
68.0	C052C680(4)2G5CA
82.0	C052C820(4)2G5CA
100.0	C052C101(4)2G5CA
120.0	C052C121(4)2G5CA
150.0	C052C151(4)2G5CA
180.0	C052C181(4)2G5CA
220.0	C052C221(4)2G5CA
270.0	C052C271(4)2G5CA
330.0	C052C331(4)2G5CA
390.0	C052C391(4)2G5CA
470.0	C052C471(4)2G5CA
560.0	C052C561(4)2G5CA
680.0	C052C681(4)2G5CA
820.0	C052C821(4)2G5CA
1,000.0	C052C102(4)2G5CA
1,200.0	C052C122(4)2G5CA
1,500.0	C052C152(4)2G5CA
1,800.0	C052C182(4)2G5CA
2,200.0	C052C222(4)2G5CA
2,700.0	C052C272(4)2G5CA
100 VOLT – C052 SIZE C0G	
390.0	C052C391(4)1G5CA
470.0	C052C471(4)1G5CA
560.0	C052C561(4)1G5CA
680.0	C052C681(4)1G5CA
820.0	C052C821(4)1G5CA
1,000.0	C052C102(4)1G5CA
1,200.0	C052C122(4)1G5CA
1,500.0	C052C152(4)1G5CA
1,800.0	C052C182(4)1G5CA
2,200.0	C052C222(4)1G5CA
2,700.0	C052C272(4)1G5CA
3,300.0	C052C332(4)1G5CA
3,900.0	C052C392(4)1G5CA
4,700.0	C052C472(4)1G5CA

NOTE 1: Insert proper symbol for capacitance tolerance as follows:

(1) 1.0 pF to 8.2 pF: D— ±.5 pF
(2) 10.0 pF to 22 pF: J— ±5%, K— ±10%
(3) 27.0 pF to 47 pF: G— ±2%, J— ±5%, K— ±10%
(4) 56.0 pF and up: F— ±1%, G— ±2%, J— ±5%, K— ±10%

CAPACITANCE pF	KEMET PART NUMBER
200 VOLT – C062 SIZE C0G	
3,300.0	C062C332(4)2G5CA
3,900.0	C062C392(4)2G5CA
4,700.0	C062C472(4)2G5CA
5,600.0	C062C562(4)2G5CA
6,800.0	C062C682(4)2G5CA
8,200.0	C062C822(4)2G5CA
10,000.0	C062C103(4)2G5CA
100 VOLT – C062 SIZE C0G	
5,600.0	C062C562(4)1G5CA
6,800.0	C062C682(4)1G5CA
8,200.0	C062C822(4)1G5CA
10,000.0	C062C103(4)1G5CA
12,000.0	C062C123(4)1G5CA
15,000.0	C062C153(4)1G5CA
18,000.0	C062C183(4)1G5CA
22,000.0	C062C223(4)1G5CA
200 VOLT – C512 SIZE C0G	
12,000.0	C512C123(4)2G5CA
15,000.0	C512C153(4)2G5CA
18,000.0	C512C183(4)2G5CA
22,000.0	C512C223(4)2G5CA
27,000.0	C512C273(4)2G5CA
33,000.0	C512C333(4)2G5CA
39,000.0	C512C393(4)2G5CA
47,000.0	C512C473(4)2G5CA
56,000.0	C512C563(4)2G5CA
68,000.0	C512C683(4)2G5CA
100 VOLT – C512 SIZE C0G	
27,000.0	C512C273(4)1G5CA
33,000.0	C512C333(4)1G5CA
39,000.0	C512C393(4)1G5CA
47,000.0	C512C473(4)1G5CA
56,000.0	C512C563(4)1G5CA
68,000.0	C512C683(4)1G5CA
82,000.0	C512C823(4)1G5CA
100,000.0	C512C104(4)1G5CA
200 VOLT – C522 SIZE C0G	
82,000.0	C522C823(4)2G5CA
100,000.0	C522C104(4)2G5CA
100 VOLT – C522 SIZE C0G	
120,000.0	C522C124(4)1G5CA
150,000.0	C522C154(4)1G5CA
180,000.0	C522C184(4)1G5CA

NOTE 1: Insert proper symbol for capacitance tolerance as follows:

(1) 1.0 pF to 8.2 pF: D— ±.5 pF
(2) 10.0 pF to 22 pF: J— ±5%, K— ±10%
(3) 27.0 pF to 47 pF: G— ±2%, J— ±5%, K— ±10%
(4) 56.0 pF and up: F— ±1%, G— ±2%, J— ±5%, K— ±10%

CERAMIC PACKAGING

KEMET Series	Military Style	Military Specification	Standard (1) Bulk Quantity	Ammo Pack Quantity Maximum	Maximum Reel Quantity	Reel Size
C114C-K-G	CK12, CC75	MIL-C-11015/	200/Box		5000	12"
C124C-K-G	CK13, CC76	MIL-PRF-20	200/Box		5000	12"
C192C-K-G	CK14, CC77		100/Box		3000	12"
C202C-K	CK15		25/Box		500	12"
C222C-K	CK16		10/Tray		300	12"
C052C-K-G	CK05, CC05		100/Bag	2000	2000	12"
C062C-K-G	CK06, CC06		100/Bag	1500	1500	12"
C114G	CCR75	MIL-PRF-20	200/Box		5000	12"
C124G	CCR76		200/Box		5000	12"
C192G	CCR77		100/Box		3000	12"
C202G	CC78-CCR78		25/Box		500	12"
C222G	CC79-CCR79		10/Tray		300	12"
C052/56G	CCR05		100/Bag		1700	12"
C062/66G	CCR06		100/Bag		1500	12"
C512G	CC07-CCR07		Footnote (2)		N/A	N/A
C522G	CC08-CCR08		Footnote (2)		N/A	N/A
C114T	CKR11	MIL-PRF-39014	200/Box		5000	12"
C124T	CKR12		200/Box		5000	12"
C192T	CKR14		100/Box		3000	12"
C202T	CKR15		25/Box		500	12"
C222T	CKR16		10/Tray		300	12"
C052/56T	CKR05		100/Bag		1700	12"
C062/66T	CKR06		100/Bag		1500	12"
C31X			500/Bag	2500	2500	12"
C32X			500/Bag	2500	2500	12"
C33X			250/Bag	1500	1500	12"
C340			100/Bag	1000	1000	12"
C350			50/Bag	N/A	500	12"
C410			300/Box	4000	5000	12"
C412			200/Box	4000	5000	12"
C420			300/Box	4000	5000	12"
C430			200/Box	2000	2500	12"
C440			200/Box	2000	2500	12"
C512	N/A	N/A	Footnote (2)		N/A	N/A
C522	N/A	N/A	Footnote (2)		N/A	N/A
C617			250/Bag		1000	12"
C622/C623			100/Bag		500	12"
C627/C628			100/Bag		500	12"
C630/C631			100/Bag		500	12"
C637/C638			50/Bag		500	12"
C640/C641			50/Bag		500	12"
C642/C643			50/Bag		500	12"
C647/C648			50/Bag		500	12"
C657/C658			50/Bag		500	12"
C667/C668			50/Bag		500	12"

NOTE: (1) Standard packaging refers to number of pieces per bag, tray or vial.
(2) Quantity varies. For further details, please consult the factory.

Tape & Reel Packaging Information

KEMET offers standard reeling of Molded and Conformally Coated Radial Leaded Capacitors in accordance with EIA standard 468. Parts are taped to a tagboard carrier strip, and wound on a reel as shown in Figure 1. Kraft paper interleaving is inserted between the layers of capacitors on the reel. Ammopack is also available, with the same lead tape configuration and package quantities.
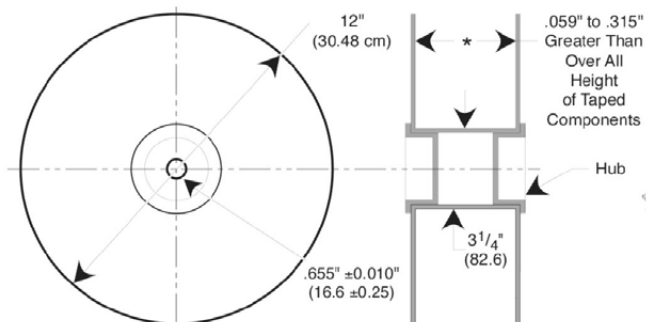
Figure 1

Carrier Strip

Adhesive Tape

Carrier Tape

Kraft Paper Interleaving

(Note: Non-standard lead lengths available in bulk only.)

12" (30.48 cm)

.059" to .315" Greater Than Over All Height of Taped Components

Hub

$3^{1}/_{4}$" (82.6)

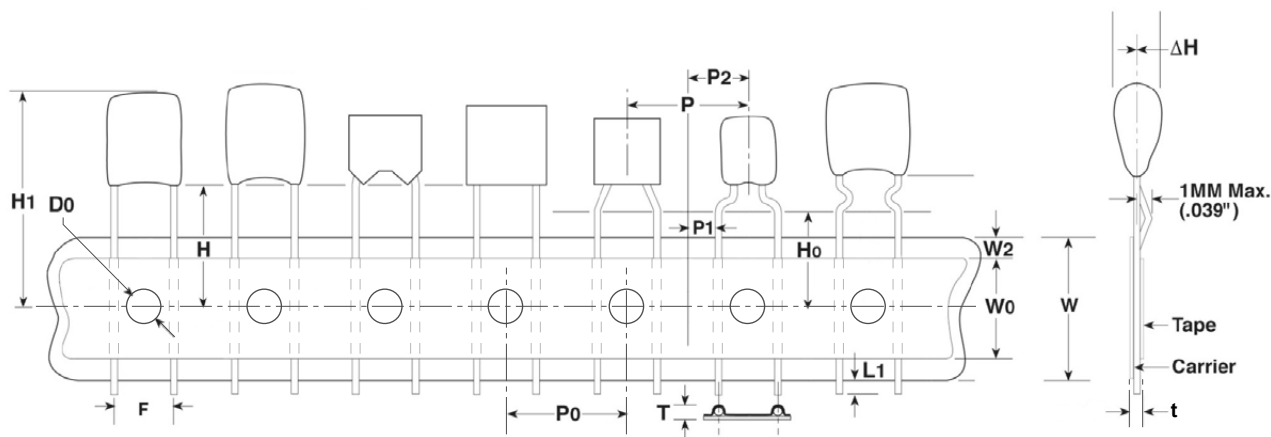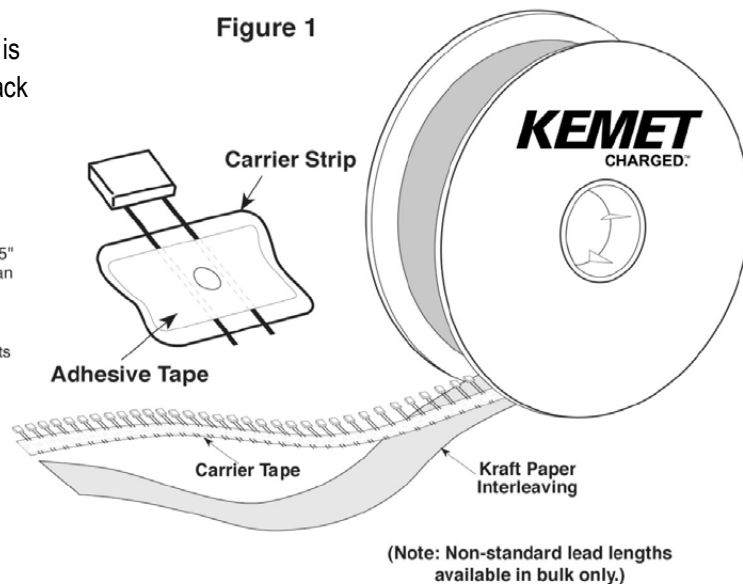.655" ±0.010" (16.6 ±0.25)

Figure 3: Standard Reel

Figure 2: Lead Tape Configuration (See Table Below)

Ceramic Radial Tape and Reel Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)									
D_0 ±0.2 (0.008)	P_0 ±0.3 (0.012)	ΔH ±0.2 (0.008)	L_1 Maximum	t ±0.2 (0.008)	T Maximum	W + 1.0/- 0.5 (+0.039/-0.020)	W_0 Minimum	W_2 Maximum
4.00 (0.157)	12.7 (0.500)	4.0 (0.157)	1.0 (0.039)	0.7 (0.051)	1.5 (0.059)	18.0 (0.709)	5.0 (0.197)	3.0 (0.118)

Ceramic Radial Tape and Reel Dimensions cont'd

Metric will govern

Variable Dimensions — Millimeters (Inches)					
F ±0.030 (0.78) Note 1	P_1 ±0.030 (0.012) Note 1	P ±0.3 (0.012)	P_2 ±1.3 (0.51)	H Minimum Note 2	H_0 ±0.5 (0.630) Note 3
2.54 (0.100)	5.08 (0.200)	12.7 (0.500)	6.35 (0.250)	18.0 (0.709)	16.0 (0.024)
4.32 (0.170)	3.89 (0.153)	12.7 (0.500)	6.35 (0.250)		
5.08 (0.200)	3.81 (0.150)	12.7 (0.500)	6.35 (0.250)		
5.59 (0.220)	3.25 (0.128)	12.7 (0.500)	6.35 (0.250)		
6.98 (0.275)	2.54 (0.100)	12.7 (0.500)	6.35 (0.250)		
7.62 (0.300)	2.24 (0.088)	12.7 (0.500)	6.35 (0.250)		
9.52 (0.375)	7.62 (0.300)	12.7 (0.500)	6.35 (0.250)		
10.16 (0.400)	7.34 (0.290)	25.4 (1.000)	N/A		
12.06 (0.475)	6.35 (0.250)	25.4 (1.000)	N/A		
14.60 (0.575)	5.08 (0.200)	25.4 (1.000)	N/A		
17.14 (0.675)	3.81 (0.15)	25.4 (1.000)	N/A		

1. Measured at the egress from the carrier tape, on the component side.

2. Straight Lead configuration part types only.

3. Formed (bent) lead configuration part types only.

Symbol Reference Table	
D_0	Sprocket Hole Diameter
P_0	Sprocket Hole Pitch
P	Component Pitch
F	Lead Spacing
P_1	Sprocket Hole Center to Lead Center
P_2	Sprocket Hole Center To Component Center
H	Height to Seating Plane (Straight Leads Only)
H_0	Height to Seating Plane (Formed Leads Only)
H_1	Component Height Above Tape Center
ΔH	Component Alignment
L_1	Lead Protrusion
t	Composite Tape Thickness
W	Carrier Tape Width
W_0	Hold-Down Tape Width
W_2	Hold-Down Tape Location

Tape & Reel Packaging Information

KEMET offers standard reeling of molded and conformally coated axial leaded ceramic capacitors for automatic insertion or lead forming machines in accordance with EIA standard 296. KEMET's internal specification four-digit suffix, 7200, is placed at the end of the part number to designate tape and reel packaging, e.g., C410C104Z5U5CA7200.

Paper (50 lb.) test minimum is inserted between the layers of capacitors wound on reels for component pitch ≤ 0.400". Capacitor lead length may extend only a maximum of .0625" (1.59 mm) beyond the tapes' edges. Capacitors are centered in a row between the two tapes and will deviate only ± 0.031" (0.79 mm) from the row center. A minimum of 36" (91.5 cm) leader tape is provided at each finished length of taped components. Universal splicing clips are used to connect the tape.
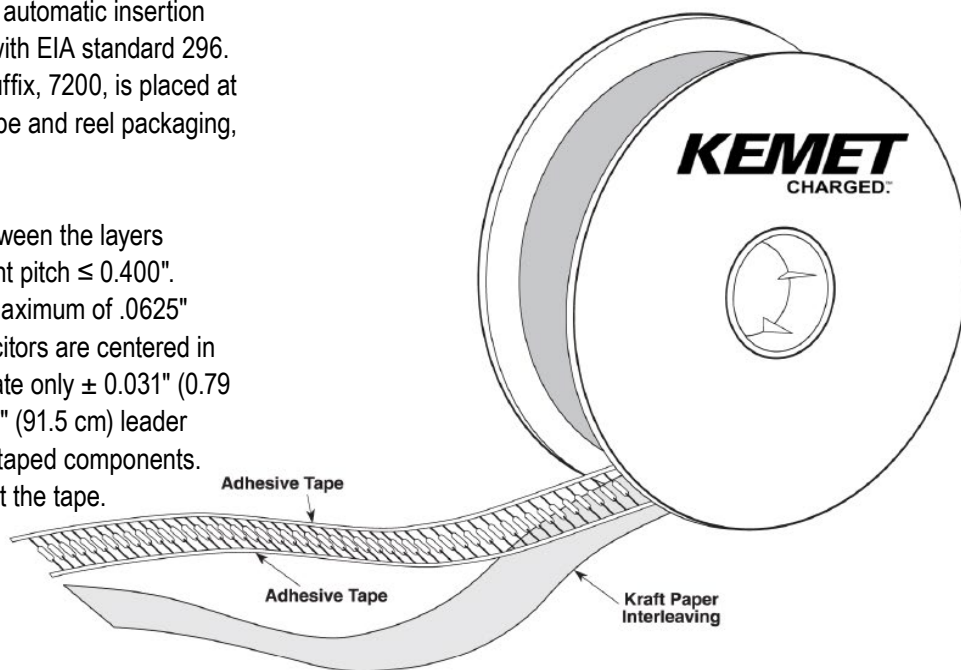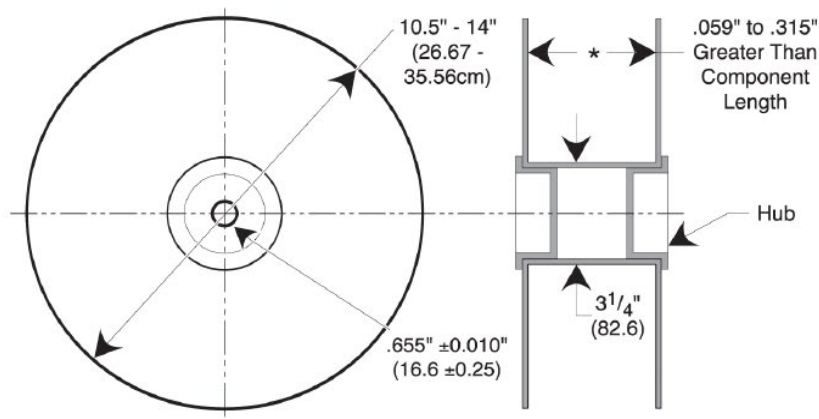

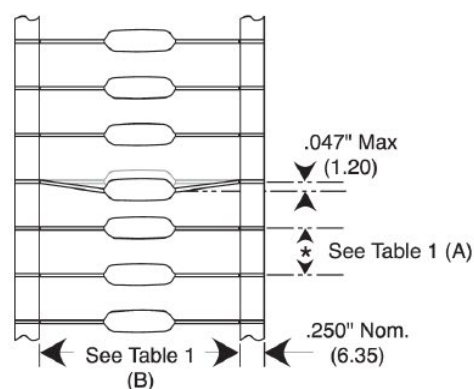Figure 1

Figure 2

Figure 3

Table 3 – Ceramic Axial Tape and Reel Dimensions
Metric will govern

Dimensions — Millimeters (Inches)		
Axial Capacitor Body Diameter	A ±0.5 (0.020)	B ±1.5 (0.059)*
0.0 to 5.0 (0.0 to 0.197)	5.0 (0.197)	52.4 (2.062)

Symbol Reference Table	
A	Component Pitch
B	Inside Tape Spacing

Inside tape spacing dimension (B) is determined by the body diameter of the capacitor.

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