Commercial "L" Series, SnPb Termination, C0G Dielectric 10 – 250 VDC (Commercial Grade)

Overview

KEMET's Commercial "L" Series with Tin/Lead Termination surface mount capacitors in C0G dielectric are designed to meet the needs of critical applications where tin/lead end metallization is required. KEMET's tin/lead electroplating process is designed to meet a 5% minimum lead content and address concerns for a more robust and reliable lead containing termination system. As the bulk of the electronics industry moves towards RoHS compliance, KEMET continues to provide tin/lead terminated products for military, aerospace and industrial applications and will ensure customers have a stable and long-term source of supply. KEMET's COG dielectric features a 125°C maximum operating temperature and is considered "stable." The Electronics Components, Assemblies & Materials Association (EIA) characterizes COG dielectric as a Class I material. Components of this classification are temperature compensating and are suited for resonant circuit applications or those where Q and stability of capacitance characteristics are required. COG exhibits no change in capacitance with respect to time and voltage and boasts a negligible change in capacitance with reference to ambient temperature. Capacitance change is limited to \pm 30 ppm/°C from -55° C to $+125^{\circ}$ C.

Benefits

С

Ceramic

- -55°C to +125°C operating temperature range
- · Reliable and robust termination system
- EIA 0402, 0603, 0805, 1206, 1210, 1808, 1812, 1825, 2220, and 2225 case sizes
- DC voltage ratings of 10 V, 16 V, 25 V, 50 V, 100 V, 200 V and 250 V
- Capacitance offerings ranging from 0.5 pF up to 0.47 μ F

С

Specification/

Series

C = Standard

• Available capacitance tolerances of ±0.10 pF, ±0.25 pF, ±0.5 pF, ±1%, ±2%, ±5%, ±10%, and ±20%

Ordering Information

1206

Case Size

(L" x W")

0402

0603

0805

1206

	1200	Use 9 for 1.0 – 9.9 pr	F = ±1%	5 = 50		
	1210	Use 8 for 0.5 – .99 pF	G = ±2%	1 = 100		
	1808	ex. 2.2 pF = 229	J = ±5%	2 = 200		
	1812	ex. 0.5 pF = 508	K = ±10%	A = 250		
	1825		M = ±20%			
	2220					
	2225					
L		· · · · · · · · · · · · · · · · · · ·				
	2225 Additional capacitance toleran	nce offerings may be available. Co				

J

Capacitance

Tolerance¹

 $B = \pm 0.10 \, pF$

 $C = \pm 0.25 \, pF$

 $D = \pm 0.5 \, pF$

E = 10/

3

Rated Voltage

(VDC)

8 = 10

4 = 16

3 = 25

E - EO

² Additional termination finish options may be available. Contact KEMET for details



G

Dielectric

G = COG

Α

Failure Rate/

Design

A = N/A

L

Termination

Finish²

L = SnPb (5%

Pb minimum)

TU

Packaging/Grade

(C-Spec)

See "Packaging

C-Spec

Ordering

Options Table" below

104

Capacitance

Code (pF)

Two significant

digits +

number of zeros.

Lice 0 for 10 00 pE







Packaging C-Spec Ordering Options Table

Packaging Type ¹	Packaging/Grade Ordering Code (C-Spec)
Bulk Bag / Unmarked	Not required (Blank)
7" Reel / Unmarked	TU
13" Reel / Unmarked	7411 (EIA 0603 and smaller case sizes) 7210 (EIA 0805 and larger case sizes)
7" Reel / Unmarked / 2 mm pitch ²	7081
13" Reel / Unmarked / 2 mm pitch ²	7082

¹ Default packaging is "Bulk Bag". An ordering code C-Spec is not required for "Bulk Bag" packaging.

¹ The terms "Marked" and "Unmarked" pertain to laser marking option of capacitors. All packaging options labeled as "Unmarked" will contain capacitors that have not been laser marked. The option to laser mark is not available on these devices. For more information see "Capacitor Marking".

² The 2 mm pitch option allows for double the packaging quantity of capacitors on a given reel size. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".

Benefits cont'd

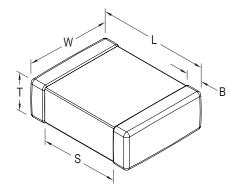
- No piezoelectric noise
- Extremely low ESR and ESL
- · High thermal stability
- High ripple current capability
- Preferred capacitance solution at line frequencies and into the MHz range
- Negligible capacitance change with respect to temperature from -55°C to +125°C
- · No capacitance change with respect to applied rated DC voltage
- · No capacitance decay with time
- · Non-polar device, minimizing installation concerns
- SnPb plated termination finish (5% Pb minimum)
- · Flexible termination option available upon request
- Available for other surface mount products, additional dielectrics and higher voltage ratings upon request

Applications

Typical applications include military, aerospace and other high reliability applications.



Dimensions – Millimeters (Inches)



EIA Size Code	Metric Size Code	L Length	W Width	T Thickness	B Bandwidth	S Separation Minimum	Mounting Technique
0402	1005	1.00 (.040) ± 0.05 (.002)	0.50 (.020) ± 0.05 (.002)		0.30 (.012) ± 0.10 (.004)	0.30 (.012)	Solder Reflow Only
0603	1608	1.60 (.063) ± 0.15 (.006)	0.80 (.032) ± 0.15 (.006)		0.35 (.014) ± 0.15 (.006)	0.70 (.028)	
0805	2012	2.00 (.079) ± 0.20 (.008)	1.25 (.049) ± 0.20 (.008)	25 (.049) ± 0.20 (.008) 0.50 (0.02) ± 0.25 (.010) 0.75 (.030)	0.75 (.030)	Solder Wave or Solder Reflow	
1206	3216	3.20 (.126) ± 0.20 (.008)	1.60 (.063) ± 0.20 (.008)	(.008) $0.50 (0.02) \pm 0.25 (.010)$ $0.75 (.030)$ Solution State $0.50 (0.02) \pm 0.25 (.010)$ $0.75 (.030)$ Solution State			
1210	3225	3.20 (.126) ± 0.20 (.008)	$1.25 (.049) \pm 0.20 (.008)$ $0.50 (0.02) \pm 0.25 (.010)$ $0.75 (.030)$ $1.60 (.063) \pm 0.20 (.008)$ $0.50 (0.02) \pm 0.25 (.010)$ $0.50 (0.02) \pm 0.25 (.010)$ $2.50 (.098) \pm 0.20 (.008)$ See Table 2 for $0.50 (0.02) \pm 0.25 (.010)$				
1808	4520	4.70 (.185) ± 0.50 (.020)	2.00 (.079) ± 0.20 (.008)	Thickness	0.60 (.024) ± 0.35 (.014)		
1812	4532	4.50 (.177) ± 0.30 (.012)	3.20 (.126) ± 0.30 (.012)		0.60 (.024) ± 0.35 (.014)	N/A	Ostidas Deflava Osta
1825	4564	4.50 (.177) ± 0.30 (.012)	6.40 (.252) ± 0.40 (.016)		0.60 (.024) ± 0.35 (.014)		Solder Reflow Only
2220	5650	5.70 (.224) ± 0.40 (.016)	5.00 (.197) ± 0.40 (.016)		0.60 (.024) ± 0.35 (.014)		
2225	5664	5.60 (.220) ± 0.40 (.016)	6.40 (.248) ± 0.40 (.016)		0.60 (.024) ± 0.35 (.014)		

Qualification/Certification

Commercial Grade products are subject to internal qualification. Details regarding test methods and conditions are referenced in Table 4, Performance & Reliability.

Environmental Compliance

These devices do not meet RoHS criteria due to the concentration of Lead (Pb) in the termination finish



Electrical Parameters/Characteristics

Item	Parameters/Characteristics
Operating Temperature Range	-55°C to +125°C
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	±30 ppm/°C
Aging Rate (Maximum % Capacitance Loss/Decade Hour)	0%
¹ Dielectric Withstanding Voltage (DWV)	250% of rated voltage (5 ±1 seconds and charge/discharge not exceeding 50 mA)
² Dissipation Factor (DF) Maximum Limit @ 25°C	0.1%
³ Insulation Resistance (IR) Limit @ 25°C	1,000 megohm microfarads or 100 G Ω (Rated voltage applied for 120 ±5 seconds @ 25°C)

¹ DWV is the voltage a capacitor can withstand (survive) for a short period of time. It exceeds the nominal and continuous working voltage of the capacitor. ²Capacitance and dissipation factor (DF) measured under the following conditions:

1 MHz ±100 kHz and 1.0 Vrms ±0.2 V if capacitance \leq 1,000 pF

1 kHz ±50 Hz and 1.0 Vrms ±0.2 V if capacitance > 1,000 pF

³To obtain IR limit, divide $M\Omega$ - μ F value by the capacitance and compare to G Ω limit. Select the lower of the two limits.

Capacitance and Dissipation Factor (DF) measured under the following conditions:

Note: When measuring capacitance it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."

Post Environmental Limits

	High Temperatu	ıre Life, Biased	Humidity, Mois	ture Resistance)										
Dielectric	Dielectric Rated DC Voltage Capacitance Value Dissipation Factor (Maximum %) Capacitance Shift Insulation Resistance														
C0G	All	All	0.5	0.3% or ±0.25 pF	10% of Initial Limit										



Table 1A – Capacitance Range/Selection Waterfall (0402 – 1206 Case Sizes)

Code Rated Voltage (VCC P R			Case Size/			CO	1403	20					C	160 [,]	30					C	1804	50					Cí	1200	60		
Cap Code Watage Code 4 4 5 5 2 A 8 4 3 5 1 2 A 8 4 3 5 1 2 A 8 4 3 5 1 2 A 8 4 3 5 1 2 A 8 4 3 5 1 2 A 8 4 3 5 1 2 A 8 4 3 5 1 2 A 8 4 3 5 1 2 A 8 4 3 5 1 2 A 8 4 3 5 1 2 A 8 4 3 5 1 2 A 8 8 8 2 2 4 3 5 1 2 A 8 4 3 5 1 2 4 8 8		Can	Series			CU	404	20					C	100.						C	.00							1200			
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2200 pF 222 2 F G J K M BB BB BB C C C C C C C C D DN	1,800 pF	182	F G J K M	BB	BB	BB					CF	CF	CF	CF	CF	СН	СН	DP	DP	DP	DP	DP	DN	DN	EB	EB	EB	EB	ED	ED	ED
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C1206C C1206C C1206C C1206C	Сар				4				2	A	8	4				2	Α	8	4				2	Α	8	4		1		2	A
			Case Size/Series			C)402	2C					_C(0603	BC					_C(080	5C					_C [,]	1206	SC		

*Capacitance range Includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82 and 91) KEMET reserves the right to substitute product with an improved temperature characteristic, tighter capacitance tolerance and/or higher voltage capability within the same form factor (configuration and dimensions).

These products are protected under US Patents 7,172,985 and 7,670,981, other patents pending, and any foreign counterparts.



Table 1A – Capacitance Range/Selection Waterfall (0402 – 1206 Case Sizes) cont'd

	Сар	Case Size/ Series			C	0402	2C					C)60:	3C					С)80	5C					C	1206	6C		
Сар		Voltage Code	8	4	3	5	1	2	A	8	4	3	5	1	2	Α	8	4	3	5	1	2	Α	8	4	3	5	1	2	Α
	Code	Rated Voltage (VDC)	10	16	25	50	100	200	250	10	16	25	50	100	200	250	9	16	25	50	100	200	250	10	16	25	50	100	200	250
		Capacitance Tolerance		Pro	odu	ct A	vail	abil	ity a	and	Chi	p Tł	nick	nes	s Co	ode	s –	See	Tab	le 2	for	Chi	p Tl	hick	nes	s D	ime	nsio	ns	
33,000 pF	333	F G J K M															DG	DG	DG					EB	EB	EB	EB	EE		
39,000 pF	393	F G J K M															DG	DG	DG					EC	EC	EC	EE	EH		
47,000 pF	473	F G J K M															DG	DG	DG					EC	EC	EC	EE	EH		
56,000 pF	563	FGJKM																						ED	ED	ED	EF			
68,000 pF	683	F G J K M																						EF	EF	EF	EH			
82,000 pF	823	F G J K M																						EH	EH	EH	EH			
0.10 µF	104	FGJKM																						EH	EH	EH				
68,000 pF	683	FGJKM																						EF	EF	EF	EH			
82,000 pF	823	FGJKM																						EH	EH	EH	EH			
0.10 µF	104	F G J K M																						EH	EH					
		Rated Voltage (VDC)	9	16	25	50	100	200	250	ę	9	25	50	100	200	250	ę	9	25	50	<u>ۇ</u>	200	250	ę	9	25	50	10	200	250
Сар	Сар	Voltage Code	8	4	3	5	1	2	A	8	4	3	5	1	2	A	8	4	3	5	1	2	A	8	4		5			_∼ A
	Code	Case Size/Series	-			0402	2C	-					0603	BC					-)80	5C	-					° 1206			

Table 1B – Capacitance Range/Selection Waterfall (1210 – 2225 Case Sizes)

		Case Size/ Series			C 1	210	C			(C18	080	;	(C18	120	;	(C18	250		(C22	200	;	(C22	250	;
Сар	Сар	Voltage Code	8	4	3	5	1	2	Α	5	1	2	A	5	1	2	A	5	1	2	Α	5	1	2	A	5	1	2	A
Oap	Code	Rated Voltage (VDC)	9	16	25	50	9	200	250	50	100	200	250	50	100	200	250	50	10	200	250	50	10	200	250	50	100	200	250
		Capacitance								Pro	duc	t Av	aila	bili	ty a	nd C	Chip	h Thi	ickn	iess	Co	des							
10.01.55	100 0101	Tolerance		50	50	50	50	50	50	S	ee I	abl	e 21	or C	Chip	In	ckn	less	Dir	nen	sion	IS				_			
1.0 – 9.1 pF*		BCD	FB	FB	FB	FB	FB	FB	FB																				
10 – 91 pF*	100 - 910*	FGJKM		FB	FB	FB	FB	FB	FB																				
100 – 300 pF*	101 – 301*	FGJKM		FB	FB	FB	FB	FB	FB	. –																			
330 – 430 pF*	331 – 431*	FGJKM		FB	FB	FB	FB	FB	FB	LF	LF	LF	LF																
470 – 910 pF*	471 – 911*	F G J K M		FB	FB	FB	FB	FB	FB	LF	LF	LF	LF	GB	GB		GB												
1,000 pF	102	F G J K M		FB	FB	FB	FB	FB	FB	LF	LF	LF	LF	GB	GB	GB	GB												
1,100 pF	112	F G J K M		FB	FB	FB	FB	FB	FB	LF	LF	LF	LF	GB	GB	GB	GB												
1,200 pF	122	F G J K M		FB	FB	FB	FB	FB	FB	LF	LF	LF	LF	GB	GB	GB	GB												
1,300 pF	132	F G J K M		FB	FB	FB	FB	FC	FC	LF	LF	LF	LF	GB	GB	GB	GB												
1,500 pF	152	F G J K M	FB	FB	FB	FB	FB	FE	FE	LF	LF	LF	LF	GB	GB	GB	GB												
1,600 pF	162	F G J K M	FB	FB	FB	FB	FB	FE	FE	LF	LF	LF	LF	GB	GB	GB	GB												
1,800 pF	182	F G J K M	FB	FB	FB	FB	FB	FE	FE	LF	LF	LF	LF	GB	GB	GB	GB												
2,000 pF	202	F G J K M	FB	FB	FB	FB	FC	FE	FE	LF	LF	LF	LF	GB	GB	GB	GB												
2,200 pF	222	F G J K M	FB	FB	FB	FB	FC	FG	FG	LF	LF	LF	LF	GB	GB	GB	GB												
2,400 pF	242	F G J K M	FB	FB	FB	FB	FC	FC	FC	LF	LF	LF	LF																
2,700 pF	272	F G J K M	FB	FB	FB	FB	FC	FC	FC	LF	LF	LF	LF	GB	GB	GB	GB												
3,000 pF	302	F G J K M	FB	FB	FB	FB	FC	FF	FF	LF	LF																		
3,300 pF	332	F G J K M	FB	FB	FB	FB	FF	FF	FF	LF	LF			GB	GB	GB	GB												
3,600 pF	362	F G J K M	FB	FB	FB	FB	FF	FF	FF	LF	LF																		
3,900 pF	392	F G J K M	FB	FB	FB	FB	FF	FF	FF	LF	LF			GB	GB	GB	GB	НВ	НВ	НВ	ΗВ								
4,300 pF	432	F G J K M		FB	FB	FB	FF	FF	FF	LF	LF				-	-													
4,700 pF	472	FGJKM	FF	FF	FF	FF	FG	FG	FG	LF	LF			GB	GB	GD	GD	ΗВ	HB	HB	HB					KE	KE	KE	KE
		Rated Voltage (VDC)	9	16	25	50	100	200	250	50	100	200	250	50	100	200	250	50	10	200	250	50	10	200	250	50	100	200	250
Сар	Cap	Voltage Code	8	4	3	5	1	2	A	5	1	2	A	5	1	2	A	5	1	2	A	5	1	2	A	5	1	2	A
	Code	Case Size/Series				1210)C				C18	08C		—	C18				C18	25C			C22				C22		

*Capacitance range Includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82 and 91) KEMET reserves the right to substitute product with an improved temperature characteristic, tighter capacitance tolerance and/or higher voltage capability within the same form factor (configuration and dimensions).

These products are protected under US Patents 7,172,985 and 7,670,981, other patents pending, and any foreign counterparts.



Table 1B – Capacitance Range/Selection Waterfall (1210 – 2225 Case Sizes)

		Case Size/ Series			C	21	C			(C18	080	;		C18	120	;	(C18	250	;		C22	200	C		C22	25C	;
Сар	Сар	Voltage Code	8	4	3	5	1	2	A	5	1	2	A	5	1	2	A	5	1	2	A	5	1	2	A	5	1	2	Α
Oap	Code	Rated Voltage (VDC)	ę	16	25	50	9	200	250	50	<u>1</u> 0	200	250	50	<u>1</u> 0	200	250	50	<u>1</u> 0	200	250	50	<u>1</u>	200	250	50	10	200	250
		Capacitance Tolerance			L					Pro	duc	t A	aila	abili for 0	ty a Chin	nd C	Chip	h Thi	ickn Din	iess	Co	des							
5,100 pF	512		FB	FB	FB	FB	FG	FG	FG						Jinp		CKI				3101					KE	KE	KE	KE
5,600 pF	562	F G J K M	FB	FB	FB	FB	FG	FG	FG					GB	GB	GH	GH	НВ	НВ	НВ	НВ					KE	KE	KE	KE
6,200 pF	622	FGJKM	FB	FB	FB	FB	FG	FB	FB																	KE	KE	KE	KE
6,800 pF	682	FGJKM	FB	FB	FB	FB	FG	FB	FB					GB	GB	GJ	GJ	НВ	ΗВ	НВ	ΗВ	JE	JE	JB		KE	KE	KE	KE
7,500 pF	752	FGJKM	FC	FC	FC	FC	FC	FB	FB						-											KE	KE	KE	KE
8,200 pF	822	FGJKM	FC	FC	FC	FC	FC	FB	FB					GB	GH	GB	GB	НВ	НВ	НВ	НВ	JE	JE	JB		KE	KE	KE	KE
9,100 pF	912	FGJKM	FE	FE	FE	FE	FE	FB	FB																	KE	KE	KE	KE
10,000 pF	103	FGJKM	FF	FF	FF	FF	FF	FB	FB					GB	GH	GB	GB	НВ	НВ	HE	HE	JE	JE	JB		KE	KE	KE	KE
12,000 pF	123	F G J K M	FG	FG	FG	FG	FB	FB	FB					GB	GG	GB	GB	HB	HB	HE	HE	JE	JE	JB		KE	KE	KE	KE
15,000 pF	153	F G J K M	FG	FG	FG	FG	FB	FC	FC					GB	GB	GB	GB	НВ	HB			JE	JE	JB		KE	KE	KE	KE
18,000 pF	183	F G J K M	FB	FB	FB	FB	FB	FC	FC					GB	GB	GB	GB	HB	HE			JE	JE	JB		KE	KE		
22,000 pF	223	F G J K M	FB	FB	FB	FB	FB	FF	FF					GB	GB	GB	GB	ΗВ	HE			JE	JB	JB		KE	KE		
27,000 pF	273	F G J K M	FB	FB	FB	FB	FB	FG	FG					GB	GB	GB	GB	ΗВ	HG			JE	JB	JB		KE	KE		
33,000 pF	333	F G J K M	FB	FB	FB	FB	FB	FH	FH					GB	GB	GB	GB					JB	JB	JB		KE			
39,000 pF	393	F G J K M	FB	FB	FB	FB	FE	FH	FH					GB	GB	GB	GB					JB	JB	JB					
47,000 pF	473	F G J K M	FB	FB	FB	FB	FE	FJ	FJ					GB	GB	GD	GD					JB	JB	JB					
56,000 pF	563	F G J K M	FB	FB	FB	FB	FF							GB	GB	GD	GD					JB	JB	JB					
68,000 pF	683	F G J K M	FB	FB	FB	FC	FG							GB	GB	GK	GK					JB	JB	JB					
82,000 pF	823	F G J K M	FC	FC	FC	FF	FH							GB	GB	GM	GM					JB	JB	JB					
0.10 µF	104	F G J K M	FE	FE	FE	FG	FM							GB	GD	GM	GM					JB	JB	JD					
0.12 µF	124	F G J K M	FG	FG	FG	FH								GB	GH							JB	JB	JD					
0.15 µF	154	F G J K M	FH	FH	FH	FM								GD	GN							JB	JB	JG					
0.18 µF	184	F G J K M	FJ	FJ	FJ									GH								JB	JD	JG					
0.22 µF	224	F G J K M	FK	FK	FK									GK								JB	JD	JL					
0.27 μF	274	F G J K M																				JB	JF						
0.33 µF	334	F G J K M																				JD	JG						
0.39 µF	394	F G J K M																				JG							
0.47 µF	474	F G J K M																				JG							
		Rated Voltage (VDC)	9	16	25	50	100	200	250	50	100	200	250	50	100	200	250	50	100	200	250	50	100	200	250	50	100	200	250
Сар	Cap Code	Voltage Code	8	4	3	5	1	2	Α	5	1	2	A	5	1	2	Α	5	1	2	Α	5	1	2	Α	5	1	2	Α
		Case Size/Series			C	1210	C				C18	08C			C18	12C			C18	25C	;		C22	200	:		C22	25C	

*Capacitance range Includes E24 decade values only. (i.e., 10, 11, 12, 13, 15, 16, 18, 20, 22, 24, 27, 30, 33, 36, 39, 43, 47, 51, 56, 62, 68, 75, 82 and 91) KEMET reserves the right to substitute product with an improved temperature characteristic, tighter capacitance tolerance and/or higher voltage capability within the same form factor (configuration and dimensions).

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Table 2A – Chip Thickness/Tape & Reel Packaging Quantities

Thickness	Case	Thickness ±	Paper Q	uantity ¹	Plastic (Quantity
Code	Size ¹	Range (mm)	7" Reel	13" Reel	7" Reel	13" Reel
BB	0402	0.50 ± 0.05	10,000	50,000	0	0
BD	0402	0.55 ± 0.05	10,000	50,000	0	0
CF	0603	0.80 ± 0.07	4,000	15,000	0	0
CH	0603	0.85 ± 0.07	4,000	10,000	0	0
DN	0805	0.78 ± 0.10	4,000	15,000	0	0
DP DE	0805 0805	0.90 ± 0.10	4,000	15,000	0 2,500	0 10,000
DE	0805	1.00 ± 0.10	0	0	2,500	10,000
DF	0805	1.10 ± 0.10 1.25 ± 0.15	0 0	0 0	2,500	10,000
EB	1206	0.78 ± 0.10	4,000	10,000	4,000	10,000
EC	1206	0.90 ± 0.10	4,000	0	4,000	10,000
ED	1206	1.00 ± 0.10	0	0	2,500	10,000
EE	1206	1.10 ± 0.10	Ő	Ő	2,500	10,000
EF	1206	1.20 ± 0.15	Ő	Ő	2,500	10,000
EH	1206	1.60 ± 0.20	Ő	Ő	2,000	8,000
FB	1210	0.78 ± 0.10	0	0	4,000	10,000
FC	1210	0.90 ± 0.10	0	0	4,000	10,000
FE	1210	1.00 ± 0.10	0	0	2,500	10,000
FF	1210	1.10 ± 0.10	0	0	2,500	10,000
FG	1210	1.25 ± 0.15	0	0	2,500	10,000
FH	1210	1.55 ± 0.15	0	0	2,000	8,000
FM	1210	1.70 ± 0.20	0	0	2,000	8,000
FJ	1210	1.85 ± 0.20	0	0	2,000	8,000
FK	1210	2.10 ± 0.20	0	0	2,000	8,000
NC	1706	1.00 ± 0.15	0	0	4,000	10,000
LF	1808	1.00 ± 0.15	0	0	2,500	10,000
GB	1812	1.00 ± 0.10	0	0	1,000	4,000
GD GH	1812	1.25 ± 0.15	0 0	0 0	1,000	4,000
GG	1812 1812	1.40 ± 0.15 1.55 ± 0.10	0	0	1,000 1,000	4,000 4,000
GG	1812	1.60 ± 0.20	0	0	1,000	4,000
GJ	1812	1.00 ± 0.20 1.70 ± 0.15	0	0	1,000	4,000
GN	1812	1.70 ± 0.10 1.70 ± 0.20	0 0	Ő	1,000	4,000
GM	1812	2.00 ± 0.20	Ő	Ő	500	2,000
HB	1825	1.10 ± 0.15	Ő	Ő	1,000	4,000
HE	1825	1.40 ± 0.15	0	0	1,000	4,000
HG	1825	1.60 ± 0.20	0	0	1,000	4,000
JB	2220	1.00 ± 0.15	0	0	1,000	4,000
JD	2220	1.30 ± 0.15	0	0	1,000	4,000
JE	2220	1.40 ± 0.15	0	0	1,000	4,000
JF	2220	1.50 ± 0.15	0	0	1,000	4,000
JG	2220	1.70 ± 0.15	0	0	1,000	4,000
JL	2220	2.00 ± 0.20	0	0	500	2,000
KE	2225	1.40 ± 0.15	0 7" Reel	0 13" Reel	1,000 7" Reel	4,000
Thickness Code	Case Size	Thickness ± Range (mm)				13" Reel
	0120		Paper C	Quantity	Plastic (Quantity

Package quantity based on finished chip thickness specifications.

¹ If ordering using the 2 mm Tape and Reel pitch option, the packaging quantity outlined in the table above will be doubled. This option is limited to EIA 0603 (1608 metric) case size devices. For more information regarding 2 mm pitch option see "Tape & Reel Packaging Information".



Table 2B – Bulk Packaging Quantities

Desheet		Loose Pa	ackaging
Раскаді	ng Type	Bulk Bag	(default)
Packagin	g C-Spec ¹	N/	'A ²
Case	Size	Packaging Quantities (pieces/unit packaging)
EIA (in)	Metric (mm)	Minimum	Maximum
0402	1005		
0603	1608	-	
0805	2012	-	50,000
1206	3216	-	
1210	3225	1	
1808	4520		
1812	4532	-	
1825	4564		20,000
2220	5650		
2225	5664		

¹ The "Packaging C-Spec" is a 4 to 8 digit code which identifies the packaging type and/or product grade. When ordering, the proper code must be included in the 15th through 22nd character positions of the ordering code. See "Ordering Information" section of this document for further details. Commercial Grade product ordered without a packaging C-Spec will default to our standard "Bulk Bag" packaging. Contact KEMET if you require a bulk bag packaging option for Automotive Grade products. ² A packaging C-Spec (see note 1 above) is not required for "Bulk Bag" packaging (excluding Anti-Static Bulk Bag and Automotive Grade products). The 15th through 22nd character positions of the ordering code should be left blank. All product ordered without a packaging C-Spec will default to out standard "Bulk Bag" packaging.



Table 3 – Chip Capacitor Land Pattern Design Recommendations per IPC–7351

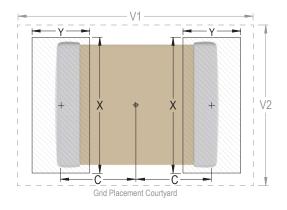
EIA Size Code	Metric Size Code			sity Lev mum (I rotrusio	Nost))		Media	sity Lev an (Nor rotrusio)		Minii	sity Lev mum (L rotrusio	east))
ooue	oode	С	Y	X	V1	V2	С	Y	X	V1	V2	С	Y	X	V1	V2
0402	1005	0.50	0.72	0.72	2.20	1.20	0.45	0.62	0.62	1.90	1.00	0.40	0.52	0.52	1.60	0.80
0603	1608	0.90	1.15	1.10	4.00	2.10	0.80	0.95	1.00	3.10	1.50	0.60	0.75	0.90	2.40	1.20
0805	2012	1.00	1.35	1.55	4.40	2.60	0.90	1.15	1.45	3.50	2.00	0.75	0.95	1.35	2.80	1.70
1206	3216	1.60	1.35	1.90	5.60	2.90	1.50	1.15	1.80	4.70	2.30	1.40	0.95	1.70	4.00	2.00
1210	3225	1.60	1.35	2.80	5.65	3.80	1.50	1.15	2.70	4.70	3.20	1.40	0.95	2.60	4.00	2.90
1210 ¹	3225	1.50	1.60	2.90	5.60	3.90	1.40	1.40	2.80	4.70	3.30	1.30	1.20	2.70	4.00	3.00
1808	4520	2.30	1.75	2.30	7.40	3.30	2.20	1.55	2.20	6.50	2.70	2.10	1.35	2.10	5.80	2.40
1812	4532	2.15	1.60	3.60	6.90	4.60	2.05	1.40	3.50	6.00	4.00	1.95	1.20	3.40	5.30	3.70
1825	4564	2.15	1.60	6.90	6.90	7.90	2.05	1.40	6.80	6.00	7.30	1.95	1.20	6.70	5.30	7.00
2220	5650	2.75	1.70	5.50	8.20	6.50	2.65	1.50	5.40	7.30	5.90	2.55	1.30	5.30	6.60	5.60
2225	5664	2.70	1.70	6.90	8.10	7.90	2.60	1.50	6.80	7.20	7.30	2.50	1.30	6.70	6.50	7.00

¹ Only for capacitance values \geq 22 μ F

Density Level A: For low-density product applications. Recommended for wave solder applications and provides a wider process window for reflow solder processes. KEMET only recommends wave soldering of EIA 0603, 0805 and 1206 case sizes.

Density Level B: For products with a moderate level of component density. Provides a robust solder attachment condition for reflow solder processes. **Density Level C:** For high component density product applications. Before adapting the minimum land pattern variations the user should perform qualification testing based on the conditions outlined in IPC Standard 7351 (IPC–7351).

Image below based on Density Level B for an EIA 1210 case size.





Soldering Process

Recommended Soldering Technique:

- Solder wave or solder reflow for EIA case sizes 0603, 0805 and 1206
- · All other EIA case sizes are limited to solder reflow only

Recommended Reflow Soldering Profile:

KEMET's families of surface mount multilayer ceramic capacitors (SMD MLCCs) are compatible with wave (single or dual), convection, IR or vapor phase reflow techniques. Preheating of these components is recommended to avoid extreme thermal stress. KEMET's recommended profile conditions for convection and IR reflow reflect the profile conditions of the IPC/J-STD-020 standard for moisture sensitivity testing. These devices can safely withstand a maximum of three reflow passes at these conditions.

Profile Feature	Termination Finish			
FIONETeature	SnPb	100% Matte Sn		
Preheat/Soak				
Temperature Minimum (T _{Smin})	100°C	150°C		
Temperature Maximum (T _{Smax})	150°C	200°C		
Time (t _s) from T_{Smin} to T_{Smax}	60 – 120 seconds	60 – 120 seconds		
Ramp-Up Rate $(T_L \text{ to } T_P)$	3°C/second maximum	3°C/second maximum		
Liquidous Temperature (T_L)	183°C	217°C		
Time Above Liquidous (t_L)	60 – 150 seconds	60 – 150 seconds		
Peak Temperature (T _P)	235°C	260°C		
Time Within 5°C of Maximum Peak Temperature (t _P)	20 seconds maximum	30 seconds maximum		
Ramp-Down Rate $(T_P \text{ to } T_L)$	6°C/second maximum	6°C/second maximum		
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum		

Note 1: All temperatures refer to the center of the package, measured on the capacitor body surface that is facing up during assembly reflow.

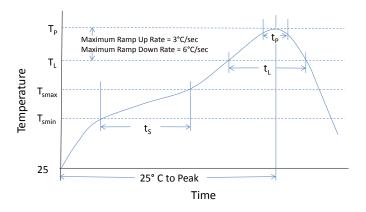




Table 4 – Performance & Reliability: Test Methods and Conditions

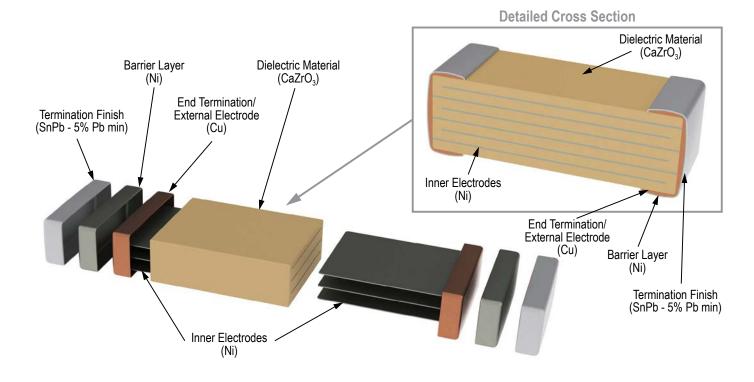
Stress	Reference	Test or Inspection Method		
Terminal Strength	JIS-C-6429	Appendix 1, Note: Force of 1.8 kg for 60 seconds.		
Board Flex	JIS-C-6429	Appendix 2, Note: Standard termination system – 2.0 mm (minimum) for all except 3 mm for C0G. Flexible termination system – 3.0 mm (minimum).		
		Magnification 50 X. Conditions:		
Solderability	J-STD-002	a) Method B, 4 hours @ 155°C, dry heat @ 235°C		
Solderability	J-31D-002	b) Method B @ 215°C category 3		
		c) Method D, category 3 @ 260°C		
Temperature Cycling	JESD22 Method JA-104	1,000 Cycles (-55°C to +125°C). Measurement at 24 hours +/- 4 hours after test conclusion.		
Biased Humidity	MIL-STD-202 Method 103	Load Humidity: 1,000 hours 85°C/85% RH and rated voltage. Add 100 K ohm resistor. Measurement at 24 hours +/- 4 hours after test conclusion. Low Volt Humidity: 1,000 hours 85°C/85% RH and 1.5 V. Add 100 K ohm resistor. Measurement at 24 hours +/- 4 hours after test conclusion.		
Moisture Resistance	MIL-STD-202 Method 106	t = 24 hours/cycle. Steps 7a and 7b not required. Measurement at 24 hours +/- 4 hours after test conclusion.		
Thermal Shock	MIL-STD-202 Method 107	-55°C/+125°C. Note: Number of cycles required – 300, maximum transfer time – 20 seconds, dwell time – 15 minutes. Air – Air.		
High Temperature Life	MIL-STD-202 Method 108 /EIA-198	1,000 hours at 125°C (85°C for X5R, Z5U and Y5V) with 2 X rated voltage applied.		
Storage Life	MIL-STD-202 Method 108	150°C, 0 VDC for 1,000 hours.		
Vibration	MIL-STD-202 Method 204	5 g's for 20 min., 12 cycles each of 3 orientations. Note: Use 8" X 5" PCB 0.031" thick 7 secure points on one long side and 2 secure points at corners of opposite sides. Parts mounted within 2" from any secure point. Test from 10 – 2,000 Hz		
Mechanical Shock	MIL-STD-202 Method 213	Figure 1 of Method 213, Condition F.		
Resistance to Solvents	MIL-STD-202 Method 215	Add aqueous wash chemical, OKEM Clean or equivalent.		

Storage and Handling

Ceramic chip capacitors should be stored in normal working environments. While the chips themselves are quite robust in other environments, solderability will be degraded by exposure to high temperatures, high humidity, corrosive atmospheres, and long term storage. In addition, packaging materials will be degraded by high temperature– reels may soften or warp and tape peel force may increase. KEMET recommends that maximum storage temperature not exceed 40°C and maximum storage humidity not exceed 70% relative humidity. Temperature fluctuations should be minimized to avoid condensation on the parts and atmospheres should be free of chlorine and sulfur bearing compounds. For optimized solderability chip stock should be used promptly, preferably within 1.5 years of receipt.



Construction



Capacitor Marking (Optional):

Laser marking option is not available on:

- C0G, Ultra Stable X8R and Y5V dielectric devices
- · EIA 0402 case size devices
- EIA 0603 case size devices with Flexible Termination option.
- · KPS Commercial and Automotive grade stacked devices.

These capacitors are supplied unmarked only.



Tape & Reel Packaging Information

KEMET offers multilayer ceramic chip capacitors packaged in 8, 12 and 16 mm tape on 7" and 13" reels in accordance with EIA Standard 481. This packaging system is compatible with all tape-fed automatic pick and place systems. See Table 2 for details on reeling quantities for commercial chips.

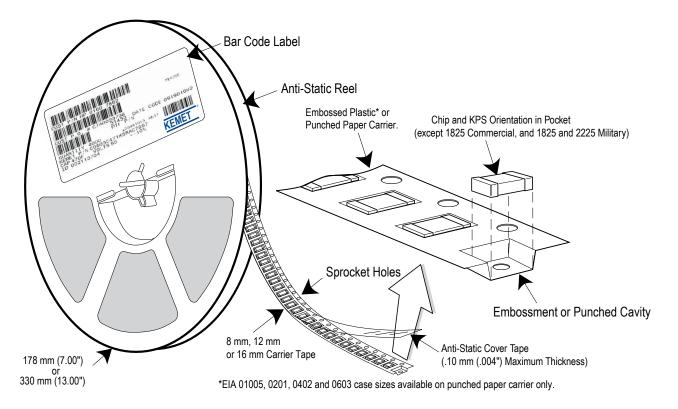


Table 5 – Carrier Tape Configuration, Embossed Plastic & Punched Paper (mm)

Таре		Embosse	ed Plastic	Punched Paper		
EIA Case Size	Size	7" Reel	13" Reel	7" Reel	13" Reel	
	(W)*	Pitch (P ₁)*		Pitch (P ₁)*		
01005 – 0402	8			2	2	
0603	8			2/4	2/4	
0805	8	4	4	4	4	
1206 – 1210	8	4	4	4	4	
1805 – 1808	12	4	4			
≥ 1812	12	8	8			
KPS 1210	12	8	8			
KPS 1812 & 2220	16	12	12			
Array 0508 & 0612	8	4	4			

*Refer to Figures 1 & 2 for W and P₁ carrier tape reference locations. *Refer to Tables 6 & 7 for tolerance specifications.

New 2 mm Pitch Reel Options*

Packaging Ordering Code (C-Spec)	Packaging Type/Options
C-3190	Automotive grade 7" reel unmarked
C-3191	Automotive grade 13" reel unmarked
C-7081	Commercial grade 7" reel unmarked
C-7082	Commercial grade 13" reel unmarked

* 2 mm pitch reel only available for 0603 EIA case size.

2 mm pitch reel for 0805 EIA case size under development.

Benefits of Changing from 4 mm to 2 mm Pitching Spacing

- Lower placement costs
- Double the parts on each reel results in fewer reel changes and increased efficiency
- Fewer reels result in lower packaging, shipping and storage costs, reducing waste



Figure 1 – Embossed (Plastic) Carrier Tape Dimensions

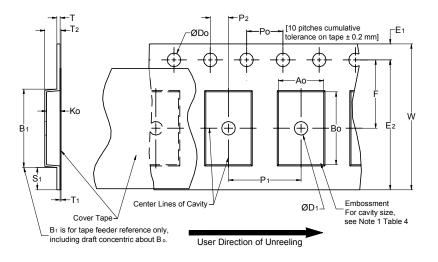


Table 6 – Embossed (Plastic) Carrier Tape Dimensions

Metric will govern

	Constant Dimensions — Millimeters (Inches)								
Tape Size	D ₀	D ₁ Minimum Note 1	E ₁	P ₀	P ₂	R Reference Note 2	S ₁ Minimum Note 3	T Maximum	T₁ Maximum
8 mm		1.0 (0.039)				25.0 (0.984)			
12 mm	1.5 +0.10/-0.0 (0.059 +0.004/-0.0)	1.5	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	30	0.600 (0.024)	0.600 (0.024)	0.100 (0.004)
16 mm		(0.059)				(1.181)			
			Variable Dime	ensions — Mil	limeters (Inch	es)			
Tape Size Pitch B1 Maximum E2 F P1 T2 W Note 4 Minimum F P1 Maximum Maximum A0,B0 & K							& K ₀		
8 mm	Single (4 mm)	4.35 (0.171)	6.25 (0.246)	3.5 ±0.05 (0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	2.5 (0.098)	8.3 (0.327)		
12 mm	Single (4 mm) & Double (8 mm)	8.2 (0.323)	10.25 (0.404)	5.5 ±0.05 (0.217 ±0.002)	8.0 ±0.10 (0.315 ±0.004)	4.6 (0.181)	12.3 (0.484)	Not	ie 5
16 mm	Triple (12 mm)	12.1 (0.476)	14.25 (0.561)	7.5 ±0.05 (0.138 ±0.002)	12.0 ±0.10 (0.157 ±0.004)	4.6 (0.181)	16.3 (0.642)		

1. The embossment hole location shall be measured from the sprocket hole controlling the location of the embossment. Dimensions of embossment location and hole location shall be applied independent of each other.

2. The tape with or without components shall pass around R without damage (see Figure 6).

3. If S₁ < 1.0 mm, there may not be enough area for cover tape to be properly applied (see EIA Standard 481 paragraph 4.3 section b).

4. B, dimension is a reference dimension for tape feeder clearance only.

5. The cavity defined by A_{α} , B_{α} and K_{α} shall surround the component with sufficient clearance that:

(a) the component does not protrude above the top surface of the carrier tape.

(b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.

(c) rotation of the component is limited to 20° maximum for 8 and 12 mm tapes and 10° maximum for 16 mm tapes (see Figure 3).

(d) lateral movement of the component is restricted to 0.5 mm maximum for 8 and 12 mm wide tape and to 1.0 mm maximum for 16 mm tape (see Figure 4).

(e) for KPS Series product, A_0 and B_0 are measured on a plane 0.3 mm above the bottom of the pocket.

(f) see Addendum in EIA Standard 481 for standards relating to more precise taping requirements.



Figure 2 – Punched (Paper) Carrier Tape Dimensions

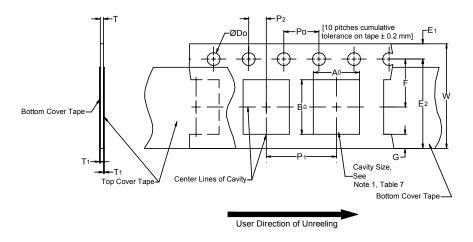


Table 7 – Punched (Paper) Carrier Tape Dimensions

Metric will govern

	Constant Dimensions — Millimeters (Inches)								
Tape Size	D ₀	E ₁	P ₀	P ₂	T ₁ Maximum	G Minimum	R Reference Note 2		
8 mm	1.5 +0.10 -0.0 (0.059 +0.004 -0.0)	1.75 ±0.10 (0.069 ±0.004)	4.0 ±0.10 (0.157 ±0.004)	2.0 ±0.05 (0.079 ±0.002)	0.10 (0.004) Maximum	0.75 (0.030)	25 (0.984)		
	Variable Dimensions — Millimeters (Inches)								
Tape Size	Pitch	E2 Minimum	F	P ₁	T Maximum	W Maximum	A_0B_0		
8 mm	Half (2 mm)	6.25	3.5 ±0.05	2.0 ±0.05 (0.079 ±0.002)	1.1	8.3 (0.327)	Note 1		
8 mm	Single (4 mm)	(0.246)	(0.138 ±0.002)	4.0 ±0.10 (0.157 ±0.004)	(0.098)	8.3 (0.327)	NOLE I		

1. The cavity defined by A_{α} , B_{α} and T shall surround the component with sufficient clearance that:

a) the component does not protrude beyond either surface of the carrier tape.

b) the component can be removed from the cavity in a vertical direction without mechanical restriction, after the top cover tape has been removed.

c) rotation of the component is limited to 20° maximum (see Figure 3).

d) lateral movement of the component is restricted to 0.5 mm maximum (see Figure 4).

e) see Addendum in EIA Standard 481 for standards relating to more precise taping requirements.

2. The tape with or without components shall pass around R without damage (see Figure 6).



Packaging Information Performance Notes

- 1. Cover Tape Break Force: 1.0 Kg minimum.
- 2. Cover Tape Peel Strength: The total peel strength of the cover tape from the carrier tape shall be:

Tape Width	Peel Strength
8 mm	0.1 to 1.0 Newton (10 to 100 gf)
12 and 16 mm	0.1 to 1.3 Newton (10 to 130 gf)

The direction of the pull shall be opposite the direction of the carrier tape travel. The pull angle of the carrier tape shall be 165° to 180° from the plane of the carrier tape. During peeling, the carrier and/or cover tape shall be pulled at a velocity of 300 ± 10 mm/minute. **3. Labeling:** Bar code labeling (standard or custom) shall be on the side of the reel opposite the sprocket holes. *Refer to EIA Standards* 556 *and* 624.

Figure 3 – Maximum Component Rotation

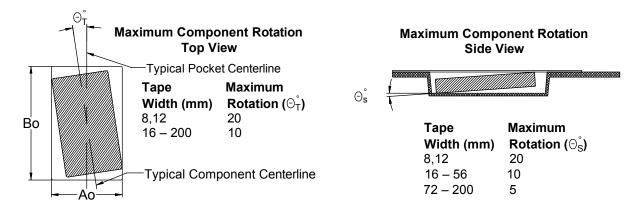


Figure 4 – Maximum Lateral Movement

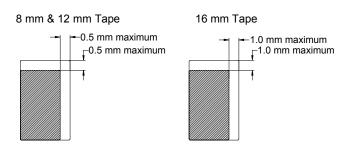


Figure 5 – Bending Radius

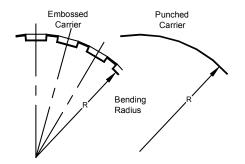
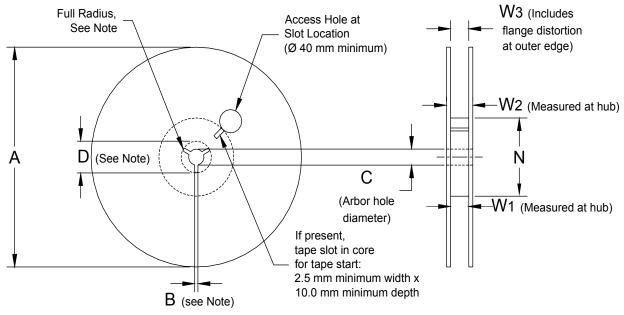




Figure 6 – Reel Dimensions



Note: Drive spokes optional; if used, dimensions B and D shall apply.

Table 8 – Reel Dimensions

Metric will govern

Constant Dimensions — Millimeters (Inches)								
Tape Size	A	B Minimum	С	D Minimum				
8 mm	178 ±0.20							
12 mm	(7.008 ±0.008) or	1.5 (0.059)	13.0 +0.5/-0.2 (0.521 +0.02/-0.008)	20.2 (0.795)				
16 mm	330 ±0.20 (13.000 ±0.008)							
	Variable Dimensions — Millimeters (Inches)							
Tape Size	N Minimum	W ₁	W ₂ Maximum	W ₃				
8 mm		8.4 +1.5/-0.0 (0.331 +0.059/-0.0)	14.4 (0.567)					
12 mm	50 (1.969)	12.4 +2.0/-0.0 (0.488 +0.078/-0.0)	18.4 (0.724)	Shall accommodate tape width without interference				
16 mm		16.4 +2.0/-0.0 (0.646 +0.078/-0.0)	22.4 (0.882)					



Figure 7 – Tape Leader & Trailer Dimensions

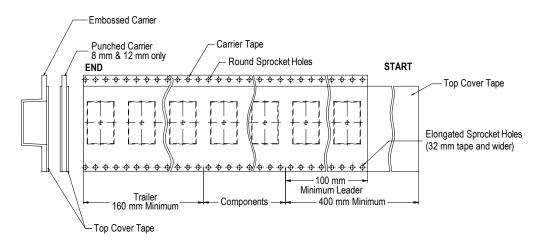
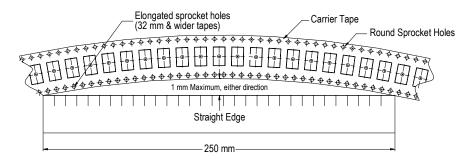


Figure 8 – Maximum Camber



Surface Mount Multilayer Ceramic Chip Capacitors (SMD MLCCs) Commercial "L" Series, SnPb Termination, C0G Dielectric, 10 – 250 VDC (Commercial Grade)



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