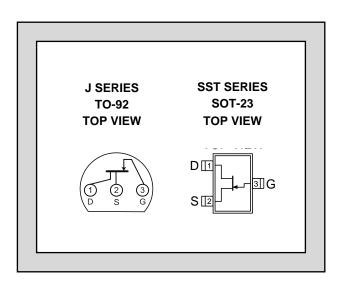
LINEAR SYSTEMS

Twenty-Five Years Of Quality Through Innovation

J/SST201 SERIES

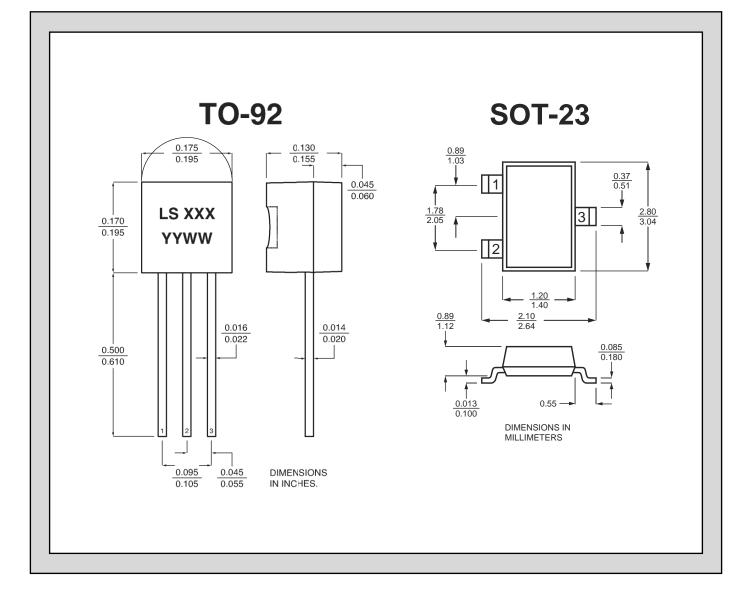
HIGH GAIN N-CHANNEL JFET AMPLIFIER

FEATURES					
DIRECT REPLACEMENT FOR SILICONIX J/SST201 SERIES					
LOW CUTOFF VOLTAGE	$V_{GS(off)} \leq 1.5V$				
HIGH GAIN	$A_V = 80 V/V$				
ABSOLUTE MAXIMUM RATINGS ¹					
@ 25 °C (unless otherwise stated)					
Maximum Temperatures					
Storage Temperature	-55 to +150 °C				
Operating Junction Temperature	-55 to +150 °C				
Maximum Power Dissipation					
Continuous Power Dissipation T _A =25°C	350mW				
Maximum Current					
Forward Gate Current	50mA				
Maximum Voltages					
Gate to Drain Voltage	-40V				
Gate to Source Voltage	-40V				



ELECTRICAL CHARACTERISTICS @ 25 °C (unless otherwise stated)

SYMBOL	CHARACTERISTIC		MIN	TYP	MAX	UNITS	CONDITIONS
BV _{GSS}	Gate to Source Breakdown Voltage	J/SST201, 202	-40				$I_G = -1\mu A, V_{DS} = 0V$
		J/SST204	-25				
$V_{GS(off)}$	Gate to Source Cutoff J/SST202	J/SST201	-0.3		-1.5	V	V _{DS} = 15V, I _D = 10nA
		J/SST202	-0.8		-4		
		J/SST204	-0.2		2		
I _{DSS}	Drain to Source Saturation Current ²	J/SST201	0.2		1	mA	$V_{DS} = 15V, V_{GS} = 0V$
		J/SST202	0.9		4.5		
		J/SST204	0.2		3		
lgss	Gate Reverse Current				-100		$V_{GS} = -20V, V_{DS} = 0V$
lg	Gate Operating Current			-2		pА	$V_{DG} = 10V, I_D = 0.1mA$
I _{D(off)}	Drain Cutoff Current			2			$V_{DS} = 15V, V_{GS} = -5V$
g fs	Forward Transconductance	J/SST201, 204	0.5			mS	$V_{DS} = 15V, V_{GS} = 0V, f = 1kHz$
		J/SST202	1				
Ciss	Input Capacitance			4.5		ъĘ	V _{DS} = 15V, V _{GS} = 0V, <i>f</i> = 1MHz
Crss	Reverse Transfer Capacitance			1.3		pF	
en	Noise Voltage			6		nV/√Hz	$V_{DS} = 10V$, $V_{GS} = 0V$, $f = 1kHz$



NOTES

- 1. Absolute maximum ratings are limiting values above which serviceability may be impaired.
- 2. Pulse Test: PW \leq 300µs, Duty Cycle \leq 3%
- 3. All characteristics MIN/TYP/MAX numbers are absolute values. Negative values indicate electrical polarity only.

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Linear Integrated Systems (LIS) is a 25-year-old, third-generation precision semiconductor company providing high-quality discrete components. Expertise brought to LIS is based on processes and products developed at Amelco, Union Carbide, Intersil and Micro Power Systems by company President John H. Hall. Hall, a protégé of Silicon Valley legend Dr. Jean Hoerni, was the director of IC Development at Union Carbide, Co-Founder and Vice President of R&D at Intersil, and Founder/President of Micro Power Systems.

