MORNSUN®

A_D-2WR2 & B_D-2WR2 SERIES

2W, FIXED INPUT, ISOLATED & UNREGULATED DUAL/SINGLE OUTPUT DC-DC CONVERTER

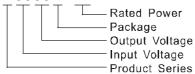




Patent Protected RoHS

PART NUMBER SYSTEM

A0505D-2WR2



FEATURES

- •Miniature DIP package
- Efficiency up to 85%
- High power density
- 1500VDC isolation
- Operating temperature range: -40°C~+85°C
- No external component required
- Industry standard pinout

APPLICATIONS

The A_D-2WR2 & B_D-2WR2 Series are designed for application where isolated output is required from a distributed power system.

These products apply to where:

- 1) Input voltage rang :±10%Vin;
- 2) 1500VDC input and output isolation;
- 3) Regulated and low ripple noise is not required.

Such as: digital circuits, low frequency analog circuits, and relay drive circuit.

	Input Voltage(VDC)	Output		Current (A)	Input C (mA)(Reflected Ripple	Max. Capacitive	Efficiency
Model	Nominal (Range)	Voltage (VDC)	Max.	Max.	@Max. Load	@No Load	Current (mA,Typ.)	Load ^① (µF)	(%, Typ.) @Max. Load
B0305D-2WR2	3.3 (2.97-3.63)	5	400	40	757	40		220	80
A0505D-2WR2		±5	±200	±20	500				80
A05X7D-2WR2		±7	±142	±15	500				80
A0509D-2WR2		±9	±111	±11	482			100	83
A0512D-2WR2	5	±12	±83	±8	476	25			84
A0515D-2WR2	(4.5-5.5)	±15	±67	±7	476	20			84
B0505D-2WR2		5	400	40	500				80
B0512D-2WR2		12	167	17	476			220	84
B0515D-2WR2		15	133	13	476				84
A1205D-2WR2		±5	±200	±20	205				81
A1209D-2WR2		±9	±111	±11	198		15	100	84
A1212D-2WR2		±12	±83	±8	196		-	100	85
A1215D-2WR2	12 (10.8-13.2)	±15	±67	±7	198	15			84
B1205D-2WR2		5	400	40	205				81
B1212D-2WR2		12	167	17	198			220	84
B1224D-2WR2		24	83	8	198				84
A2405D-2WR2		±5	±200	±20	101				82
A2409D-2WR2		±12	±83	±8	99	8		100	84
A2415D-2WR2	24	±15	±67	±7	99				84
B2405D-2WR2	(21.6-26.4)	5	400	40	101				82
B2409D-2WR2		9	222	22	99	8		220	84
B2412D-2WR2		12	167	17	99				84

INPUT SPECIFICATIONS									
Item	Test Conditions	Min.	Тур.	Max.	Unit				
	3.3VDC input	-0.7		5	VDC				
Input Surge Voltage (1000 may)	5VDC input	-0.7		9					
Input Surge Voltage (1sec.max.)	12VDC input	-0.7		18	VDC				
	24VDC input	-0.7		30]				
Input Filter			Capacitar	nce Filter					

Item	Test Conditions		Min.	Тур.	Max.	Unit		
Output Voltage Accuracy					See tolerance envelope curve			
Line Regulation	For Vin change of ±1%	6			±1.2			
		5V output		12		%		
	10% to 100% load	9V output		10				
Load Degulation		9V output		9				
Load Regulation		12V output		8				
		15V output		7				
		24V output		6	<u></u>			
Temperature Drift	100% load				±0.03	%/°C		
Dinale 9 Neige*	20MHz bandwidth	Output Voltage≤12V		60	-	mVp-p		
Ripple & Noise*		Output Voltage:15V,24V		75				
Short Circuit Protection**			λ		1	s		

 1.*Ripple and noise tested by "parallel cable" method. See detailed operation instructions at DC-DC Application Notes. 2.**Supply voltage must be discontinued at the end of short circuit duration.

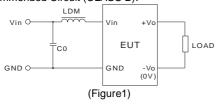
COMMON SPECIFICATIONS								
Item	Test Conditions	Min.	Тур.	Max.	Unit			
Isolation Voltage	ation Voltage Input-Output, tested for 1 minute and leakage current less than 1 mA				VDC			
Isolation Resistance	Input-Output, test at 500VDC	1000			ΜΩ			
Isolation Capacitance	Input-Output,100KHz/0.1V		20		pF			
Switching Frequency	100% load, Input voltage range		100	300	KHz			
MTBF	MIL-HDBK-217F@25℃	3500			K hours			
Case Material	al			JL94-V0)				
Weight			2.4		g			

ENVIRONMENTAL SPECIFICATIONS								
Item	Test Conditions	Min.	Тур.	Max.	Unit			
Storage Humidity	Non condensing			95	%			
Operating Temperature	Power derating (above 85℃, see Figure 2)	-40		85				
Storage Temperature		-55		125	°C			
Temp. rise at full load	Ta=25°C		25					
Lead Temperature	1.5mm from case for 10 seconds			300				
Cooling			Free air c	onvection				

EMC SPECIFICATIONS								
EMI	CE		CISPR22/EN55022 CLASS B (Recommended Circuit Refer to Figure 1)					
EIVII	RE		CISPR22/EN55022 CLASS B (Recommended Circuit Refer to Figure 1)					
EMS	ESD	A_D-2WR2	IEC/EN61000-4-2 Contact ±6KV perf. Criteria B					
EWS	ESD	B_D-2WR2	IEC/EN61000-4-2 Contact ±8KV perf. Criteria B					

EMC RECOMMENDED CIRCUIT

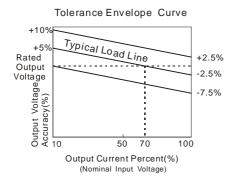
EMI Typical Recommended Circuit (CLASS B):

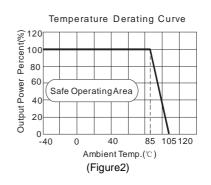


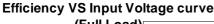
Recommended external circuit parameters:

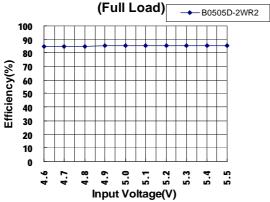
	Vin(V)	5/12/24
EMI	C0	4.7µF /50V
⊏IVII	LDM	6.8µH

PRODUCT TYPICAL CURVE

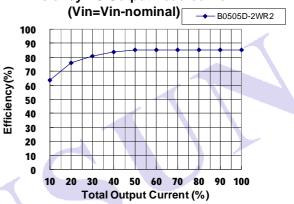




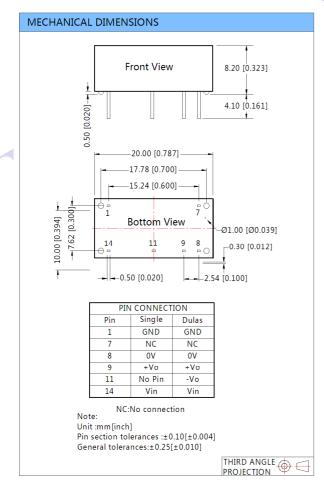


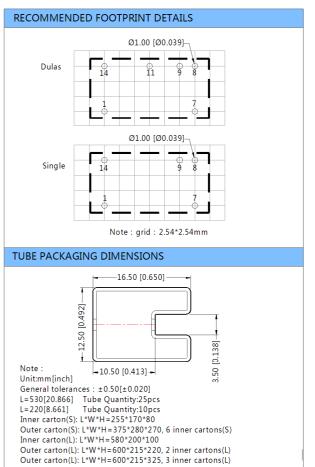


Efficiency VS Output Load curve



DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING

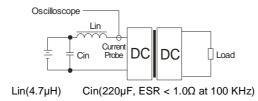




TEST CONFIGURATIONS

Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.



DESIGN CONSIDERATIONS

1) Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load is not less than 10% of the full load. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, or use our company's products with a lower rated output power.

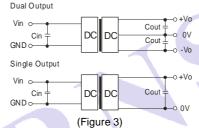
2) Overload Protection

Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is to add a circuit breaker to the circuit.

3) Recommended Circuit

If you want to further decrease the input/output ripple, a capacitor filtering network may be connected to the input and output ends of the DC/DC converter, see (Figure 3).

It should also be noted that the capacitance of filter capacitor must be proper. If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the recommended capacitance of its filter capacitor sees (Table 1).



EXTERNAL CAPACITOR TABLE (Table 1)

	Vin (VDC)	Cin (µF)	Single Vout (VDC)	Cout (µF)	Dual Vout (VDC)	Cout [#] (µF)
Į	3.3	4.7	5	10	±5	4.7
	5	4.7	9	4.7	±7/9	2.2
	12	2.2	12	2.2	±12	1
	24	1	15/24	1	±15	0.47

Note: # for each output. It's not recommended to connect any external capacitor in the application field with less than 0.5 watt output.

4) The input and the output of the product are recommended to be connected to ceramic capacitor or electrolytic capacitor. Using tantalum capacitor may cause risk of failure

5) It is not recommended to increase the output power capability by connecting two or more converters in parallel. The product is not hot-swappable

Note

- 1. Operation under minimum load will not damage the converter; However, they may not meet all specifications.
- 2. Max. Capacitive Load is tested at nominal input voltage and full load.
- 3. Unless otherwise noted, All specifications are measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load.
- 4. In this datasheet, all test methods are based on our corporate standards.
- 5. All characteristics are for listed models, and non-standard models may perform differently. Please contact our technical support for more detail.
- 6. Please contact our technical support for any specific requirement.
- 7. Specifications of this product are subject to changes without prior notice.

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