

100W isolated DC-DC converter, Ultra-wide input and regulated single output





FEATURES

- Ultra-wide 4:1 input voltage range
- High efficiency up to 90%
- I/O isolation test voltage: 2.25k VDC
- Input under-voltage protection, output short-circuit, over-current, over-voltage, over-temperature protection
- Operating ambient temperature range:
 -40° to +85°
- Five-sided metal shielded package
- Industry standard ¼-Brick package and pin-out
- EN62368 approved
- Meet UL62368, IEC62368, EN50155 standards





URF24_QB -100W(F/H)R3 series of isolated 100W DC-DC products with 4:1 input voltage. They feature efficiency up to 90%, 2250VDC input to output isolation, operating ambient temperature of -40°C to +85°C, input under-voltage, output over-voltage, short-circuit, over-current protection, over-temperature protection. The products meet CLASS B of CISPR32/EN55032 EMI standards by adding the recommended external components, and they are widely used in applications such as battery powered systems, industrial controls, electricity, instrumentation, railway, communication.

Selection Guide								
		Input Voltage (VDC)		Output		Full Load	Capacitive	
Certification	Part No. [®]	Nominal (Range)	Max. ²	Voltage (VDC)	Current (A) Max.	Efficiency (%) Min./Typ.	Load (µF) Max.	
	URF2405QB-100W(F/H)R3			5	20	87/89	6000	
	URF2412QB-100W(F/H)R3	24	40	12	8.3	88/90	2000	
CF.	URF2415QB-100W(F/H)R3			15	6.7	88/90	2000	
CE	CE URF2424QB-100W(F/H)R3	(9-36)	40	24	4.2	88/90	1000	
	URF2428QB-100W(F/H)R3			28	3.6	88/90	1000	
	URF2448QB-100W(F/H)R3			48	2.1	88/90	470	

Note:

①Use"F"suffix is for added aluminum baseplate and "H" suffix for heat sink mounting. We recommend to choose modules with a heat sink for enhanced heat dissipation and applications with extreme temperature requirements;

2) Exceeding the maximum input voltage may cause permanent damage.

Input Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
Input Current (full load/no-load)	Nominal input voltage		4682/120	4789/160	4
Reflected Ripple Current	Nominal input voltage		30	-	mA
Surge Voltage (1sec. max.)		-0.7	_	50	
Start-up Voltage		-	_	9	VDC
Under-voltage Protection		7.0	7.5	_	
Input Filter			Pi filter		
	Module on	Ctrl pir	Ctrl pin open or pulled high (3.5-12VDC)		
Ctrl*	Module off Ctrl pin pulled low to GND (0-			GND (0-1.2)	VDC)

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DC/DC Converter URF24_QB-100W(F/H)R3 Series



Ctrl*	Input current when off	_	2	10	mA
Hot Plug		Unavailable			
Note: *The Ctrl pin voltage is referenced to input GND.					

Item	Operating Conditions		Min.	Тур.	Max.	Unit
Voltage Accuracy	0%-100% load			±1	±3	
Linear Regulation	Input voltage variation from	n low to high at full load		±0.2	±0.5	%
Load Regulation	5%-100% load			±0.5	±0.75	
Transient Recovery Time	25% load step change			200	500	μs
Transient Response Deviation	050/ 1 1	5V output	-	±3	±7.5	%
	25% load step change	Others	-	±3	±5	
Temperature Coefficient	Full load				±0.03	%/ °C
Discola O Naisas	000 41 le le eur et d'elle	12V/15V output		100	200	
Ripple & Noise*	20MHz bandwidth	Others	-	130	250	mVp-r
Over-voltage Protection			110	125	160	%Vo
Over-current Protection	Input voltage range		110	125	150	%lo
Short-circuit Protection			Hiccup, continuous, self-recovery			very

General Specifications						
Item	Operating Conditions	S	Min.	Тур.	Max.	Unit
	Input-output	Electric Strength Test for 1	2250	-	_	
Isolation	Input-case	minute with a leakage	1600	-		VDC
	Output-case	current of 1mA max.	500	-		
Insulation Resistance	Input-output resistance	ce at 500VDC	100	-		ΜΩ
Isolation Capacitance	Input-output capacit	ance at 100KHz/0.1V		2200	_	рF
Tulun	5V, 15V output		91	-	110	
Trim	Others		90	-	110	%Vo
Sense	See remote sense ap	See remote sense application		-	110	
	Natural convection	URF24_QB-100WR3		-	8	°C/W
Thermal Resistance		URF24_QB-100WFR3		-	6.8	
		URF24_QB-100WHR3		-	5.7	
Operating Temperature			-40	-	+85	
Storage Temperature			-55	-	+125	
Over-temperature Protection	Max. case temperatu	ure		115	120	·c
	Wave-soldering, 10 se	econds			260	
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds			-	300	
Storage Humidity	Non-condensing		5	-	95	%RH
Vibration			IEC/EN	N61373 - Cate	gory 1, Gra	de B
Switching Frequency	PWM mode		-	250	-	KHz
MTBF	MIL-HDBK-217F@25°C		500			K hours

Mechanic	Mechanical Specifications					
Case Material		Aluminum alloy case; Black plastic bottom, flame-retardant and heat-resistant (UL94 V-0)				
Dimondona	URF24xxQB-100WR3	61.8 x 40.2 x 12.7 mm				
Dimensions	URF24xxQB-100WFR3	62.0 x 56.0 x 14.6 mm				

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DC/DC Converter URF24_QB-100W(F/H)R3 Series

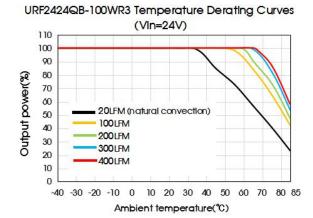


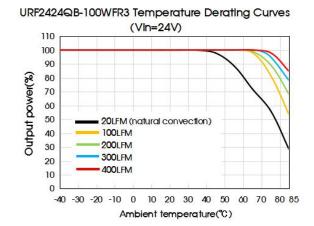
Dimensions	URF24xxQB-100WHR3	61.8 x 40.2 x 27.7 mm
URF24xxQB-100WR3 86.0g(Typ.)		86.0g(Typ.)
Weight	URF24xxQB-100WFR3	106.0g(Typ.)
URF24xxQB-100WHR3		117.0(Typ.)
Cooling Method		Natural air convection or forced convection

Electromagnetic Compatibility (EMC)						
Emissions	CE CISPR32/EN55032 CLASS A and CLASS B (see Fig. 3 for recommended circuit)					
ETHISSIONS	RE	CISPR32/EN55032	CLASS A and CLASS B (see Fig. 3 for recommended circuit)			
	ESD	IEC/EN61000-4-2	Contact ±6KV Air ±8KV	perf.Criteria B		
	RS	IEC/EN61000-4-3	20V/m	perf.Criteria A		
Immunity EFT IEC/EN61000-4-4		IEC/EN61000-4-4	±2KV(see Fig. 2 for recommended circuit)	perf.Criteria A		
CS		IEC/EN61000-4-6	10 Vr.m.s	perf.Criteria A		

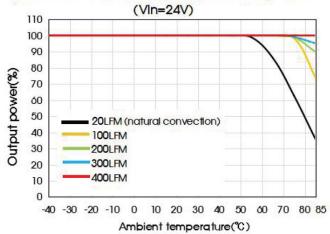
Electromo	Electromagnetic Compatibility (EMC) (EN50155)					
Emissions	CE	EN50121-3-2 EN55016-2-1	150kHz-500kHz 99dBuV (see Fig.3 for recommended circuit) 500kHz-30MHz 93dBuV			
ETTISSIOTIS	RE	EN50121-3-2 EN55016-2-1	30MHz-230MHz 40dBuV/m at 10m (see Fig.3 for recommended circuit) 230MHz-1GHz 47dBuV/m at 10m			
	ESD	EN50121-3-2	Contact ±6KV/Air ±8KV			
	RS	EN50121-3-2	80MHz-800MHz 20V/m(rms)			
Immunity	EFT	EN50121-3-2	±2kV 5/50ns 5kHz (see Fig.2 for recommended circuit)			
	Surge	EN50121-3-2	line to line ± 1 KV (42 Ω 0.5uF see Fig.2 for recommended circuit)			
	CS	EN50121-3-2	0.15MHz-80MHz 10V(rms)			

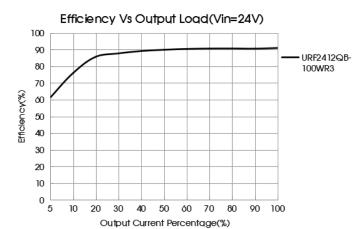
Typical Characteristic Curves

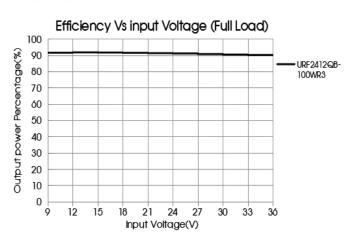










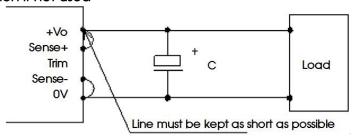


Notes:

1) Product application thermal design should be referred to the recommended PCB layout and recommended heat dissipation structure, please see DC-DC Converter Application Notes for specific operation.

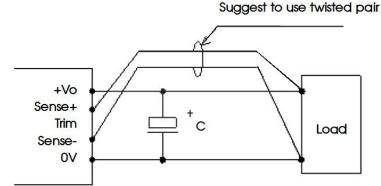
Remote Sense Application

1. Remote Sense Connection if not used



- (1) If the sense function is not used for remote regulation the user must connect the +Sense to + Vo and -Sense to 0V at the DC-DC converter pins and will compensate for voltage drop across pins only.
- (2) The connections between sense lines and their respective power lines must be kept as short as possible, otherwise they may be picking up noise, interference and/or causing unstable operation of the power module.

2. Remote Sense Connection used for Compensation

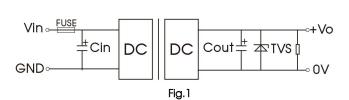


- (1) Using remote sense with long wires may cause unstable output, please contact technical support if long wires must be used.
- (2) PCB-tracks or cables/wires for Remote Sense must be kept as short as possible. Twisted pair or shielded wairs are suggested for remote compensation and must be kept as short as possible.
- (3) We recommend using adequate cross section for PCB-track layout and/or cables to connect the power supply module to the load in order to keep the voltage drop below 0.3V and to make sure the power supply's output voltage remains within the specified range.
- (4) Note that large wire impedance may cause oscillation of the output voltage and/or increased ripple. Consult technical support or factory for further advice of sense operation.

Design Reference

1. Typical application

- (1) We recommended using the recommended circuit shown in Fig.1 during product testing and application, otherwise please ensure that at least a 220µF electrolytic capacitors is connected at the input in order to ensure adequate voltage surge suppression and protection.
- (2) We recommended increasing the value of Cin and pay attention to the unstable input voltage if the product input side is paralleled with motor drive circuit and/or larger energy transient circuits, to ensure the stablity of input terminal and avoid repeatedly start-up problems due to input voltage lower than undervoltage protection point.
- (3) We recommended increasing the output capacitance with limited to the capacitive load specification and/or increasing the voltage clamping circuit(such as TVS) if the output terminal is inductive device such as relay or a motor, to ensure adequate voltage surge suppression and protection.
- (4) Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values Cin and Cout and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the specified max. capacitive load value of the product.



Vout(VDC)	Fuse	Cin	Cout	TVS
5			470µF	SMDJ7.0A
12			220µF	SMDJ15A
15	20A, slow blow	220µF	ΖΖΟμΓ	SMDJ18A
24		220µF		SMDJ30A
28			100µF	SMDJ36A
48				SMDJ64A

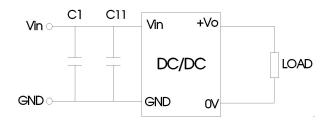
Note:

*Please pay attention to the ambient temperature of the product when using an external capacitor, increase the electrolytic capacitor values to at least 1.5 times the original parameter if the ambient temperature is low(such as $-25\,^{\circ}$ C).



2. EMC compliance circuit

We recommended using the recommended circuit shown in Fig.2 during product EMC testing and application.



Capacitor	Recommended value	Function
Cl	150 µ F electrolytic capacitor	Meets EFT and
C11	47 μ F electrolytic capacitor	surge

Fig. 2

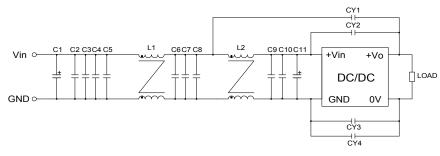
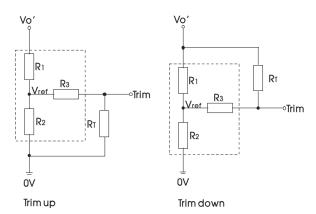


Fig. 3

CLASS A components	CLASS B components Recommended compone		Function
C1		150 μ F electrolytic capacitor	
C11		47 μ F electrolytic capacitor	1
C2, C3, C4, C5, C6, C7, C8, C9, C10		10 μ F ceramic capacitor	Meets conducted
Lī	, L2	1.6mH common mode inductor	emission and radiated emission
0)/0	CY1, CY2	2.2nF Y1safety capacitor	
CY3	CY3, CY4	1nF Y1safety capacitor	

3. Trim Function for Output Voltage Adjustment (open if unused)



TRIM resistor connection (dashed line shows internal resistor network)

Calculation formula of Trim resistance:

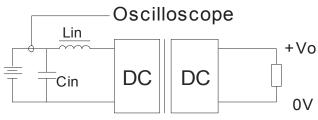
up:
$$RT = \frac{aR_2}{R_2 - a} - R_3$$
 $a = \frac{Vref}{Vo' - Vref} \cdot R_1$
down: $RT = \frac{aR_1}{R_1 - a} - R_3$ $a = \frac{Vo' - Vref}{Vref} \cdot R_2$

 R_T = Trim Resistor value; a = self-defined parameter Vo'= desired output voltage (±10% max.)

Vout(VDC)	R1(KΩ)	R2(KΩ)	R3(KΩ)	Vref(V)
5	3.036	3	10	2.5
12	11.00	2.87	15	2.5
15	14.03	2.8	15	2.5
24	24.872	2.87	15	2.5
28	29.201	2.851	15	2.5
48	53.017	2.894	15	2.5

Note: If the Trim pin is shorted with "+Vo", or its value is too low, then the output voltage Vo' would be lower than 0.90Vo, which may cause permanent damage.

4. Reflected ripple current--test circuit

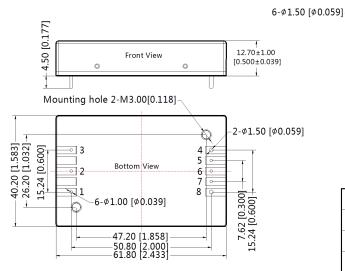


Note:Lin(4.7 μ H) , Cin(220 μ F, ESR < 1.0 Ω at 100 KHz)

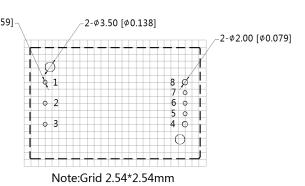
- 5. The products do not support parallel connection of their output.
- 6. For additional information please refer to application notes on www.mornsun-power.com

Dimensions and Recommended Layout (URF24xxQB-100WR3)





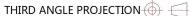
Note:
Unit: mm[inch]
Pin1, 2, 3, 5, 6, 7's diameter: 1.00[0.039]
Pin4, 8's diameter: 1.50[0.059]
Pin diameter tolerances: ±0.10[±0.004]
General tolerances: ±0.50[±0.020]
Mounting hole screwing torque: Max 0.4 N·m



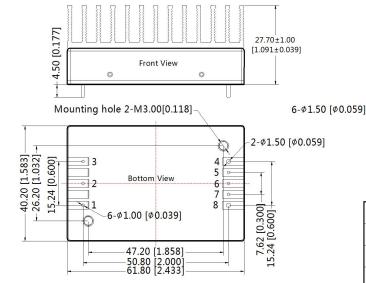
Pin-Out						
Pin	Function	Pin	Function			
1	+Vin	5	Sense-			
2	Ctrl	6	Trim			
3	-Vin	7	Sense+			
4	0V	8	+Vo			



Dimensions and Recommended Layout (URF24xxQB-100WHR3)







2-\$\psi_2.00 [\$\psi_0.079] o 0 0 3

2-\$\phi 3.50 [\$\phi 0.138]

Note:Grid 2.54*2.54mm

Pin-Out						
Pin	Function	Pin	Function			
1	+Vin	5	Sense-			
2	Ctrl	6	Trim			
3	-Vin	7	Sense+			
4	0V	8	+Vo			

Note:

Unit: mm[inch]

Pin1, 2, 3, 5, 6, 7's diameter: 1.00[0.039]

Pin4, 8's diameter: 1.50[0.059]

Unit: mm[inch]

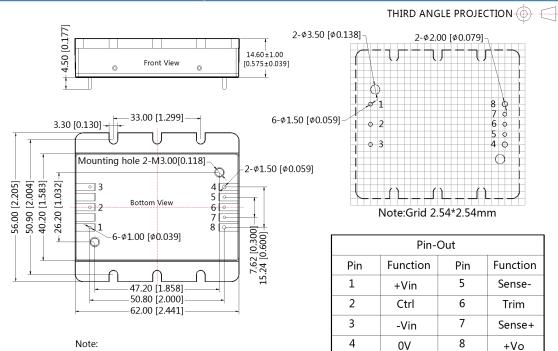
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General tolerances: $\pm 0.50[\pm 0.020]$

Mounting hole screwing torque: Max 0.4 N·m

Dimensions and Recommended Layout(URF24xxQB-100WFR3)



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Note:

- 1. For additional information on Product Packaging please refer to www.mornsun-power.com. Packaging bag number: 58010113(URF24xxQB-100WR3), 58200069(URF24xxQB-100WFR3), 58220017(URF24xxQB-100WHR3);
- 2. The maximum capacitive load offered were tested at input voltage range and full load;
- 3. Unless otherwise specified, data in this datasheet should be tested under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated load;
- 4. All index testing methods in this datasheet are based on our company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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