

Ultra low capacitance bidirectional ESD protection diode14 February 2014Product data sheet

### 1. General description

Ultra low capacitance bidirectional ElectroStatic Discharge (ESD) protection diode in a DFN1006-2 (SOD882) ultra small and leadless Surface-Mounted Device (SMD) plastic package designed to protect one signal line from the damage caused by ESD and other transients.

### 2. Features and benefits

- Ultra low diode capacitance C<sub>d</sub> = 0.30 pF
- High reverse standoff voltage V<sub>RWM</sub> = 24 V
- Ultra low leakage current: I<sub>RM</sub> = 1 nA
- ESD protection up to 10 kV; IEC 61000-4-2
- AEC-Q101 qualified

### 3. Applications

- NFC antenna protection
- Protection of high-speed data lines

### 4. Quick reference data

Table 1. Qui	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V	[1]	0.23	0.3	0.45	pF
V <sub>RWM</sub>	reverse standoff voltage			-	-	24	V

[1] Measured from pin 1 to pin 2.



# 5. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K1	cathode (diode 1)		
2	K2	cathode (diode 2)		sym045
			Transparent top view	- ,
			DFN1006-2 (SOD882)	

# 6. Ordering information

Table 3. Ordering in	formation		
Type number	Package		
	Name	Description	Version
PESD24VF1BL	DFN1006-2	DFN1006-2: leadless ultra small plastic package; 2 terminals	SOD882

# 7. Marking

Table 4. Marking codes	
Type number	Marking code
PESD24VF1BL	7L

### 8. Limiting values

#### Table 5.Limiting values

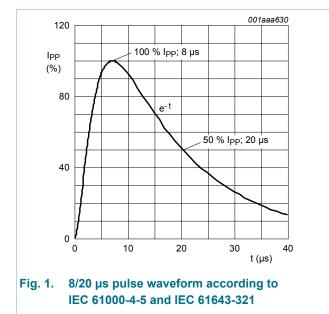
In accordance with the Absolute Maximum Rating System (IEC 60134).

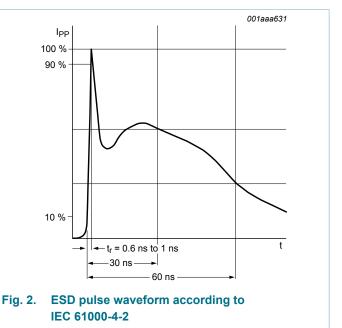
Symbol	Parameter	Conditions		Min	Мах	Unit
I <sub>PPM</sub>	peak pulse current	t <sub>p</sub> = 8/20 μs	[1][2]	-	1	А
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maxim	um ratings	1				
V <sub>ESD</sub>	electrostatic discharge voltage	IEC 61000-4-2 (air discharge)	[1][3]	-	15	kV
		IEC 61000-4-2 (contact discharge)	[1][3]	-	10	kV
		MIL-STD-883 (human body model)	[1]	-	10	kV

[1] Measured from pin 1 to pin 2.

[2] According to IEC 61000-4-5 and IEC 61643-321.

[3] Device stressed with ten non-repetitive ESD pulses.



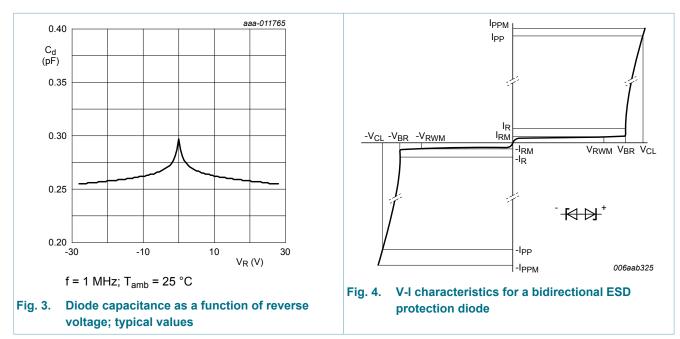


#### **Characteristics** 9.

Table 6. Characteristics							
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>RWM</sub>	reverse standoff voltage			-	-	24	V
I <sub>RM</sub>	reverse leakage current	V <sub>R</sub> = 24 V	[1]	-	1	30	nA
V <sub>CL</sub>	clamping voltage	I <sub>PP</sub> = 1 A	[1][2]	-	-	17	V
V <sub>BR</sub>	breakdown voltage	I <sub>R</sub> = 10 mA	[1]	24.5	28	31.5	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V	[1]	0.23	0.3	0.45	pF
R <sub>dyn</sub>	dynamic resistance	I <sub>R</sub> = 7.5 A	[3][1]	-	0.8	-	Ω

[1]

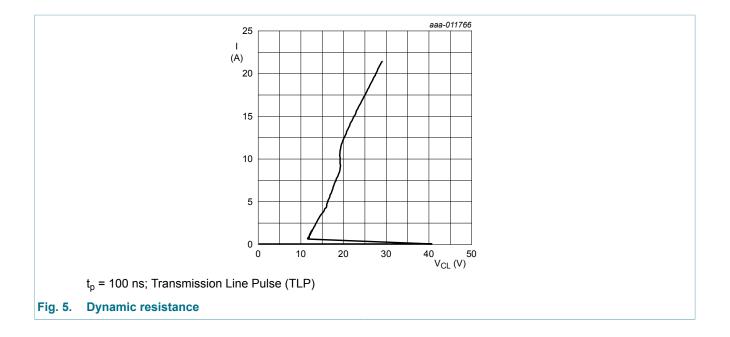
- Measured from pin 1 to pin 2. According to IEC 61000-4-5 and IEC 61643-321. [2]
- [3] Non-repetitive current pulse; Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008.



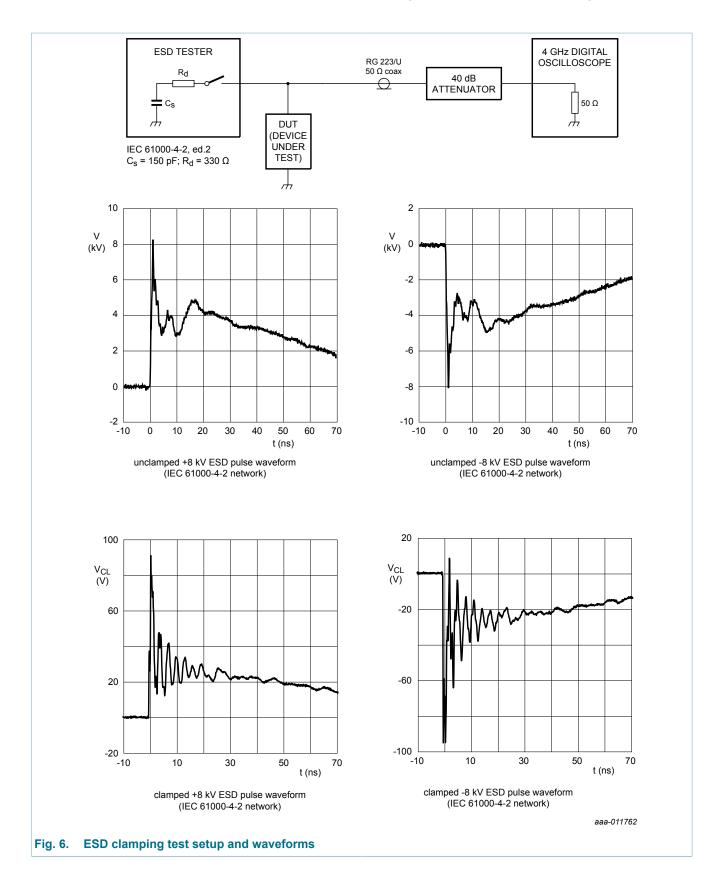
### Nexperia

# PESD24VF1BL

### Ultra low capacitance bidirectional ESD protection diode



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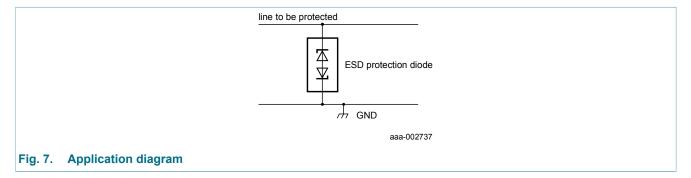
### **10. Test information**

### **10.1 Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

# **11. Application information**

The device is designed for the protection of one bidirectional data line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are both positive and negative with respect to ground.



#### Circuit board layout and protection device placement

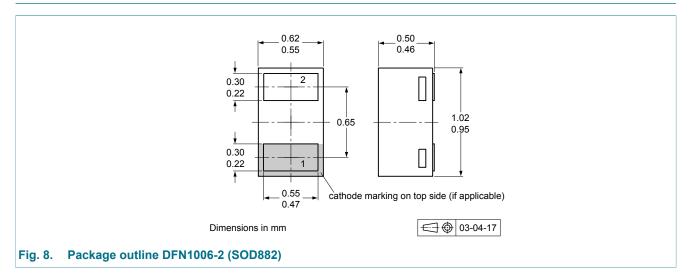
Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- 6. Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

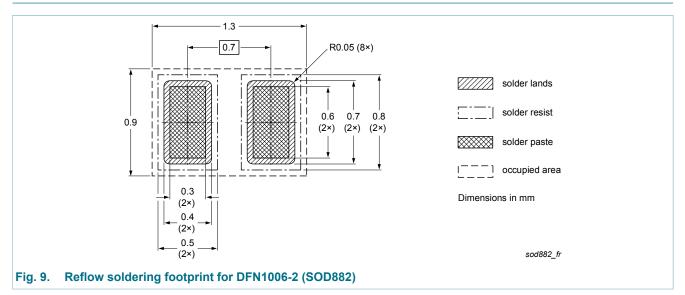
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#### Ultra low capacitance bidirectional ESD protection diode

### 12. Package outline



# 13. Soldering



# 14. Revision history

Table 7. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PESD24VF1BL v.1	20140214	Product data sheet	-	-		

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#### Ultra low capacitance bidirectional ESD protection diode

### **15. Legal information**

#### 15.1 Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.nexperia.com</u>.

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