

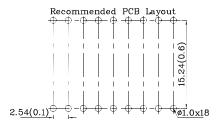
14.22mm (0.56") DUAL DIGIT NUMERIC DISPLAY

Features

- Low power consumption
- Robust package
- I.C. Compatible
- Standard configuration: Gray face w/ white segments
- Optional black face provides superior color contrast
- RoHS Compliant



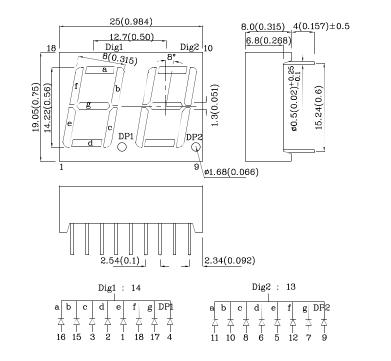






ATTENTION OBSERVE PRECAUTIONS FOR HANDLING ELECTROSTATIC DISCHARGE SENSITIVE DEVICES

Package Schematics



Notos

1. All dimensions are in millimeters (inches), Tolerance is $\pm 0.25(0.01")$ unless otherwise noted. 2. Specifications are subject to change without notice.

Absolute Maximum Ratings $(T_A=25^{\circ}C)$		Red (GaAlAs)	Unit	
Reverse Voltage	V_{R}	5	V	
Forward Current	I_{F}	30	mA	
Forward Current (Peak) 1/10 Duty Cycle 0.1ms Pulse Width	i_{FS} 155		mA	
Power Dissipation	P_{D}	75	mW	
Operating Temperature	T_{A}	-40 ~ +85	°C	
Storage Temperature	Tstg	-40 ~ +85		
Lead Solder Temperature [2mm Below Package Base]	260°C For 3-5 Seconds			

A Relative Humidity between 40% and 60% is recommended in ESD-protected work areas to reduce static build up during assembly process (Reference JEDEC/JESD625-A and JEDEC/J-STD-033)

Operating Characteristics (T _A =25°C)	Red (GaAlAs)	Unit	
Forward Voltage (Typ.) (I _F =10mA)	V_{F}	1.8	V
Forward Voltage (Max.) (I _F =10mA)	V_{F}	2.3	V
Reverse Current (Max.) (V _R =5V)	I_{R}	10	μA
Wavelength of Peak Emission CIE127-2007* (Typ.) (I _F =10mA)	λΡ	655*	nm
Wavelength of Dominant Emission CIE127-2007* (Typ.) $(I_F=10\text{mA})$	λD	640*	nm
Spectral Line Full Width At Half-Maximum (Typ.) $(I_F=10\text{mA})$	$\triangle \lambda$	20	nm
Capacitance (Typ.) (V _F =0V, f=1MHz)	С	45	pF

Wavelength

Part Number	Emitting Color	Emitting Material	CIE127-2007* (I _F =10mA) ucd		CIE127-2007* nm λP	Description
			min.	typ.		
XDMR14C2	Red	GaAlAs		27990 9990*	655*	Common Cathode, Rt.Hand Decimal.

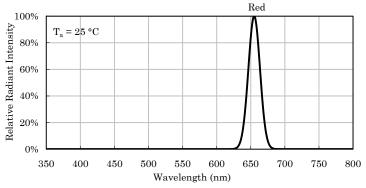
Luminous Intensity

XDSA1003 V9-X Layout: Maggie L.

^{*}Luminous intensity value and wavelength are in accordance with CIE127-2007 standards. Feb 20.2019

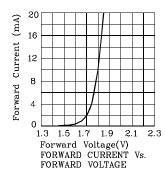


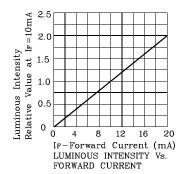


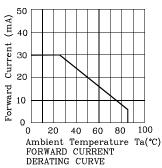


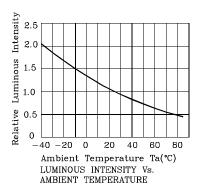
Relative Intensity Vs. CIE Wavelength

❖ Red

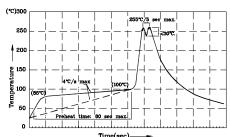








Wave Soldering Profile for Thru-Hole Products (Pb-Free Components)



- 1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C
 2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec
- 2.Peak wave soldering temperature between 245°C ~ 255°C for 3 secmax).
 3.Do not apply stress to the epoxy resin while the temperature is a 4.Fixtures should not incur stress on the component when mounting during soldering process.
 5.SAC 305 solder alloy is recommended.
 6.No more than one wave soldering pass.
 7.During wave soldering, the PCB top-surface temperature should be kept below 105°C.

Remarks:

If special sorting is required (e.g. binning based on forward voltage, luminous intensity / luminous flux, or wavelength),

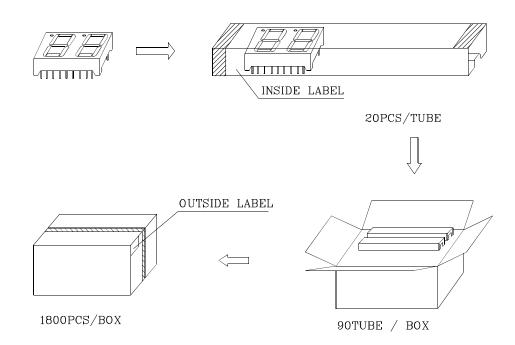
the typical accuracy of the sorting process is as follows:

- 1. Wavelength: +/-1nm
- 2. Luminous Intensity / Luminous Flux: +/-15%
- 3. Forward Voltage: +/-0.1V

Note: Accuracy may depend on the sorting parameters.



PACKING & LABEL SPECIFICATIONS





TERMS OF USE

- 1. Data presented in this document reflect statistical figures and should be treated as technical reference only.
- 2. Contents within this document are subject to improvement and enhancement changes without notice.
- 3. The product(s) in this document are designed to be operated within the electrical and environmental specifications indicated on the datasheet. User accepts full risk and responsibility when operating the product(s) beyond their intended specifications.
- 4. The product(s) described in this document are intended for electronic applications in which a person's life is not reliant upon the LED. Please consult with a SunLED representative for special applications where the LED may have a direct impact on a person's life.
- 5. The contents within this document may not be altered without prior consent by SunLED.
- 6. When any special process such as potting is required for LED assembly, please consult with SunLED representative before proceeding.
- 7. Additional technical notes are available at https://www.SunLEDusa.com/TechnicalNotes.asp

Feb 20,2019 XDSA1003 V9-X Layout: Maggie L.