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LM113QML Reference Diode

Check for Samples: LM113QML

FEATURES

- Low Breakdown Voltage: 1.220V
- Dynamic Impedance of 0.3Ω from 500 µA to 20 mA
- Temperature Stability Typically 1% over-55°C to 125°C Range
- Tight Tolerance: ±5% or ±1%
 - The characteristics of this reference recommend it for use in bias-regulation circuitry, in low-voltage power supplies or in battery powered equipment. The fact that the breakdown voltage is equal to a physical property of silicon—the energyband gap voltage—makes it useful for many temperature-compensation and temperature-measurement functions.

DESCRIPTION

The LM113 are temperature compensated, low voltage reference diodes. They feature extremely-tight regulation over a wide range of operating currents in addition to an unusually-low breakdown voltage and good temperature stability.

The diodes are synthesized using transistors and resistors in a monolithic integrated circuit. As such, they have the same low noise and long term stability as modern IC op amps. Further, output voltage of the reference depends only on highly-predictable properties of components in the IC; so they can be manufactured and supplied to tight tolerances.

Connection Diagrams

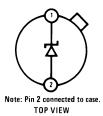


Figure 1. 2-Pin PFM See NDU0002A Package

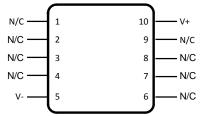
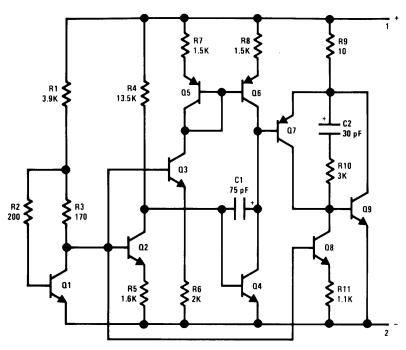


Figure 2. 10-Pin CLGA See NAC0010A Package

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Schematic Diagram





These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

ABSOLUTE MAXIMUM RATINGS(1)

Power Dissipation (2)			100 mW		
Reverse Current			50 mA		
Forward Current			50 mA		
Storage Temperature F	Range		-65°C ≤ T _A ≤ +150°C		
Lead Temperature (So	ldering, 10	seconds)	300°C		
Maximum Junction Ter	nperature (T _{Jmax})	+150°C		
Operating Temperature Range			-55°C ≤ T _A ≤ +125°C		
Thermal Resistance	θ_{JA}	PFM (Still Air)	440°C/W		
		PFM (500LF / Min Air Flow)	TBD		
		CLGA (Still Air)	218°C/W		
		CLGA (500LF / Min Air Flow)	140°C/W		
	θ_{JC}	PFM	80°C/W		
		CLGA	27°C/W		
Package Weight		PFM	275mg		
		CLGA	220mg		
ESD Tolerance ⁽³⁾			4000V		

⁽¹⁾ Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not ensure specific performance limits. For ensured specifications and test conditions, see the Electrical Characteristics. Theensured specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{Jmax} (maximum junction temperature), θ_{JA} (package junction to ambient thermal resistance), and T_A (ambient temperature). The maximum allowable power dissipation at any temperature is $P_{Dmax} = (T_{Jmax} - T_A)/\theta_{JA}$ or the number given in the Absolute Maximum Ratings, whichever is lower. Human body model, 1.5K Ω in series with 100pF.

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Table 1. QUALITY CONFORMANCE

Mil-Std-883, Method 5005 - Group A				
Subgroup	Description	Temp (°C)		
1	Static tests at	+25		
2	Static tests at	+125		
3	Static tests at	-55		
4	Dynamic tests at	+25		
5	Dynamic tests at	+125		
6	Dynamic tests at	-55		
7	Functional tests at	+25		
8A	Functional tests at	+125		
8B	Functional tests at	-55		
9	Switching tests at	+25		
10	Switching tests at	+125		
11	Switching tests at	-55		
12	Settling time at	+25		
13	Settling time at	+125		
14	Settling time at	-55		

LM113 ELECTRICAL CHARACTERISTICS DC PARAMETERS

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
V	Zanar Valtaga	1 1 2 2		1.16	1.28	V	1
V_{ZR}	Zener Voltage	$I_R = 1 \text{ mA}$		1.157	1.283	V	2, 3
ΔV_{ZR}	Delta Zener Voltage	0.5mA ≤ I _R ≤ 20mA			15	mV	1
		0.5mA ≤ I _R ≤ 10mA			15	mV	2, 3
V _F	Forward Voltage Drop	I _F = 1mA			1.0	V	1, 2, 3
R _R	Reverse Dynamic Impedance	I _R = 1mA	See ⁽¹⁾		1.0	Ω	4
		I _R = 10mA			0.8	Ω	4

⁽¹⁾ Specified parameter, not tested.

LM113 ELECTRICAL CHARACTERISTICS DC DRIFT PARAMETERS

Delta Calculations performed on QMLV devices at Group B, Subgroup 5, only.

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
V_{ZR}	Zener Voltage	I _R = 1mA		-0.02	0.02	V	1

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LM113-1 ELECTRICAL CHARACTERISTICS DC PARAMETERS

Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
V	Zonor Voltogo	1 4 4		1.210	1.232	V	1
V_{ZR}	Zener Voltage	$I_R = 1 \text{ mA}$		1.206	1.234	V	2, 3
A) /	Dalla Zaran Vallana	0.5mA ≤ I _R ≤ 20mA			15	mV	1
ΔV_{ZR}	Delta Zener Voltage	0.5mA ≤ I _R ≤ 10mA			15	mV	2, 3
V_{F}	Forward Voltage Drop	I _F = 1mA			1.0	٧	1, 2, 3
D	Reverse Dynamic Impedance	I _R = 1mA	See ⁽¹⁾		1.0	Ω	4
R _R		I _R = 10mA			8.0	Ω	4

⁽¹⁾ Specified parameter, not tested.

LM113-1 Electrical Characteristics DC Drift Parameters

Delta Calculations performed on QMLV devices at Group B, Subgroup 5, only.

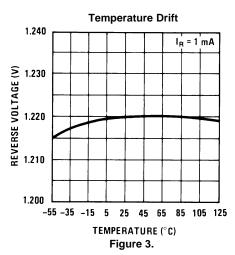
Symbol	Parameter	Conditions	Notes	Min	Max	Unit	Sub- groups
V_{ZR}	Zener Voltage	$I_R = 1 \text{mA}$		-0.02	0.02	V	1

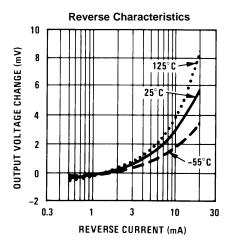
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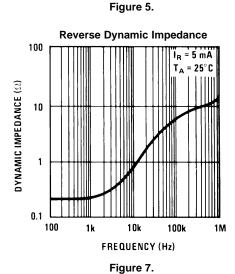


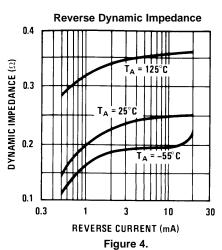
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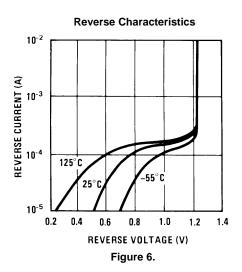
TYPICAL PERFORMANCE CHARACTERISTICS

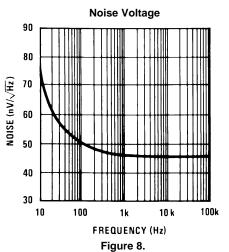






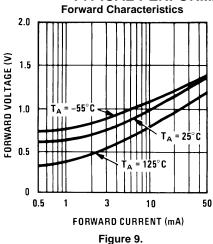


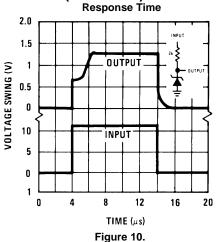


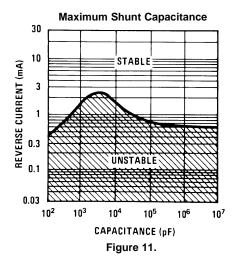




TYPICAL PERFORMANCE CHARACTERISTICS (continued) orward Characteristics Response Time









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TYPICAL APPLICATIONS

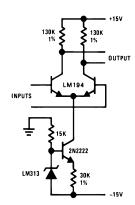


Figure 12. Amplifier Biasing for Constant Gain with Temperature

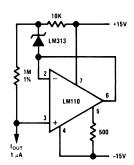
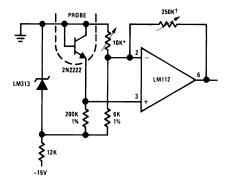


Figure 13. Constant Current Source



Adjust for 0V at 0°C Adjust for 100 mV/°C

Figure 14. Thermometer

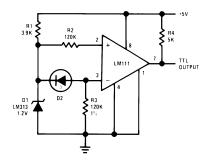
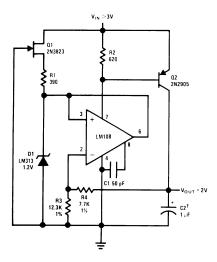


Figure 15. Level Detector for Photodiode



†Solid tantalum.

Figure 16. Low Voltage Regulator

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REVISION HISTORY

Released	Revision	Section	Changes
12/16/2010	A	•	2 MDS data sheets converted into one Corp. data sheet format. MDSs MNLM113-X Rev 1C1 and MNLM113-1-X Rev. 2A1 will be archived.

Product Folder Links: LM113QML

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