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3KV Hi-K CERAMIC DISC CAPACITOR

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	PRODUCT SPECIFICATION
J	PRODUCT: <u>CERAMIC DISC CAPACITOR</u> TYPE: 3KV HI-K CERAMIC CAPACITOR
	CUSTOMER:
	DOC. NO.: POE-D05-00-E-10
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IO.277,H JUANG Z	R : PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD. ONG MING ROAD,EASTERN SECTION, ZHOU ECONOMIC AND TECHNOLOGY PMENT ZONE,CHINA

2015/11/25

2016/3/3

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5.

1.

2.

3. Review 4. Marking

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		Record of change					
Date	rsio n						
2008.6.3	1	1. E13-00-E-06(before) \rightarrow POE-E05-00-E-01(1 st edition)					
2008.8.22	2	1. Revised diameter as below :					
		Before After					
		YP302272X140* not available	6				
		YP302332X140* not available					
		YP302362X150* not available					
		YP302392X150* not available					
		YP302472X170* not available					
		2. Remove H (inside kink lead) lead type for 3 KV.	13-14				
		3.Add last SAP code "H" for halogen and Pb free, epoxy resin.	2				
2008.12.12	3	1. Complete the 13 th to 17 th codes of SAP P/N.	4-5				
2008.12.12	5	 2. Page layout adjustment. 3. Added Marking when the coating resin is Halogen and Pb free Epoxy. 	4-5				
2009.8.19	4	1.Change PSA & POE logo to Walsin & POE logo.					
2010/9/9	5	 Review "but Dφ≤6.0 mm shall be omitted." To "but when the code of body diameter dimension ≤060 shall be omitted." 	7				
		2. Add date code on marking (item 7~12).	7				
2013/5/6	6	 Review the Lead diameter φ from 0.60 +/-0.06mm to 0.55+/-0.05mm Review the Solderability temperature from 255(+5/-0)°C to 245±5°C. Solderability time from 2±0.5s to 5±0.5s. 	5,6,8 10				
2013/10/18	7	 Review the packing specification Delete Z5U 3KV 822/103 Beview the term range V5B 25°C + 85°C Characteristics (25°C) 	11				
2015/8/4	8	 Review the temp range: Y5P(-25°C ~ +85°C)Change(-25°C ~ +125°C) review the high temperature loading: FOR 1000(+48/-0) HOURS AT 85 ± 2°C AND THEN DRIED FOR 24±2 HOURS AND MEASURED.Change FOR 1000(+48/-0) HOURS AT 125 ± 2°C AND THEN DRIED FOR 24±2 HOURS AND MEASURED. 	9 11				
		1. Add the YV(Y5V) type	4,6				
015/11/05		2. Delete the definition about "Old Part No."	7				

4. Review 6. Specification and test method:

Review 9. Drawing of internal structure and material list

Review the Available lead code of Lead Configuration.

Review 6. Specification and test method(add Pre-treatment):



7 9,10,11

14

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9-11

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7	Packing specification	12/14
8	Notices	13/14
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1. Part number for SAP system:

ΥP	302	102	K	090	В	20	С	7	В
(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)	(10)

(1)Temperature Characteristic : YP=Y5P, ZU=Z5U, YU=Y5U, YV=Y5V

(2)Rate Voltage : 302=3KVDC

(3)Rate Capacitance : ex. 221=220pF, 102=1000pF

(4)Tolerance of Capacitance : $K=\pm 10\%$, $M=\pm 20\%$

(5) Nominal body diameter dimension (Ref. to page.6 $D\phi$ Code spec.) .

(6)Lead Style : Refer to "2. Mechanical".

(7)Packing mode and lead length (identified by 2-figure code) :

Taping Code	Description	
AF	Box and Pitch : 15.0 mm	
AM	Box and Pitch : 25.4 mm	17
	ALL PILL	
Bulk Code	Description	行首之
3E	Lead length : 3.5mm	
04	Lead length : 4.0mm	
4E	Lead length : 4.5mm	
20	Lead length : 20.0mm	A
ength tolerance :	8 2 PASSIVE SYSTEM	ALLIANCE

(8)Length tolerance :

Code	Description
А	±0.5 mm
	(only for kink lead type)
В	±1.0 mm
С	MIN. MIN.
D	Taping special purpose

(9)Lead Pitch:

Code	Description
7	7.5±1 mm
0	10±1 mm

(10)Epoxy Resin Code:

Code	Description
В	Pb free, Epoxy Resin
Н	Halogen and Pb free , epoxy resin.

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2. Mechanical:

Available lead code (Epoxy Resin Coating)- (unit: mm)

Lead type	ad code (Epoxy SAP P/N	Pitch	Lead Length	Packing	Lead Configuration			
Lead type	(13-17)digits	(F)	(L)	Packing	Lead Configuration			
	B20C7	7.5 ± 1.0	20 MIN.		D max. T max.			
	B20C0	10 ± 1.0	20 MIN.	Bulk				
Lead style : B								
	BAFD7	7.5 ± 1.0						
Straight long			Refer to "5. Taping	T A				
lead	BAMD0	10 ± 1.0	format"	Tap. Ammo				
					Ø d+ +			
	L03B7	7.5 ± 1.0	3.0 ± 1.0		D max. T max.			
	L4EB7	7.5 ± 1.0	4.5 ± 1.0					
Lead style : L	L05B7	7.5 ± 1.0	5.0 ± 1.0					
Lead style · L	L10B7	7.5 ± 1.0	10.0 ± 1.0					
Straight short	L03B0	10 ± 1.0	3.0 ± 1.0	Bulk				
lead	L4EB0	10 ± 1.0	4.5 ± 1.0					
	L05B0	10 ± 1.0	5.0 ± 1.0					
	L10B0	10 ± 1.0	10.0 ± 1.0					
	X3EA7	7.5 ± 1.0	∃ 3.5 ± 0.5		D max. T max.			
	X04A7	7.5 ± 1.0	4.0 ± 0.5					
Lead style : X	X05B7	7.5 ± 1.0	5.0 ± 1.0	Bulk				
	X3EA0	10 ± 1.0	3.5 ± 0.5					
Outside kink	X04A0	10 ± 1.0	4.0 ± 0.5					
lead	X05B0	10 ± 1.0	5.0 ± 1.0		5 <u> </u>			
	XAFD7	7.5 ± 1.0	Refer to "5. Taping	Tap. Ammo				
	XAMD0	10 ± 1.0	SYSTEMATINCE	Tap. Annio				
	D3EA7	7.5 ± 1.0	3.5 ± 0.5	NC آرآ	D max. T max			
	D04A7	7.5 ± 1.0	4.0 ± 0.5	Bulk				
Lead style : D	D3EA0	10 ± 1.0	3.5 ± 0.5	Duik				
	D04A0	10 ± 1.0	4.0 ± 0.5	Sec. 1				
Vertical kink	DAFD7	7.5 ± 1.0	hology Co	2 Contraction of the second se				
short lead	DAMD0	10 ± 1.0	Taping SPEC.	Tap. Ammo				
Lead style : H	H3EA0	10.0±1.0	3.5±0.5 mm	Bulk	D max.			
Inside kink	HAFD0							
lead	HAMD0	Refer to "5.	Taping format"	Tap. Ammo				
Lead style : M	M04B7	7.5 ± 1.0	4.0 ± 1.0	Bulk	D max.			
Double outside kink lead	M04B0	10 ± 1.0	4.0 ± 1.0	DUIK				

* Lead diameter Φd: 0.55 +/-0.05mm

* e (Coating extension on leads): 3.0mmMax for straight lead lead style, not exceed the kink for kink lead.

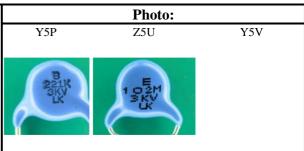
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3.	Capacitance	value vs.	Rate volt	age, produc	t diameter	:

T.C.	(CLA	ASS ∐, T T.	Y5P emperatu C.C.:±10	re:-25℃~ %)	.+85℃,	(C	Z5U / Y5U (CLASS II, Temperature: +10°C ~+85°C, T.C.C.: +22~-56%)					Y5V (CLASS Ⅱ, Temperature:-25°C~+85°C,, T.C.C.: +22~-82%)			
Rate voltage			3KV					3KV				3	KV		
D φ (Code)	060	070	090	110	130	060	080	100	110	120	060	080	100	140	
D max. (mm)	7.5	8.5	10.5	12.5	14.5	7.5	9.5	11.5	12.5	13.5	7.5	9.5	11.5	15.5	
T max. (mm)	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	5.0	
100	101														
150	151						公石	1	Ŧ						
220	221					相			Z 320						
330	331					KINP	法系	i177.2		1					
470		471			tu		XI	1		.C.					
560		561			141/	1			YE	24					
680			681			HHÎ			Y Y						
750			751			זאיי			Ľ	2					
820			821												
1000			102		8	102	997 VG 913	IEM ALL	TANCE	FD FD	102				
1500				152	NR	5	152				152				
2200					222	S	222			RE	222				
3300					C	2		332				332			
3900							Chnol		392	10°.			392		
4700						SAT	2/10	051	non AL	472			472		
8200						146	TNOLOGY	CORPORI	110					105	
10000								VVIII						103	
φ d (mm)								0.55+/-	0.05						
Packing							Т/	APING or	BULK						
Coating								Epoxy R							

Manufacturing capacity list



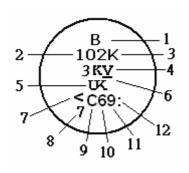


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4 Marking :



1. Temperature characteristic	2. Nominal capacitance	3. Capacitance tolerance	4. Rated voltage	5. Manufacturer's identification	6. Halogen and Pb free
Y5P: Be marked "B" Z5U / Y5U: Be marked "E" Y5V: Shall be omitted	Identified by 3-figure code when Cap.≥100pF Ex. 1000pF →"102"	K: ±10% (For Y5P) M: ±20% (For Z5U or Y5V) Z: +80%-20% (For Y5V)	3000V : Be marked "3kV"	Shall be marked as " ↓ ", but when the code of body diameter dimension ≤060 shall be omitted.	When the epoxy resin is Halogen and Pb free, there is a "–"marking.
Definition of date	code marking:	所有人	7		
7.Supplier of Epoxy	8.No. of test equipment	9.Factory of manufacture	10. Year of manufacture	11.Month of manufacture	12.Week of manufacture by month
<:K-company , : P-company	1~9: No.1~No.9, J: No.10, K: No.11, L: No.12		1:2011, 2:2012, 3:2013, 4:2014, 5:2015, 6:2016, 7:2017,	 1~9:January~ September, O: October, N: November, D: December 	week 1: - week 2: ' week 3: : week 4: ' week 5: ;
	GAT ON ASIANTE	echnology Corport	orport and a start of the start		

3KV Hi-K CERAMIC DISC CAPACITOR POE-D05-00-E-10 Ver:10 Page: 8 / 14 5. Taping format: 15mm pitch/lead spacing 7.5mm taping 25.4mm pitch/lead spacing 10.0mm taping Lead Code: *BAFD? & *DAFD? & *HAFD? & *XAFD? Lead Code: *DAMD0 & *XAMD0 & *HAMD0 & *BAMD0 ØD0 ØD0 Dmax Tmax Dmax Dmax Tmax Tmax Tmax Dmax High of kink High of kink e:3.0max High of kink F 4.0max 5.0max 5.0max Ød Ød ***B*** *D* *н *X* *BAMD0 *DAFD7 *BAFD7 *DAMD0 POE Part Number *HAFD7 *HAMD0 *XAFD7 *XAMD0 Dimensions Symbol Item Dimensions (mm) Dimensions (mm) (mm) 15.0 25.4 P 15.0 Pitch of component 15.0±0.3 Pitch of sprocket PO 15.0±0.3 12.7±0.3 7.5±1.0 Lead spacing F 7.5±1.0 10.0±1.0 Length from hole center to component center 7.5±1.5 12.7 ± 1.5 P2 7.5±1.5 3.75 ± 1.0 Length from hole center to lead **P1** 3.75±1.0 7.7±1.5 D See the "3. Capacitance value vs. Rate voltage, product diameter" Body diameter Deviation along tape, left or right $\triangle S$ 0±2.0 Carrier tape width W 18.0 +1/-0.5 W1 Position of sprocket hole 9.0±0.5 18.0+2.0/-0 For: Lead distance between the kink and nolog 18.0+2.0/-0 H0 *DAMD0 center of sprocket hole *HAMD0 *XAMD0 Lead distance between the bottom of 20.0+1.5/-1.0 Η 20.0+1.5/-1.0 --body and the center of sprocket hole For: *BAMD0 Ø Protrusion length 2.0max (Or the end of lead wire may be inside the tape.) Diameter of sprocket hole D0 4.0±0.2 Lead diameter 0.55 +/-0.05 φd Total tape thickness t1 0.6±0.3 Total thickness, tape and lead wire t2 1.5 max. \triangle h1 2.0 max. Deviation across tape \triangle h2 2.0 max. 11.0 max. Portion to cut in case of defect L W0 11.5min Hole-down tape width

W2

e Т

Hole-down tape distortion Coating extension on leads

Body thickness

1.5±1.5

3.0 max for straight lead style; Not exceed the kink leads for kink lead.

See the "3. Capacitance value vs. Rate voltage, product diameter"

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6. Specification and test method:

- 6.1 SCOPE: THIS SPECIFICATION APPLIES TO HIGH VOLTAGE CONSTANT, 3KV CERAMIC CAPACITOR.
- 6.2 TEST CONDITIONS:

UNLESS OTHERWISE SPECIFIED, ALL TESTS SHALL BE OPERATED AT THE STANDARD TEST CONDITIONS OF TEMPERATURE 5°C TO 35°C AND RELATIVE HUMIDITY 45% TO 85%. WHEN FAILS A TEST, RETEST BE OPERATED AT THE CONDITIONS OF TEMPERATURE 25°C \pm 2°C, RELATIVE HUMIDITY OF 60% TO 70% AND BAROMETRIC PRESSURE 860 TO 1060 MBAR.

6.3 HANDLE PROCEDURE: TO AVOID UNEXPECT TESTING RESULTS FROM OCCURING, THE TESTED CAPACITOR MUST BE KEPT AT ROOM TEMPERATURE FOR AT LEAST 30 MINUTES AND COMPLETELY DISCHARGED.

6.4 TEST ITEMS:				
ITEM	POST-TEST REQUIREMENTS	TESTING PROCEDURE		
APPEARANCE STRUCTURE SIZE	NO ABNORMALITIES			
MARKING		AS ITEM 4.MARKING.		
	BETWEEN TERMINALS: NO ABNORMALITIES	2 TIMES OF THE RATED VOLTAGE. TEST VOLTAGE : 6KVDC, 1~5 SEC, WITH 50mA MAX. CHARGING CURRENT		
WITHSTAND VOLTAGEN	BETWEEN TERMINAL AND ENCLOSURE : NO ABNORMALITIES	SMALL METALLIC BALLS WITH 1mm DIAMETERS SHALL BE PUT ON A VESSEL AND THE TEST CAPACITOR SHALL BE SUBMERGED EXCEPT 2mm FROM THE TOP OF ITS COMPONENT BODY. THE TEST VOLTAGE SHALL BE APPLIED BETWEEN THE SHORT-CIRCUITED TERMINALS AND THE METALLIC BALLS. (APPLY 1.3KV DC OF RATED VOLTAGE BETWEEN TERMINALS AND ENCLOSURE FOR 1~5 SEC)		
INSULATION RESISTANCE	10000 ΜΩ ΜΙΝ	INSULATION RESISTANCE SHALL BE MEASURED AT 60±5 SECONDS AFTER RATED VOLTAGE APPLIED. RATED VOLTAGE : 500VDC		
CAPACITANCE	TOLERANCE : K : ±10% M : ±20% Z:+80%-20%	TESTING FREQUENCY : 1 KHZ ± 20% TESTING TEMPERATURE : 25 ± 2°C, TESTING VOLTAGE : 1.0 ± 0.2 VRMS		
TEMP. RANGE	OPERATING TEMPERATURE : $Y5P$: $-25^{\circ}C$ to $+125^{\circ}C$ / $Z5U/Y5U$: $+10^{\circ}C$ to $+85^{\circ}C$ / $Y5V$ $-25^{\circ}C$ to $+125^{\circ}C$			
DISSIPATION FACTOR(D.F.) Y5P : < 2.5% Z5U/Y5U : BELOW 2.5% Y5V : BELOW 5.0% AS ABOVE ST		AS ABOVE STIPULATION OF CAPACITANCE		
TEMPERATURE CHARACTERISTIC	CAP. CHANGE: Y5P : WITHIN ± 10% Z5U/Y5U : WITHIN +22,-56% Y5V : WITHIN +22%, -82%	CAPACITANCE SHALL BE MEASURED AT 25° C. AND CLASSIFIED AS CAP. CHANGE : CLASS Y5 : -25° C ~ $+85^{\circ}$ C CLASS Z5 : $+10^{\circ}$ C ~ $+85^{\circ}$ C Pre-treatment: Capacitor shall be stored at $125\pm3^{\circ}$ C for 1hour.then placed at % 1room condition for 24 ± 2 hours		

%1"room condition" Temperature:15~35, Relative humidity: 45~75%, Atmospheric pressure:86~106kPa

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ITEM	POST-TEST REQUIREMENTS	TESTING PROCEDURE		
TERMINAL	TENSIBLE STRENGTH : NO BREAKDOWN	WIRE DIA. 0.5mm, LOADING WEIGHT 0.5KG FOR 10±1 SECONDS. WIRE DIA. 0.6mm, LOADING WEIGHT 1.0KG FOR 10±1 SECONDS.		
STRENGTH	BENDING STRENGTH : NO BREAKDOWN.	WIRE DIA.0.5 M/M, LOADING WEIGHT 0.25KG WIRE DIA.0.6 M/M, LOAIDNG WEIGHT 0.5KG (BENDING BACK AND FORTH 90 DEGREE TWICE)		
SOLDERABILITY	LEAD WIRE SHALL BE SOLDERED OVER 3/4 OF THE CIRCUMFERENTIAL DIRECTION.	TO COMPLY WITH JIS-C-5102 8.4 SOLDER TEMPERATURE 245±5°C AND DIPPING TIME 5±0.5 SECONDS. FLUX : WEIGHT RATIO OF POSIN 25%		
	APPEARANCE : NO ABNORMALITIES	LEAD WIRE OR TERMINALS SHALL BE IMMERSED UP TO 2.0 M/M FORM BODY.		
SOLDERING	CAP. CHANGE : Y5P : ±5% MAX	 INTO THE MOLTEN SOLDER OF WHICH TEMPERATURE: 260(+5/-0)°C FOR 5~10 SECONDS.THEN LEAVE AT STANDARD TEST CONDITIONS FOR 4~24 HOURS, THEN MEASURED. WHEN SOLDERING CAPACITOR WITH A SOLDERING IRON, IT SHOULD BE PERFORMED IN 		
HEAT RESISTANCE	Z5U/Y5U : ±15% MAX Y5V : ± 20% MAX			
	WITHSTAND VOLTAGE: (BETWEEN TERMINALS)	FOLLOWING CONDITIONS. TEMPERATURE OF IRON-TIP: 350~400 °C SOLDERING IRON WATTAGE : 50W MAX. SOLDERING TIME : 3.5 SEC. MAX.		
HUMIDITY CHARACTERISTIC (STABLE SITUATION)	NO ABNORMALITIES APPEARANCE : NO ABNORMALITIES CAP. CHANGE : Y5P : $\pm 10\%$ MAX Z5U/Y5U : $\pm 20\%$ MAX Y5V : $\pm 30\%$ MAX D.F.: Y5P : 5% MAX Z5U/Y5U : 5% MAX Y5V : 7.5% MAX INSULATION RESISTANCE: 1000M Ω MIN.	CAPACITORS SHALL BE SUBJECTED TO A RELATIVE HUMIDITY OF 90 \sim 95% AT 40±2°C FOR 500(+24/-0) HOURS. THEN DRIED FOR 1 \sim 2 HOURS AND MEASURED.		
HUMIDITY	APPEARANCE : NO ABNORAMLITIES CAP. CHANGE : Y5P : ±10% MAX Z5U/Y5U : ±20% MAX	CAPACITORS SHALL BE SUBJECTED TO A RELATIVE HUMIDITY OF 90 \sim 95% AT 40 ± 2°C FOR 500(+24/-0) HOURS WITH RATED VOLTAGE APPLIED WITH 50mA MAX. THEN DRIED FOR 1 \sim 2 HOURS AND MEASURED.		
LOADING	Y5V : \pm 30% MAX D.F.: Y5P : 5% MAX Z5U/Y5U : 5% MAX Y5V : 7.5% MAX INSULATION RESISTANCE: 500 MΩ MIN	Pre-treatment: Capacitor shall be stored at125±3°C for 1hour.then placed at % 1room condition for 24±2hours		

X1"room condition" Temperature:15~35, Relative humidity: 45~75%, Atmospheric pressure:86~106kPa

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ITEM	POST-TEST REQUIREMENTS	TESTING PROCEDURE	
	APPEARANCE:	CAPACITORS SHALL BE SUBJECTED TO A TEST OF	
	NO ABNORMALITIES	150% RATED VOLTAGE WITH 50mA MAX. FOR	
	CAP. CHANGE :	1000(+48/-0) HOURS AT 125 \pm 2°C $$ AND THEN DRIED	
	Y5P:±10% MAX	FOR 24±2 HOURS AND MEASURED.	
	Z5U/Y5U : ±20% MAX		
HIGH	Y5V: ± 30% MAX	Pre-treatment:	
TEMPERATURE LOADING	D.F. :	Capacitor shall be stored at $125\pm3^{\circ}$ °C for 1 hour. then placed	
LUADING	Y5P:4% MAX	at%1room condition for 24±2hours	
	Z5U/Y5U : 4% MAX		
	Y5V : 7.5% MAX		
	INSULATION RESISTANCE : 1000		
	$M\Omega$ MIN.		
	.+		
	APPEARANCE :	CAPACITORS SHALL BE SUBJECTED TO :	
	NO ABNORMALITIES	$-25\pm3^{\circ}\mathbb{C}(30\pm3\min) \rightarrow 25^{\circ}\mathbb{C}(3\min) \rightarrow 85\pm3^{\circ}\mathbb{C}(30\pm3\min) \rightarrow 25^{\circ}\mathbb{C}(30\pm3\min) \rightarrow 25$	
	NO ADNORMALITIES	°C (3min) FOR 5 CYCLE	
	CAP. CHANGE:		
	Y5P:±10% MAX	Pre-treatment:	
	Z5U/Y5U: ±20% MAX PASSIVE SY	Capacitor shall be stored at125±3°C for 1hour.then placed	
TEMPERATURE	Y5V : ± 30% MAX	at % 1 room condition for 24±2 hours	
CYCLING	D.F.:		
	Y5P:5% MAX	LORN COTP ILES	
	Z5U/Y5U : 5% MAX	CODDORATION. H	
	Y5V : 7.5% MAX	CONFORM	
	INSULATION DESISTANCE.		
	INSULATION RESISTANCE: $1000 \text{ M}\Omega$ MIN.		
	1000 WI 22 WIIIN.		

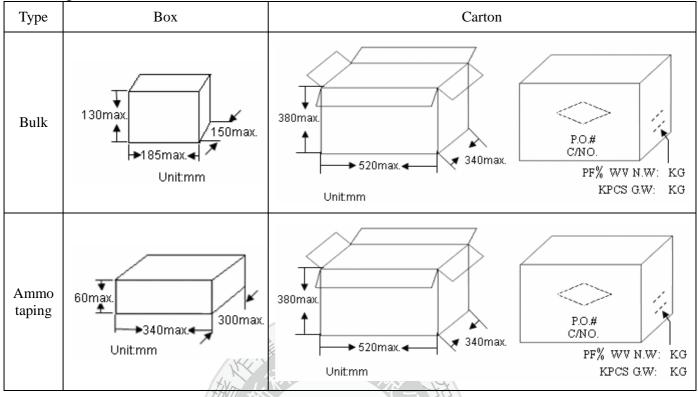
*1"room condition" Temperature:15~35, Relative humidity: 45~75%, Atmospheric pressure:86~106kPa



P

7. Packing Baggage :

7.1 Packing size:



7.2 Packing quantity:

Packing type		The code of 14th to15th in SAP P/NSSIVE SYSTEM ALLIANCE	MPQ(Kpcs/Box)		
Taping Packing type	AF		1 0.5		
	Lead length	Size code of 10th to 12th in SAP P/N	MPQ (Kpcs/Bag)	Kpcs/Box	
	Long lead (L≧ 16mm)	060~100	1	2	
		110~120	0.5	1.5	
		130~170	0.5	1	
D11-	Short lead (L16mm)	060	1	6	
Bulk		070~080	1	4	
		090~100	1	3	
		110~140	1	2	
		170	0.5	1	

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8. Notices:

8.1 Operating Voltage:

When DC-rated capacitors are to be used in AC or ripple current circuits, be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range.

When the voltage is applied to the circuit, starting or stopping may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

Voltage	DC Voltage	DC Voltage DC+AC Voltage AC Voltage		Pulse Voltage (1)	Pulse Voltage (2)	
Positional measurement	Vo-p	V0-p	Vp-p	Vp-p	Vp-p	

8.2 Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high frequency current, pulse current or similar current, it may self-generate heat due to dielectric loss. The frequency of the applied sine wave voltage should be less than 100kHz. The applied voltage load (*) should be such that the capacitor's self-generated heat is within 20°C at an atmosphere temperature of 25°C. When measuring, use a thermocouple of small thermal capacity-K of Ø0.1mm in conditions where the capacitor is not affected by radiant heat from other components or surrounding ambient fluctuations.

Excessive heat may lead to deterioration of the capacitor's characteristics and reliability. (Never attempt to perform measurement with the cooling fan running. Otherwise, accurate measurement cannot be ensured.)

8.3 Fail-Safe

When capacitor is broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

8.4 Operating and storage environment

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed –10 to 40 degrees centigrade and 15 to 85 % for 6 months maximum and use within the period after receiving the capacitors.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

8.5 Vibration and impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

8.6 Soldering

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element. When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 degrees C. max.

Soldering iron wattage : 50W max.

Soldering time : 3.5 sec. max.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.

8.7 Cleaning (ultrasonic cleaning)

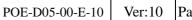
To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity : Output of 20 watts per liter or less.

Rinsing time : 5 min. maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.



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9.Drawing of internal structure and material list:

産品結構圖

Remarks :

No.	Part name	Material	Model/Type	Component
1	Insulation Coating	Epoxy polymeres	1.EF-150C 2.EF-150(HF) 3.PCE-210 2.PCE-300(HF)	Epoxy resin、Pigment (Blue / UL 94 V-0 /) The minimum thickness of coating (reinforced insulation) is 0.4mm
2	Dielectric Element	Ceramic	Y5P/Y5U/Z5U/Y5V	BaTiO ₃
3	Solder	Tin-silver	Sn96.5-Ag3-Cu0.5	Sn96.5-Ag3-Cu0.5
4	Electrodes	Ag	2.SP-260PL	Silver 、 Glass frit
5	Leads wire	Tinned copper clad steel wire	0.55±0.05 mm	Substrate metal: Fe & Cu Surface plating: Sn 100%(3~7µm)