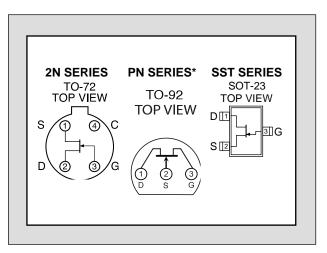
LINEAR SYSTEMS

Twenty-Five Years Of Quality Through Innovation

FEATURES					
Replacement For SILICONIX 2N/SST4416 & 2N4416A					
VERY LOW NOISE FIGURE (400 MHz)	4 dB				
EXCEPTIONAL GAIN (400 MHz)	10 dB				
ABSOLUTE MAXIMUM RATINGS ¹					
@ 25 °C (unless otherwise stated)					
Maximum Temperatures					
Storage Temperature	-55 to +150 °C				
Operating Junction Temperature	-55 to +135 °C				
Maximum Power Dissipation					
Continuous Power Dissipation	300mW				
Maximum Currents					
Gate Current	10mA				
Maximum Voltages					
Gate to Drain or Gate to Source 2N4416	-30V				
Gate to Drain or Gate to Source 2N4416A	-35V				

2N/PN SST4416 2N4416A

N-CHANNEL JFET HIGH FREQUENCY AMPLIFIER



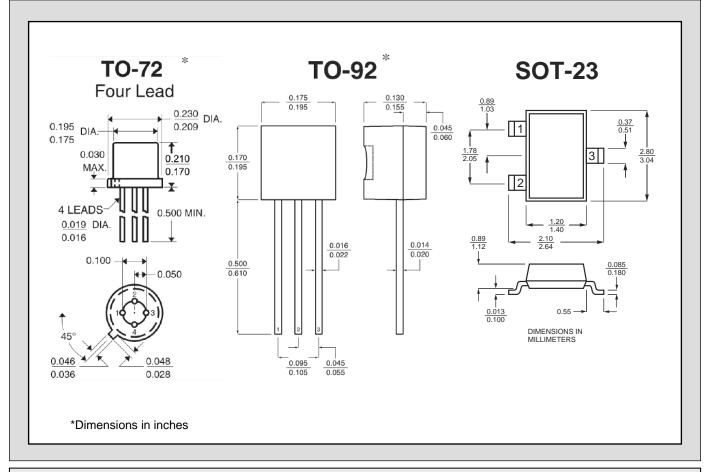
*Optional Package For 2N4416

ELECTRICAL CHARACTERISTICS @ 25 °C (unless otherwise stated)

SYMBOL	CHARACTERISTIC				TYP	MAX	UNITS	CONDITIONS
BV _{GSS}	Gate to Source	2N/PN/SST4416		-30			v	$I_G = -1\mu A, V_{DS} = 0V$
	Breakdown Voltage	2N4416A		-35				
Vcc(e#)	Gate to Source	2N/PN/SST4416				-6		
	Cutoff Voltage	2N	4416A	-2.5		-6		$V_{DS} = 15V, I_D = 1nA$
I _{DSS}	Gate to Source Saturation Current			5		15	mA	$V_{DS} = 15V, V_{GS} = 0V$
I _{GSS}	Gate Leakage Current 2N PN/SST				-0.1	nA	$V_{GS} = -20V, V_{DS} = 0V$	
					-1.0		$V_{GS} = -15V, V_{DS} = 0V$	
g fs	Forward Transconductance			4000		7500		$V_{DS} = 15V. V_{GS} = 0V. f = 1kHz$
g _{os}	Output Conductance					100	μ S V _{DS} = 15V, V _{GS} = 0V, f = 1k	$v_{\rm DS} = 15v, v_{\rm GS} = 0v, t = 1 \text{ kHz}$
Ciss	Input Capacitance ²					0.8		
C _{rss}	Reverse Transfer Capacitance ²					4	pF	$V_{DS} = 15V$, $V_{GS} = 0V$, $f = 1MHz$
C _{oss}	Output Capacitance ²					2		
en	Equivalent Input Noise Voltage				6		nV/√Hz	$V_{DS} = 10V, V_{GS} = 0V, f = 1kHz$

SYMBOL	CHARACTERISTIC	100 MHz		400 MHz		UNITS	CONDITIONS	
		MIN	MAX	MIN	MAX		CONDITIONS	
g _{iss}	Input Conductance ²		100		1000			
b _{iss}	Input Susceptance ²		2500		10000			
g _{oss}	Output Conductance ²		75		100	μS	V_{DS} = 15V, V_{GS} = 0V	
b _{oss}	Output Susceptance ²		1000		4000			
G _{fs}	Forward Transconductance ²			4000				
G _{ps}	Power Gain ²	18		10		٩D	$V_{DS} = 15V, I_D = 5mA$	
NF	Noise Figure ²		2		4	dB	V_{DS} = 15V, I_D = 5mA, R_G = 1k Ω	





NOTES

1. Absolute maximum ratings are limiting values above which serviceability may be impaired.

2. Not production tested, guaranteed by design.

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Linear Integrated Systems (LIS) is a 25-year-old, third-generation precision semiconductor company providing high-quality discrete components. Expertise brought to LIS is based on processes and products developed at Amelco, Union Carbide, Intersil and Micro Power Systems by company President John H. Hall. Hall, a protégé of Silicon Valley legend Dr. Jean Hoerni, was the director of IC Development at Union Carbide, co-founder and vice president of R&D at Intersil, and founder/president of Micro Power Systems.

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