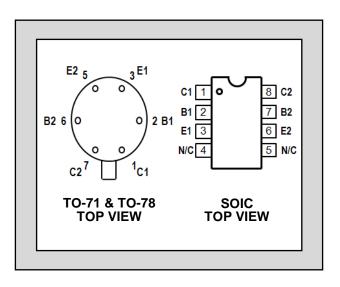
LINEAR SYSTEMS

Twenty-Five Years Of Quality Through Innovation

FEATURES						
VERY HIGH GAIN		h _{FE} 2000 @ 1.0μΑ TYP.				
LOW OU	JTPUT CAPACITANCE	Сово 2.0pF				
TIGHT VBE MATCHING		IV_{BE1} - $V_{BE2}I$ =0.2mV TYP.				
HIGH f⊤		100 MHz				
ABSOLUTE MAXIMUM RATINGS NOTE 1						
@ 25 °C (unless otherwise stated)						
lc	Collector Current	5mA				
Maximum Temperatures						
Storage Temperature		-55 to +150 °C				
Operating Junction Temperature -55 to +150 °C						
Maximum Power Dissipation		ONE SIDE	BOTH SIDES			
Device Dissipation @ Free Air		250mW	500mW			
Linear Derating Factor		2.3mW/°C	4.3mW/°C			

LS301 LS302 LS303

HIGH VOLTAGE SUPER-BETA MONOLITHIC DUAL NPN TRANSISTORS

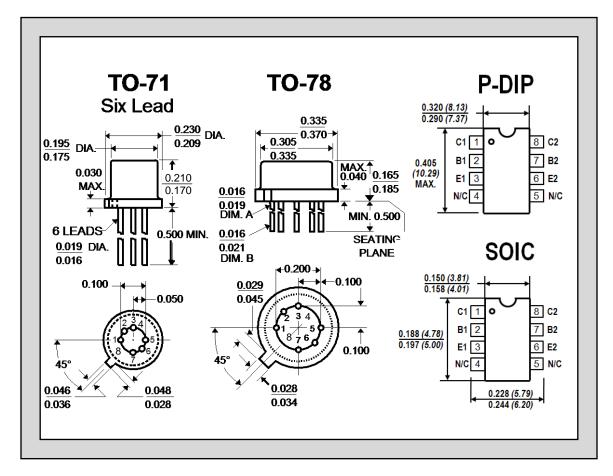


ELECTRICAL CHARACTERISTICS @ 25 °C (unless otherwise stated)

SYMBOL	CHARACTERISTIC	LS301	LS302	LS303		UNITS	CONDITIONS
ВV _{сво}	Collector to Base Voltage	18	35	10	MIN.	V	$I_C = 10 \mu A$ $I_E = 0$
BV _{CEO}	Collector to Emitter Voltage	18	35	10	MIN.	V	$I_C = 1mA$ $I_B = 0$
BVEBO	Emitter-Base Breakdown Voltage	6.0	6.0	6.0	MIN.	V	$I_E = 10\mu A$ $I_C = 0$ <u>NOTE 2</u>
BVcco	Collector To Collector Voltage	80	80	20	MIN.	V	$I_C = 1 \mu A \qquad I_E = I_B = 0$
hfe	DC Current Gain	2000	1000	2000	TYP.		$I_C = 1\mu A$ $V_{CE} = 5V$
hfe	DC Current Gain	2000	1000	2000	MIN.		$I_C = 10 \mu A$ $V_{CE} = 5 V$
h _{FE}	DC Current Gain	2000	1000	2000	TYP.		$I_C = 500 \mu A$ $V_{CE} = 5V$
V _{CE} (SAT)	Collector Saturation Voltage	0.5	0.5	0.5	MAX.	V	$I_C = 1mA$ $I_B = 0.1mA$
Ісво	Collector Cutoff Current	100	100	100	MAX.	pА	$I_E = 0$ $V_{CB} = NOTE 3$
I _{EBO}	Emitter Cutoff Current	0.2	0.2	0.2	MAX.	pА	$I_E = 0$ $V_{EB} = 3V$
Сово	Output Capacitance	2	2	2	MAX.	pF	$I_E = 0$ $V_{CB} = 1V$
C _{C1C2}	Collector to Collector Capacitance	2	2	2	MAX.	pF	$V_{CC} = 0$
Ic1c2	Collector to Collector Leakage Current	1.0	1.0	1.0	MAX.	μA	$V_{CC} = NOTE 4$, $I_E = I_B = 0$
f⊤	Current Gain Bandwidth Product	100	100	100	MIN.	MHz	$I_C = 200 \mu A$ $V_{CE} = 5V$
NF	Narrow Band Noise Figure	3	3	3	MAX.	dB	$I_C = 10 \mu A$ $V_{CE} = 3V$
							$BW = 200Hz \qquad R_G = 10K$
							f = 1KHz

MATCHING CHARACTERISTICS

SYMBOL	CHARACTERISTIC	LS301	LS302	LS303		UNITS	CONDITIONS
IV _{BE1} -V _{BE2} I	Base Emitter Voltage Differential	0.2	0.2	0.2	TYP.	mV	$I_C = 10 \mu A$ $V_{CE} = 5 V$
		1	1	1	MAX.	mV	
I(V BE1-VBE2)I/°C	Base Emitter Voltage Differential	1	1	1	TYP.	µV/°C	$I_C = 10 \mu A$ $V_{CE} = 5 V$
	Change with Temperature	5	5	5	MAX.	µV/°C	$T = 55^{\circ}C$ to $+125^{\circ}C$
ll _{B1} - l _{B2} l	Base Current Differential	0.5	1	0.5	TYP.	nA	$I_C = 10 \mu A$ $V_{CE} = 1 V$
		1	5	1.5	MAX.	nA	$I_C = 10 \mu A$ $V_{CE} = 5 V$
hfe1/hfe2	DC Current Gain Differential	5	5	5	TYP.	%	$I_C = 10 \mu A$ $V_{CE} = 5 V$



NOTES:

- 1. These ratings are limiting values above which the serviceability of any semiconductor may be impaired
- 2. The reverse base-to-emitter voltage must never exceed 6.0 volts; the reverse base-to-emitter current must never exceed 10 µAmps.
- 3. For LS301 & LS302: V_{CB} =10V; for LS303: V_{CB} =5V
- 4. For LS301 & LS302: V_{CC}=±80V; for LS303: V_{CC}=±20V

Information furnished by Linear Integrated Systems is believed to be accurate and reliable. However, no responsibility is assumed for its use; nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Linear Integrated Systems.

Linear Integrated Systems (LIS) is a 25-year-old, third-generation precision semiconductor company providing high-quality discrete components. Expertise brought to LIS is based on processes and products developed at Amelco, Union Carbide, Intersil and Micro Power Systems by company President John H. Hall. Hall, a protégé of Silicon Valley legend Dr. Jean Hoerni, was the director of IC Development at Union Carbide, Co-Founder and Vice President of R&D at Intersil, and Founder/President of Micro Power Systems.

Linear Integrated Systems