LINEAR SYSTEMS

Twenty-Five Years Of Quality Through Innovation

<u>LS318</u>

LOG CONFORMANCE MONOLITHIC DUAL NPN TRANSISTORS

| FEATURES | | | | | | |
|--|-------------------------------|-------|-----------------|------------|--|--|
| LOG CON | Δre =1 TYP. | | | | | |
| ABSOLUTE MAXIMUM RATINGS <u>NOTE 1</u> (T _A = 25°C unless otherwise noted) | | | | | | |
| lc | Collector-Current | 10mA | | | | |
| Maximum Temperatures | | | | | | |
| Storage Temperature Range | | | -55°C to +150°C | | | |
| Operating Junction Temperature | | | -55°C to +150°C | | | |
| Maximum | Power Dissipation | ONE S | SIDE | BOTH SIDES | | |
| Device Dis | sipation T _A =25°C | 250m | W | 500mW | | |
| Linear Der | ating Factor | 2.3m | W/°C | 4.3mW/°C | | |



ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)

| SYMBOL | CHARACTERISTIC | LS318 | | UNITS | CONDITIONS | |
|-----------------------|--|-------|------|-------|--|--|
| Δre | Log Conformance | 1.5 | MAX. | Ω | $I_C = 10-100-1000 \mu A$ $V_{CE} = 5 V$ | |
| ВVсво | Collector-Base Breakdown Voltage | 25 | MIN. | V | $I_C = 10\mu A$ $I_E = 0A$ | |
| BV _{CEO} | Collector to Emitter Voltage | 25 | MIN. | V | $I_C = 100 \mu A \qquad I_B = 0 A$ | |
| BVEBO | Emitter-Base Breakdown Voltage | 6.0 | MIN. | V | $I_E = 10\mu A$ $I_C = 0A$ <u>NOTE 2</u> | |
| BV _{CCO} | Collector to Collector Voltage | 45 | MIN. | V | $I_C = 10\mu A$ $I_B = I_E = 0A$ | |
| hfe | DC Current Gain | 150 | MIN. | | $I_C = 10\mu A$ $V_{CE} = 5V$ | |
| | | 600 | MAX. | | | |
| hfe | DC Current Gain | 150 | MIN. | | Ic = 100μA V _{CE} = 5V | |
| | | 600 | MAX. | | | |
| h _{FE} | DC Current Gain | 150 | MIN. | | $I_C = 1mA$ $V_{CE} = 5V$ | |
| V _{CE} (SAT) | Collector Saturation Voltage | 0.25 | MAX. | V | Ic = 1mA I _B = 0.1 mA | |
| I _{CBO} | Collector Cutoff Current | 0.2 | MAX. | nA | $I_E = 0A$ $V_{CB} = 20V$ | |
| IEBO | Emitter Cutoff Current | 0.2 | MAX. | nA | $I_C = 0A$ $V_{EB} = 3V$ | |
| Сово | Output Capacitance | 1.8 | | pF | $I_E = 0A$ $V_{CB} = 3V$ f=1MHz <u>NOTE 3</u> | |
| CC1C2 | Collector to Collector Capacitance | 1.8 | | pF | Vcc = 0V | |
| Ic1c2 | Collector to Collector Leakage Current | 0.5 | MAX. | μA | $V_{CC} = \pm 45V$ $I_B = I_E = 0A$ | |
| f _T | Current Gain Bandwidth Product | 220 | | MHz | Ic = 1mA V _{CE} = 5V <u>NOTE 3</u> | |
| NF | Narrow Band Noise Figure | 3 | MAX. | dB | lc = 100µA V _{CE} = 5V <u>NOTE 3</u> BW = 200Hz, R _G = 10 K f=1KHz | |

| SYMBOL | CHARACTERISTIC | LS318 | | UNITS | CONDITIONS |
|---|-----------------------------------|-------|------|-------|---|
| VBE1-VBE2 | Base Emitter Voltage Differential | 0.4 | TYP. | mV | $I_C = 10 \ \mu A$ $V_{CE} = 5V$ |
| | | 1 | MAX. | mV | |
| (V _{BE1} -V _{BE2)} //°C | Base Emitter Voltage Differential | 1 | TYP. | μV/°C | Ic = 10 μA Vcε = 5V |
| | Change with Temperature | | | | $T_A = -55^{\circ}C$ to $+125^{\circ}C$ |
| I _{B1} -I _{B2} | Base Current Differential | 10 | MAX. | nA | $I_C = 10 \ \mu A$ $V_{CE} = 5V$ |
| (I _{B1} -I _{B2}) /ºC | Base Current Differential | 0.4 | TYP. | nA/⁰C | $I_C = 10 \ \mu A$ $V_{CE} = 5V$ |
| | Change with Temperature | | | | $T_{A} = -55^{\circ}C$ to $+125^{\circ}C$ |
| hfe1/hfe2 | DC Current Gain Differential | 5 | TYP. | % | Ic = 10 μA Vcε = 5V |





NOTES:

- 1. These ratings are limiting values above which the serviceability of any semiconductor may be impaired.
- 2. The reverse base-to-emitter voltage must never exceed 6.2 volts; the reverse base-to-emitter current must never exceed 10 µA.
- 3. Not tested; guaranteed by design.
- 4. All MIN/TYP/MAX values are absolute numbers. Negative signs indicate electrical polarity only.

Information furnished by Linear Integrated Systems is believed to be accurate and reliable. However, no responsibility is assumed for its use; nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Linear Integrated Systems.

Linear Integrated Systems (LIS) is a 25-year-old, third-generation precision semiconductor company providing high-quality discrete components. Expertise brought to LIS is based on processes and products developed at Amelco, Union Carbide, Intersil and Micro Power Systems by company President John H. Hall. Hall, a protégé of Silicon Valley legend Dr. Jean Hoerni, was the director of IC Development at Union Carbide, Co-Founder and Vice President of R&D at Intersil, and Founder/President of Micro Power Systems.

Linear Integrated Systems