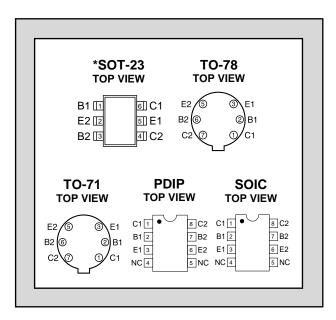
LINEAR SYSTEMS

Twenty-Five Years Of Quality Through Innovation

FEATURES								
6 LEAD SOT-23 SURFACE MOUNT PACKAGE*								
TIGHT MATCHING ¹ 2mV								
EXCELLENT THERMAL TRACKING ¹	3µV/°C							
ABSOLUTE MAXIMUM RATINGS ²								
@ 25 °C (unless otherwise stated)								
Maximum Temperatures								
Storage Temperature	-55 to +150 °C							
Operating Junction Temperature	-55 to +150 °C							
Maximum Power Dissipation								
Continuous Power Dissipation	TBD							
Maximum Currents								
Collector Current	50mA							
Maximum Voltages								
Collector to Collector Voltage	60V							

LS3550 SERIES

MONOLITHIC DUAL PNP TRANSISTORS



MATCHING ELECTRICAL CHARACTERISTICS @25 °C (unless otherwise stated)

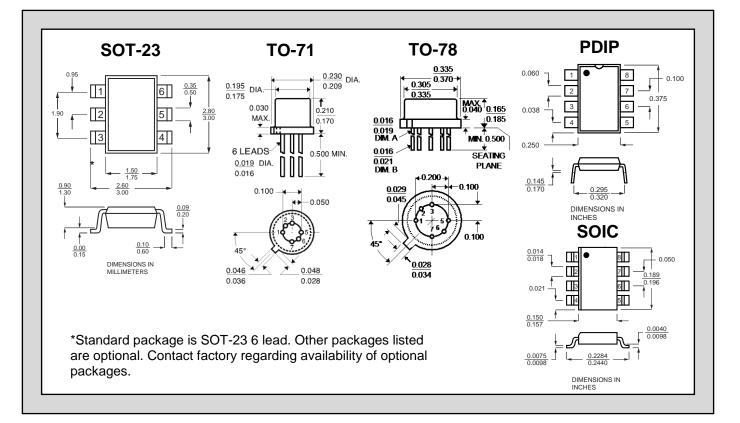
SYMBOL	CHARACTERISTIC	LS3550A		LS3550B		LS3550C			CONDITIONS
STMBOL		MIN	MAX	MIN	MAX	MIN	MAX		CONDITIONS
$\left V_{\text{BE1}}-V_{\text{BE2}}\right $	Base to Emitter Voltage Differential		2		5		10	mV	$I_C = -100 \mu A, V_{CE} = -5V$
$\frac{\left V_{\text{BE1}}-V_{\text{BE2}}\right }{\Delta T}$	Base to Emitter Voltage Differential Change with Temperature		3		5		15	µV/°C	$I_{C} = -100 \mu A$, $V_{CE} = -5V$ $T_{A} = -40^{\circ}C$ to +85°C
В1 — В2	Base Current Differential		10		10		10	nA	$I_C = -500 \mu A, V_{CE} = -5V$
$\frac{\left I_{B1}-I_{B2}\right }{\Delta T}$	Base Current Differential Change with Temperature		0.5		0.5		1.0	nA/°C	$I_{C} = -500 \mu A$, $V_{CE} = -5V$ $T_{A} = -40^{\circ}C$ to +85°C
hfe1/hfe2	Current Gain Differential		10		10		15	%	Ic = -1mA, Vce = -5V

ELECTRICAL CHARACTERISTICS @25 °C (unless otherwise stated)

SYMBOL	CHARACTERISTIC	LS3550A		LS3550B		LS3550C			CONDITIONS
		MIN	MAX	MIN	MAX	MIN	MAX		CONDITIONS
ВVсво	Collector to Base Breakdown Voltage	-45		-40		-20		15	$I_{C} = -10 \mu A$, $I_{E} = 0 A$
BVCEO	Collector to Emitter Breakdown Voltage	-45		-40		-20		16	$I_C = -5mA$, $I_B = 0A$
BV _{CCO}	Collector to Collector Breakdown Voltage	±60		±60		±60			$I_{CC} = -\pm 1 \mu A$, $I_B = I_C = 0 A$
BVEBO	Emitter to Base Breakdown Voltage ³	-6.0		-6.0		-6.0		V	$I_E = -10 \mu A$, $I_C = 0 A$
V _{CE(SAT)}	Collector to Emitter Saturation Voltage		-0.50		-0.50		-1.2		Ic = -10mA I _B = -1mA

SYMBOL	CHARACTERISTIC	LS3550A		LS3550B		LS3550C		UNIT	CONDITIONS
		MIN	MAX	MIN	MAX	MIN	MAX	UNIT	CONDITIONS
hfe	DC Current Gain	150		100		50			$I_C = -1mA$, $V_{CE} = -5V$
		120		80		40			I_C = -10mA, V_{CE} = -5V
		100		60		30			I_C = -50mA, V_{CE} = -5V
lana	Collector Cutoff Current		-0.35		-0.35				$I_E = 0A, V_{CB} = -30V$
I _{CBO}							-0.35	nA	$I_E = 0A, V_{CB} = -20V$
I _{EBO}	Emitter Cutoff Current		-0.35		-0.35		-0.35		$I_E = 0A$, $V_{CB} = -3V$
Ic1c2	Collector to Collector Leakage Current		±1		±1		±1	μA	$V_{CC} = \pm 60V$, $I_B = I_C = 0A$
Сово	Output Capacitance		2		2		2	pF	$I_E = 0A, V_{CB} = -10V$
f⊤	Gain Bandwidth Product (Current)		600		600		600	MHz	I_C = -1mA, V_{CE} = -5V
NF	Noise Figure (Narrow Band)		3		3		3	dB	$l_{C} = -100 \mu A, V_{CE} = -5V \\ BW = 200Hz \\ R_{B} = 10\Omega, f = 1 \text{kHz}$





NOTES

1. Maximum rating for LS3550A, SOT23-6.

2. Absolute maximum ratings are limiting values above which serviceability may be impaired.

3. The reverse Base-to-Emitter voltage must never exceed -6.0 Volts. The reverse Base-to-Emitter current must never exceed -10µA. Information furnished by Linear Integrated Systems is believed to be accurate and reliable. However, no responsibility is assumed for its use; nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Linear Integrated Systems.

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