Powering the SiC evolution™

AS3T040A120T

V_{DS}	1200 V
$R_{DS,on}$	38 mΩ
$I_{D\ (TC=25C)}$	64 A
T_j,max	175°C

QSiC™ 1200V SiC MOSFET

Features

- High speed switching
- Reliable body diode
- All parts tested to greater than 1,400V
- Avalanche tested to 330mJ
- Driver source pin for gate driving
- Qualified for Automotive Applications
  Product Validation according to AEC-Q101

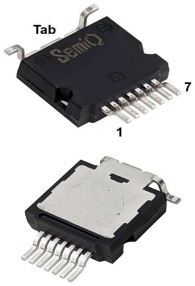
Benefits

- Lower capacitance
- Higher system efficiency
- Lower Switching Loss
- Longer creepage distance
- Small footprint
- Longer clearance distance

Applications

- On board chargers
- High voltage DC/DC converters
- Motor drives
- Switch mode power supplies
- EV charging stations

Package



(Tab) D (Drain)
(1) G (Gate)
(2) S_G (Driver Source)
(3-7) S (Source)

Part #	Package	Marking
AS3T040A120T	TCPAK	AS3T040A120T

AEC-Q101 QUALIFIED

RoHS

Maximum Ratings, at T_j=25°C, unless otherwise specified

Characteristics	Symbol	Conditions	Values	Unit
Drain-Source Voltage	V_{rated}	V_{GS}=0V, I_{DS}=1μA	1200	V
Continuous Drain Current	I_D	T_C=25 °C, T_j=175 °C	64	A
		T_C=100 °C, T_j=175 °C	46	
Pulsed Drain Current	$I_{D,pulse}$*	T_C=25°C	140	
Gate Source Voltage	V_{GSmax}		-8/22	V
	V_{GSop}	Recommended operational	-4.5/18	
Power Dissipation	P_{tot}	T_C=25°C	268	W
Operating & Storage Temperature	T_j, $T_{storage}$	Continuous	-55...175	°C
Single Pulse Avalanche Energy	E_{AS}	L=1.0mH, I_{AS}=25.7A, V=50V	330	mJ

Thermal Characteristics

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Thermal Resistance, Junction to Case	R_{thJC}		-	0.44	0.56	°C/W
Thermal Resistance, Junction to Ambient	R_{thJA}		-	-	40.0	

* Pulse width is limited by Tj_{max}

Static Electrical Characteristics, at T_j=25°C, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Drain-Source Breakdown Voltage	BV_{DSS}	I_{DS}=1mA	1200	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=1200V, V_{GS}=0V	-	0.1	1.0	μA
		V_{DS}=1200V, V_{GS}=0V, T_j=175°C	-	1	-	
Gate-Source Leakage Current	I_{GSS+}	V_{GS}=18V, V_{DS}=0V	-	10	100	nA
	I_{GSS-}	V_{GS}=-4.5V, V_{DS}=0V	-	-10	-100	
Gate Threshold Voltage	$V_{GS(th)}$	V_{GS}=V_{DS}, I_{DS}=10mA	1.8	3.5	4	V
		V_{GS}=V_{DS}, I_{DS}=10mA, T_j=125°C	-	2.7	-	
		V_{GS}=V_{DS}, I_{DS}=10mA, T_j=175°C	-	2.5	-	
Drain-Source On-Resistance	R_{DSon}	V_{GS}=18V, I_{DS}=20A	-	38	52	mΩ
		V_{GS}=18V, I_{DS}=20A, T_j=125°C	-	50	-	
		V_{GS}=18V, I_{DS}=20A, T_j=175°C	-	63	-	
Transconductance	g_{fs}	V_{DS}=20V, I_{DS}=20A	-	12	-	S
Gate Input Resistance	R_G	f=1MHz, V_{AC}=25mV, D-S Short	-	2.9	-	Ω

AC Electrical Characteristics, at T_j=25°C, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Input Capacitance	C_{ISS}	V_{GS}=0V, V_{DS}=1000V, f=200kHz, V_{AC}=25mV	-	2803	-	pF
Output Capacitance	C_{OSS}		-	97	-	
Reverse Transfer Capacitance	C_{RSS}		-	9	-	
Coss Stored Energy	E_{OSS}		-	56	-	μJ
Turn-On Switching Energy	E_{ON}	V_{DD}=800V, I_{DS}=20A, $R_{G(ext)}$=3.9Ω, V_{GS}=-4.5/+18V, L=273μH, FWD=AS3T040A120T	-	358	-	μJ
Turn-Off Switching Energy	E_{OFF}		-	54	-	
Total Switching Energy	E_{TOT}		-	412	-	
Turn-On Delay Time	$t_{D(on)}$	V_{DD}=800V, I_{DS}=20A, $R_{G(ext)}$=3.9Ω, V_{GS}=-4.5/+18V, L=273μH, FWD=AS3T040A120T	-	14	-	ns
Rise Time	t_R		-	7	-	
Turn-Off Delay Time	$t_{D(off)}$		-	34	-	
Fall Time	t_F		-	15	-	
Total Gate Charge	Q_G	V_{DD}=800V, I_{DS}=20A, V_{GS}=-4.5/+18V	-	109	-	nC
Gate to Source Charge	Q_{GS}		-	40	-	
Gate to Drain Charge	Q_{GD}		-	26	-	

Body Diode Characteristics, at Tj=25°C, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Max Continuous Diode Fwd Current	I_S	V_{GS}=-5V, T_C=25°C	-	-	59	A
Diode Forward Voltage	V_{SD}	V_{GS}=-5V, I_{SD}=10A	-	4.2	-	V
Reverse Recovery Time	t_{RR}	I_{SD}=40A, V_R=800V, V_{GS}=-4.5/+18V, di_F/dt=4.8A/ns	-	19	-	ns
Reverse Recovery Charge	Q_{RR}		-	254	-	nC
Peak Reverse Recovery Current	I_{RRM}		-	34	-	A

Typical Performance

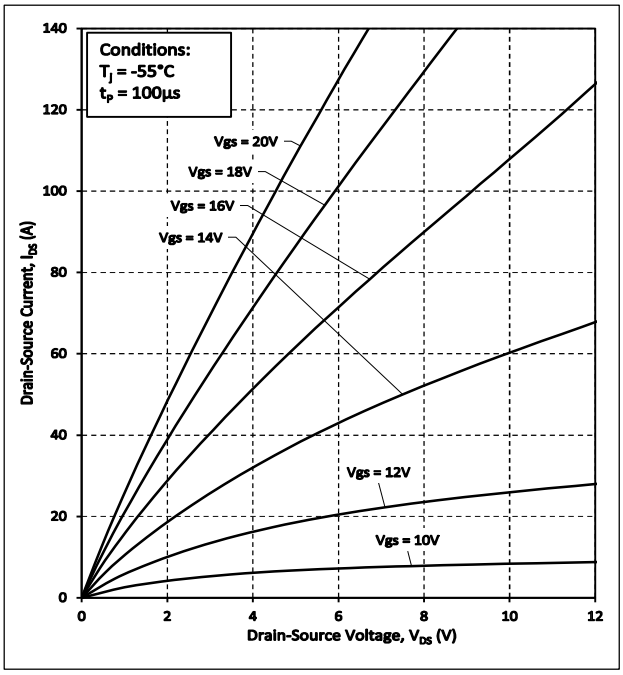


Figure 1. Output Characteristics T_j = -55°C

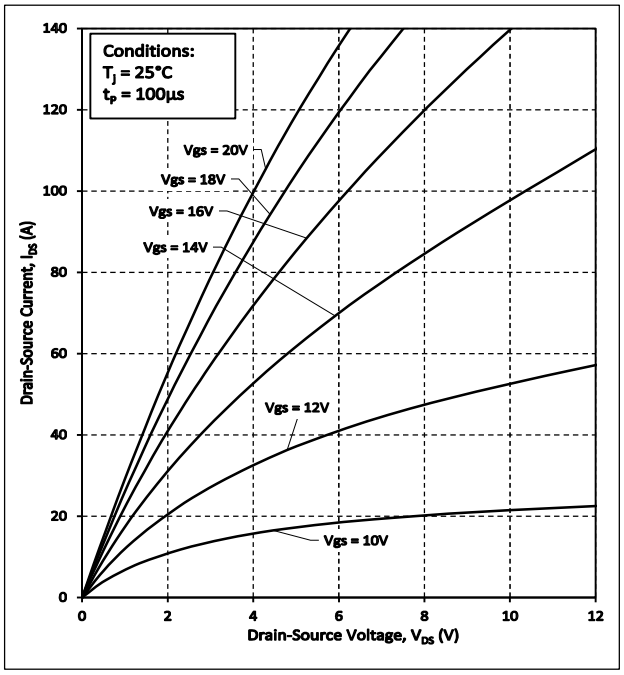


Figure 2. Output Characteristics T_j = 25°C

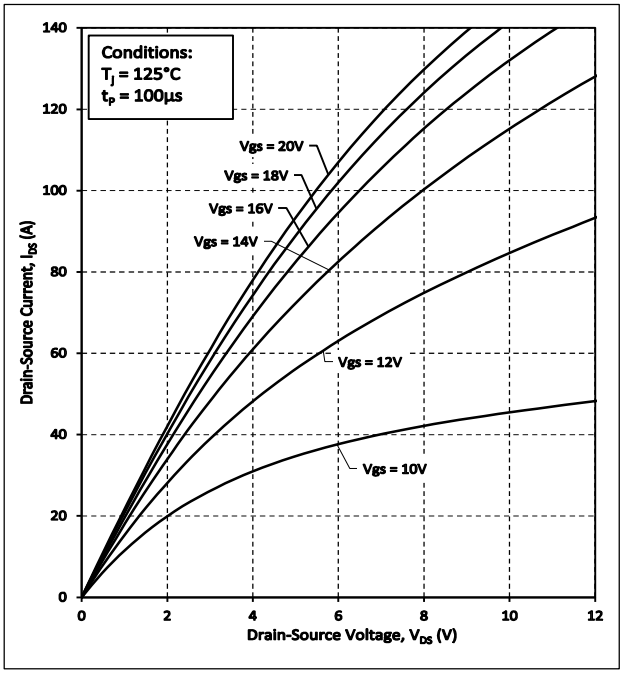


Figure 3. Output Characteristics T_j = 125°C

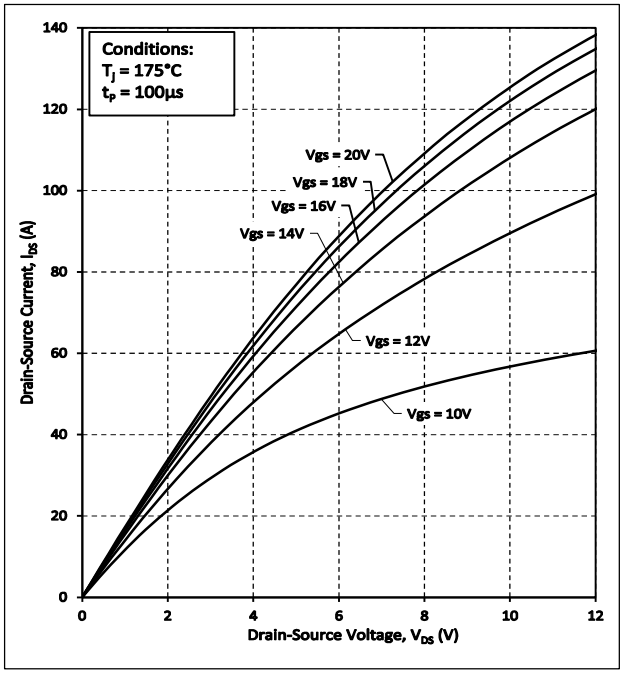


Figure 4. Output Characteristics T_j = 175°C

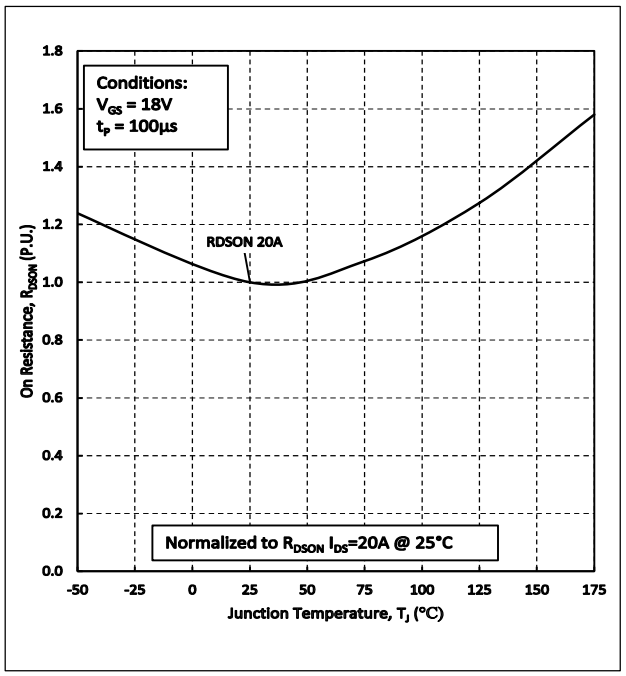


Figure 5. Normalized On-Resistance vs. Temperature

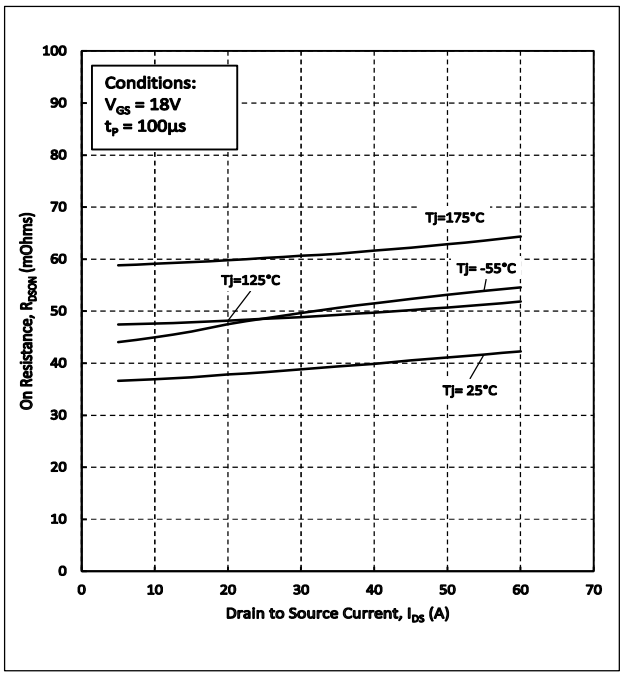


Figure 6. On-Resistance vs. Drain Current For Various Temperature

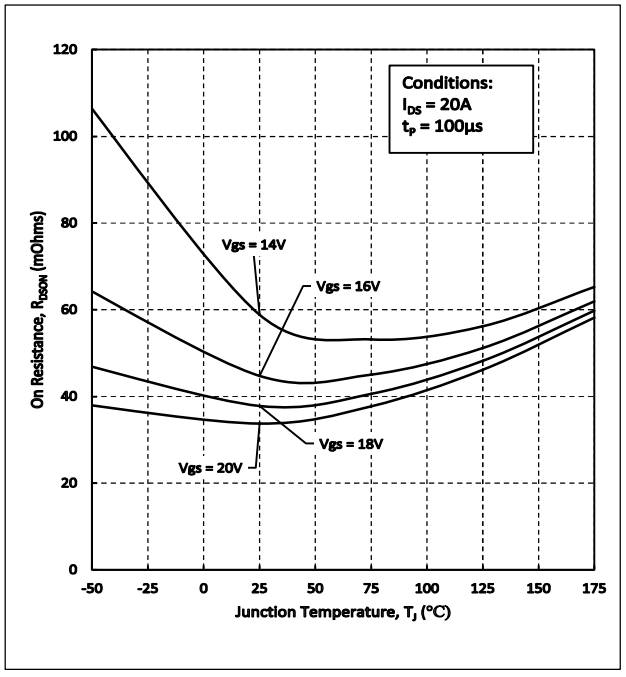


Figure 7. On-Resistance vs. Temperature For Various Gate Voltages

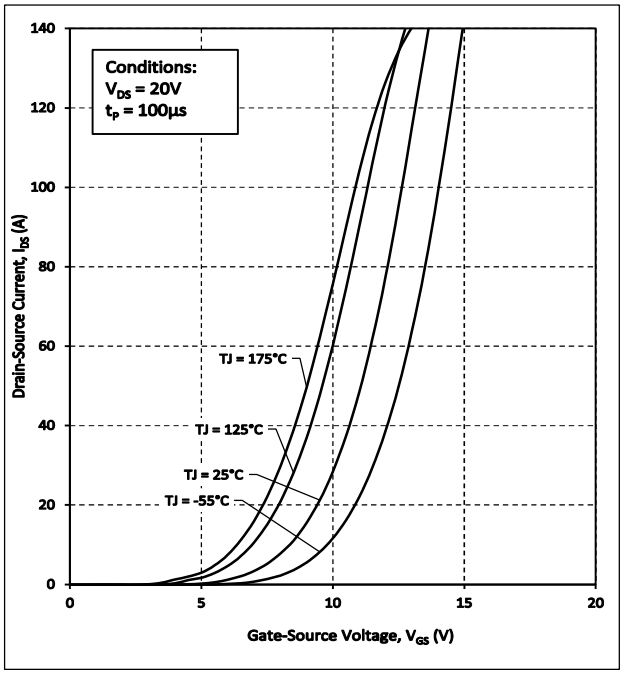


Figure 8. Transfer Characteristic for Various Junction Temperatures

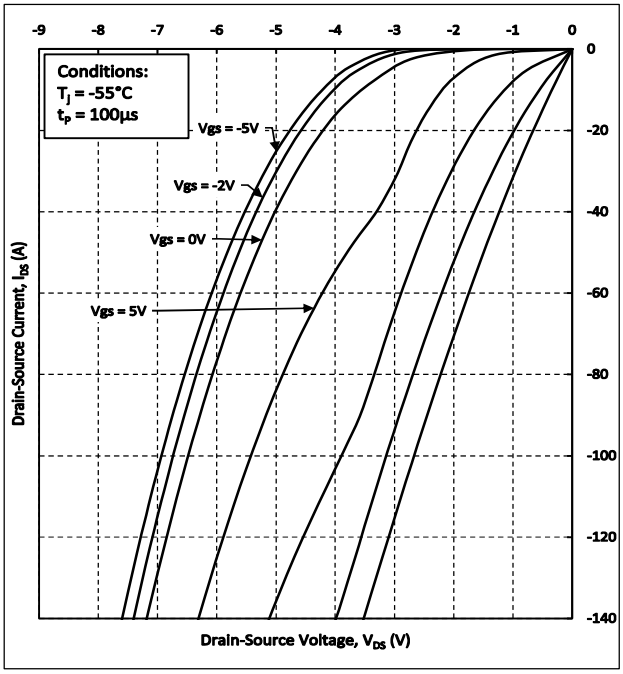


Figure 9. Body Diode Characteristics at T_j = -55°C

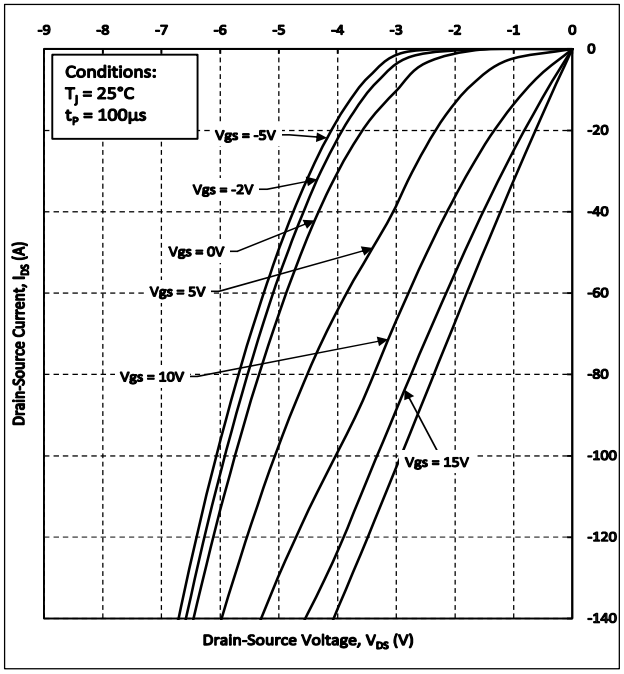


Figure 10. Body Diode Characteristics at T_j = 25°C

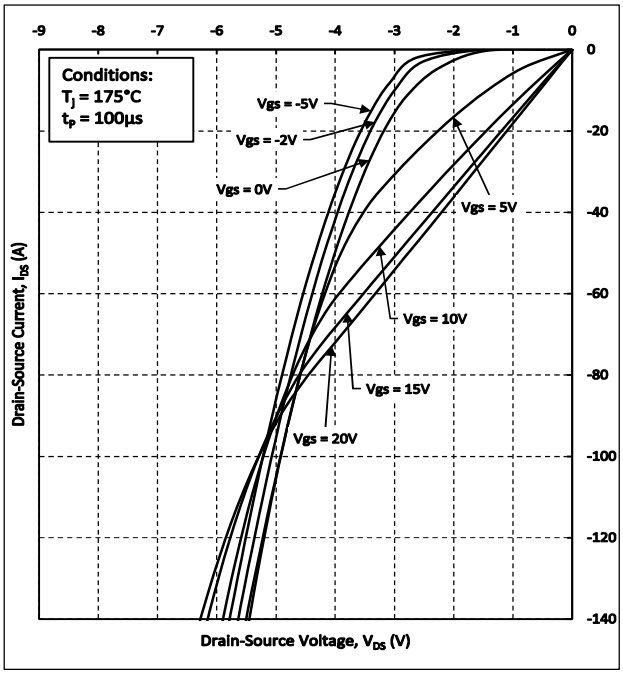


Figure 11. Body Diode Characteristics at T_j = 175°C

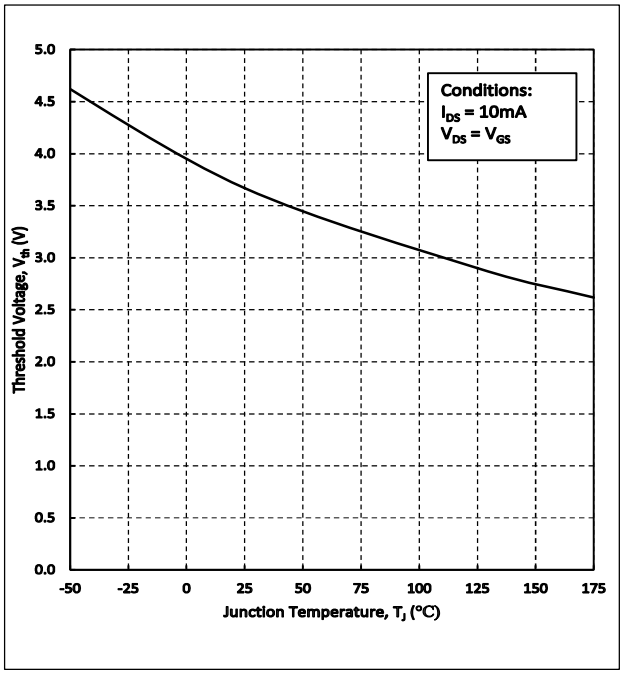


Figure 12. Threshold Voltage vs. Temperature

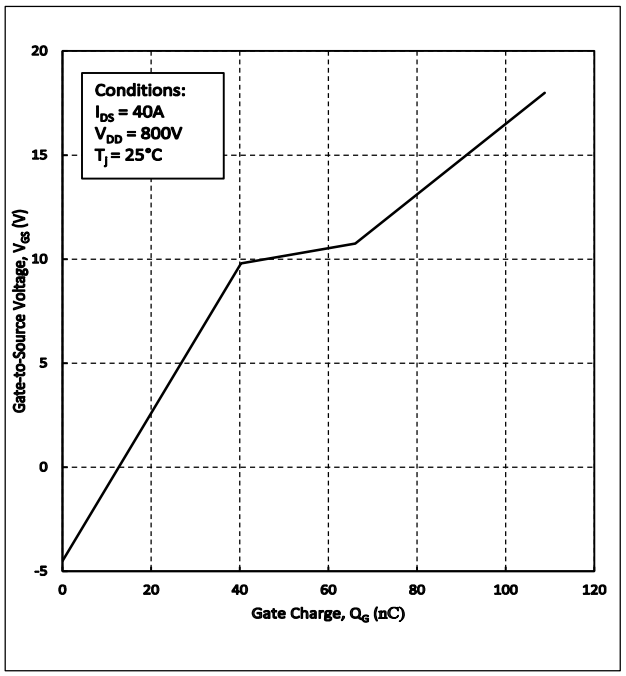


Figure 13. Gate Charge Characteristics

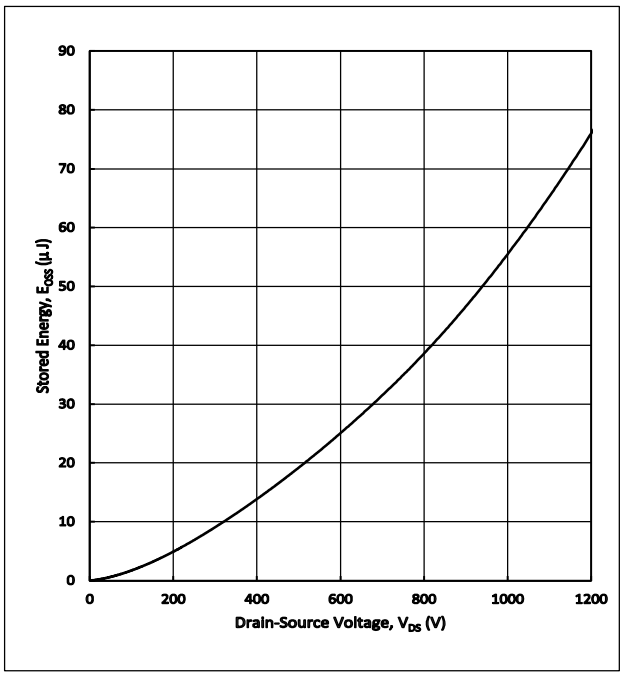


Figure 14. Output Capacitor Stored Energy

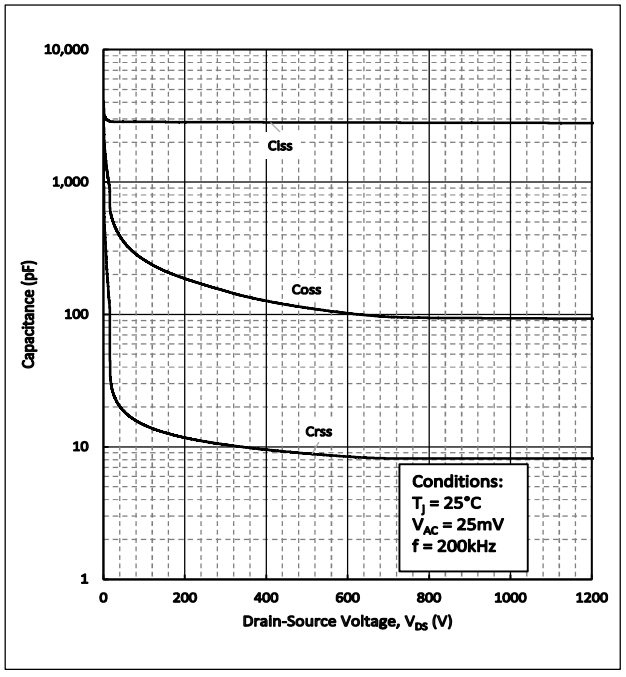


Figure 15. Capacitance vs Drain-Source Voltage

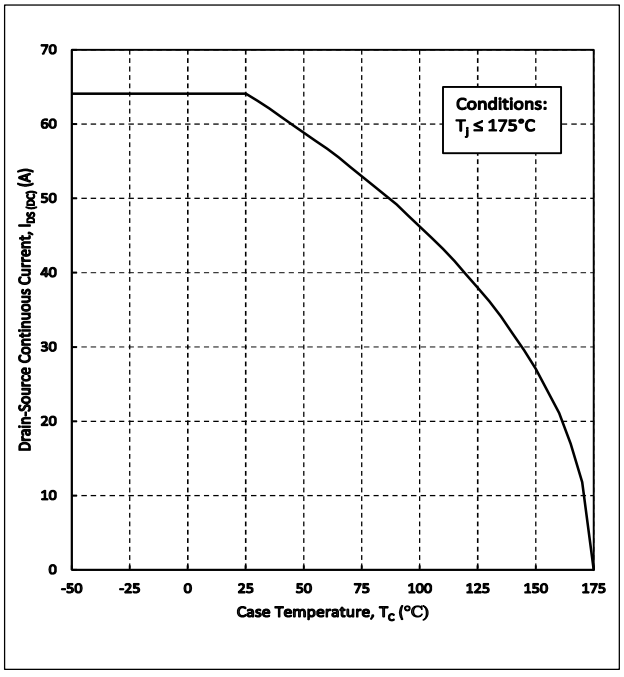


Figure 16. Continuous Drain Current Derating vs. Case Temperature

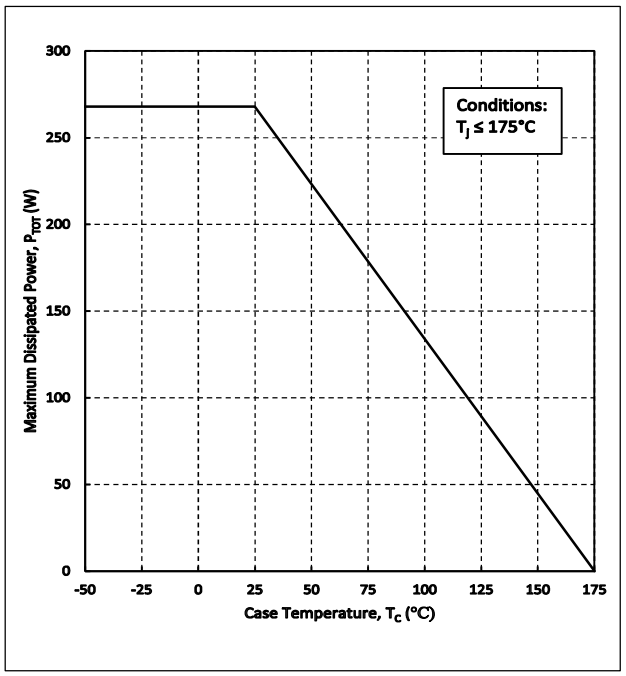


Figure 17. Maximum Power Dissipation Derating vs Case Temperature

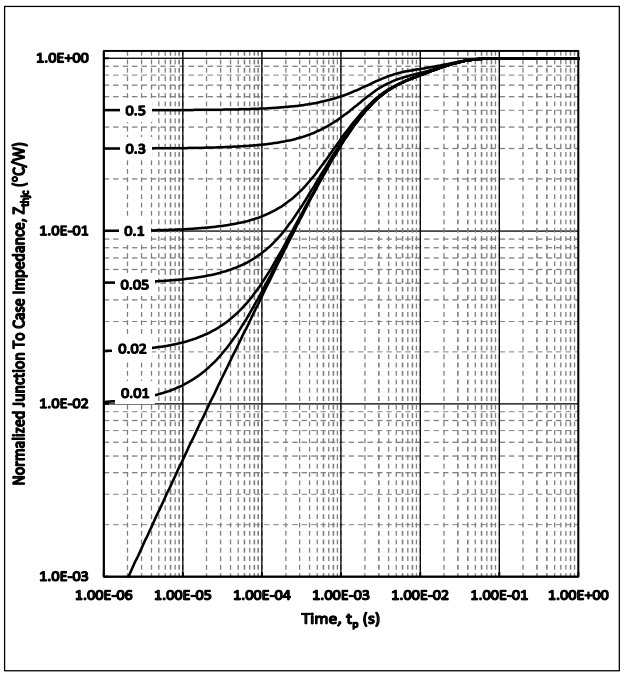


Figure 18. Transient Thermal impedance (Junction to Case)

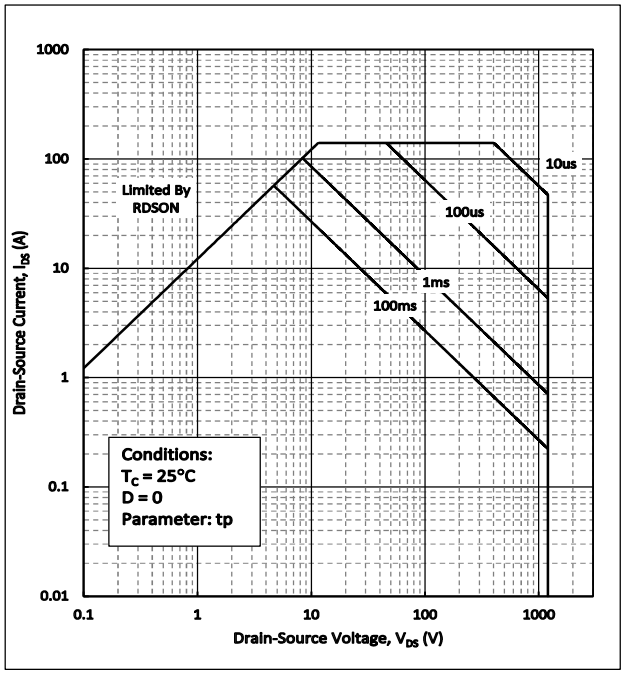


Figure 19. Safe Operating Area

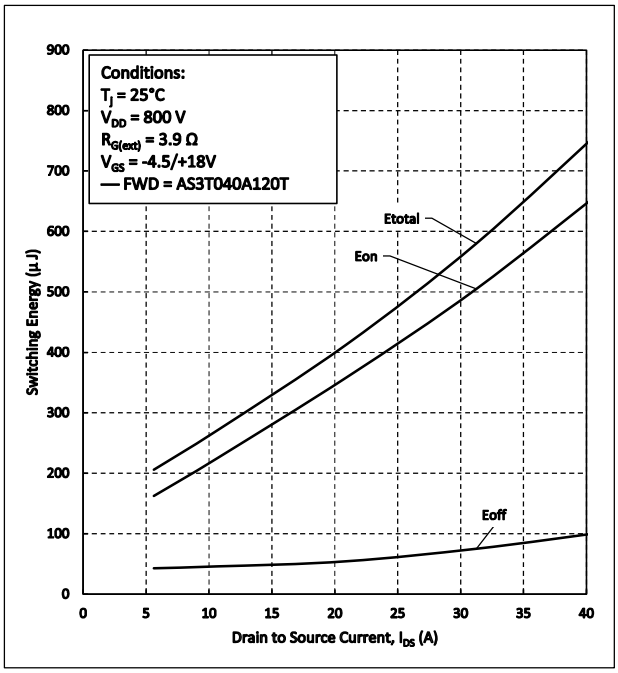


Figure 20. Clamped Inductive Switching Energy vs. Drain Current

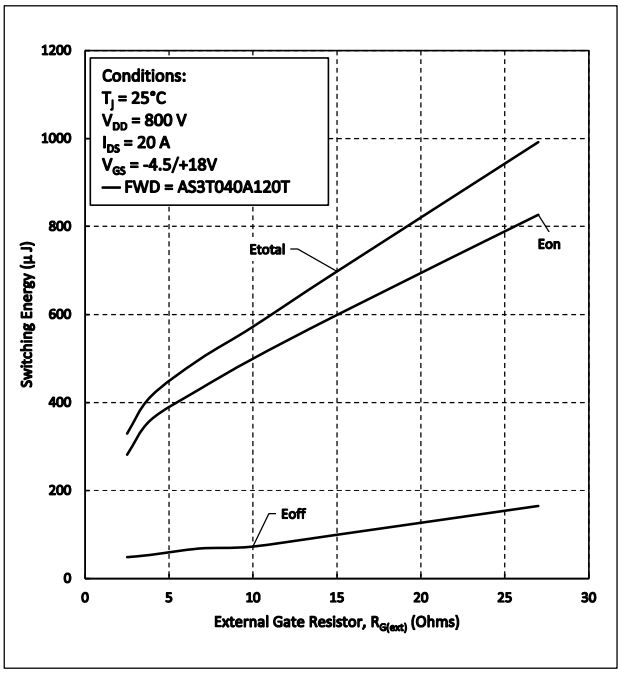


Figure 21. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

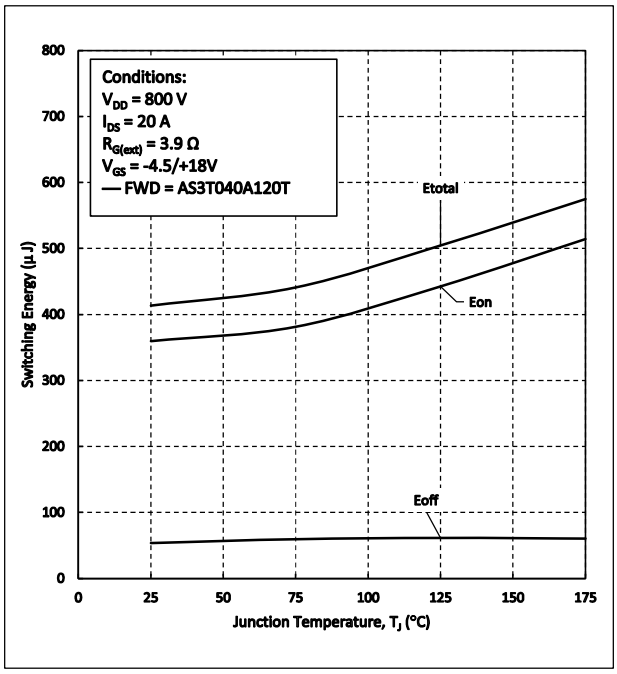


Figure 22. Clamped Inductive Switching Energy vs. Temperature

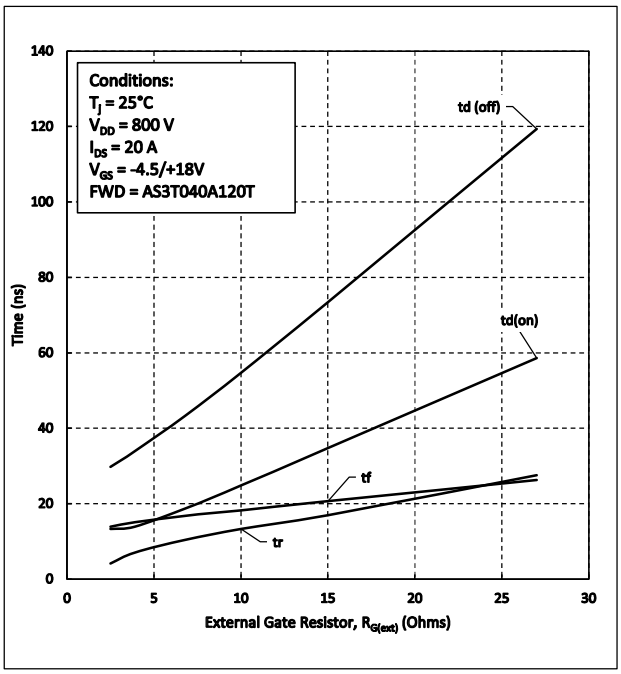


Figure 23. Switching Times vs $R_{G(ext)}$

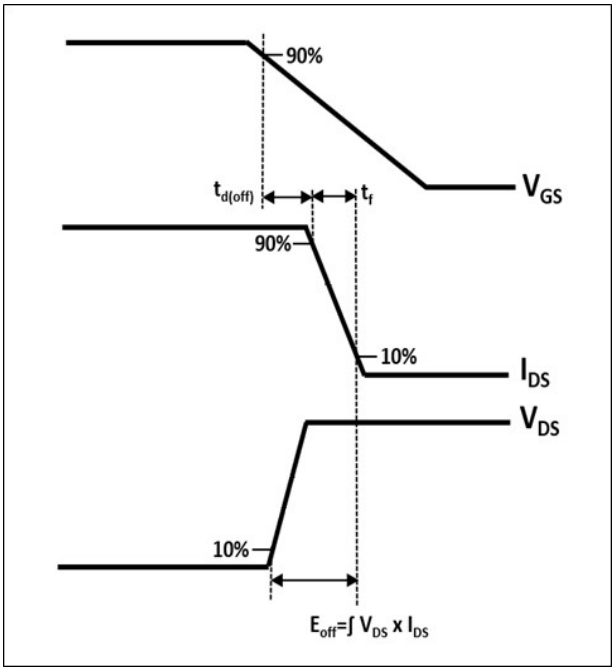


Figure 24. Turn-off Transient Definitions

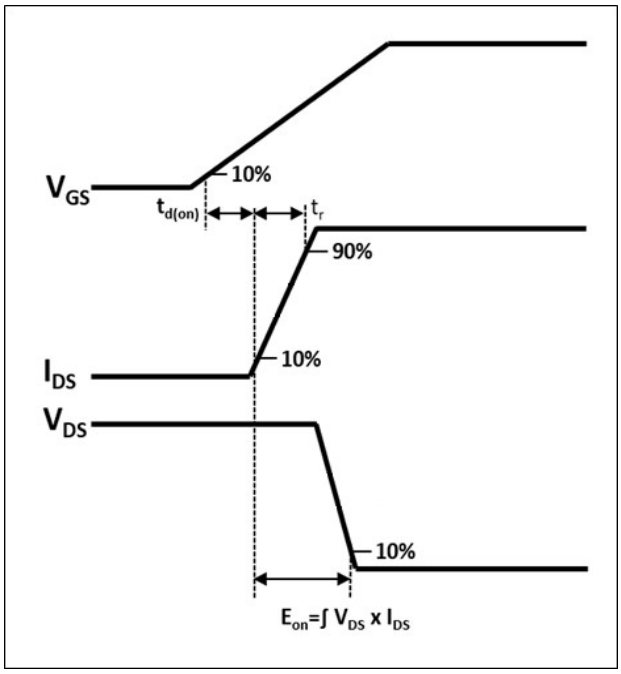


Figure 25. Turn-on Transient Definitions

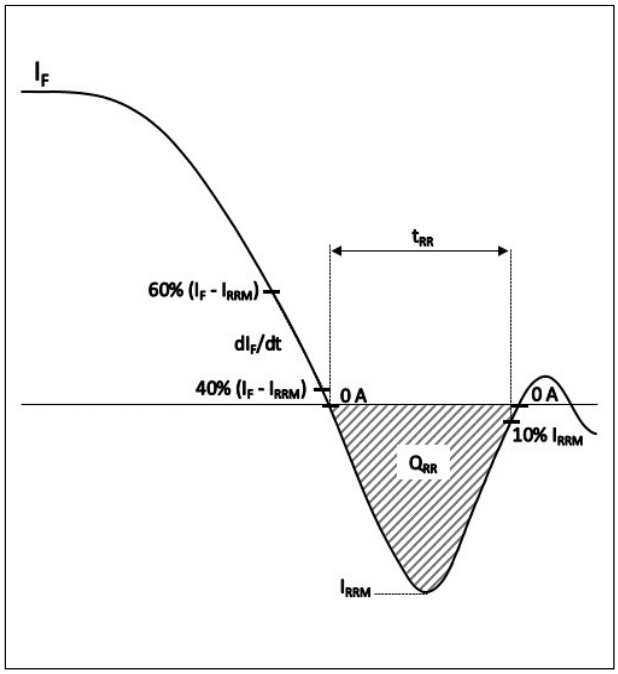
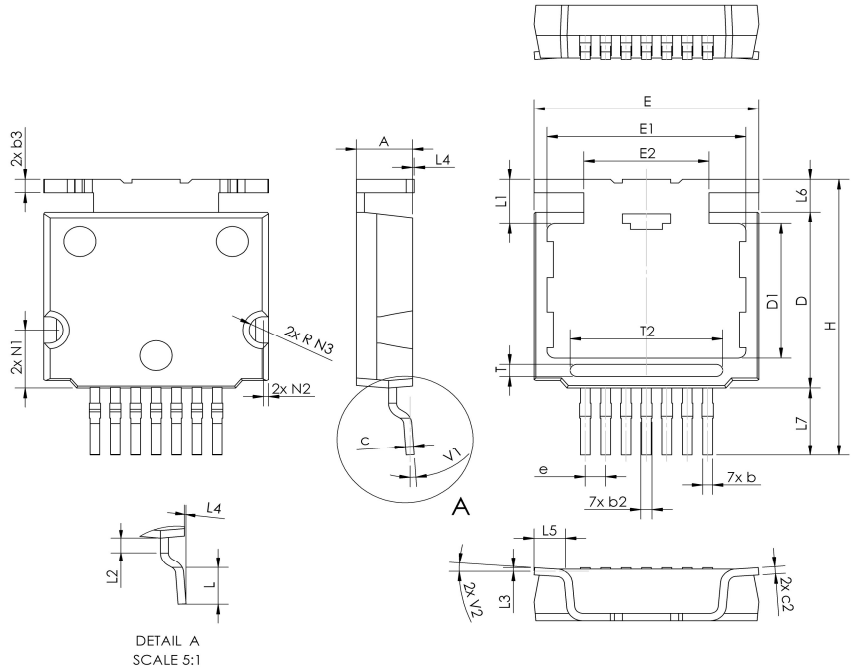


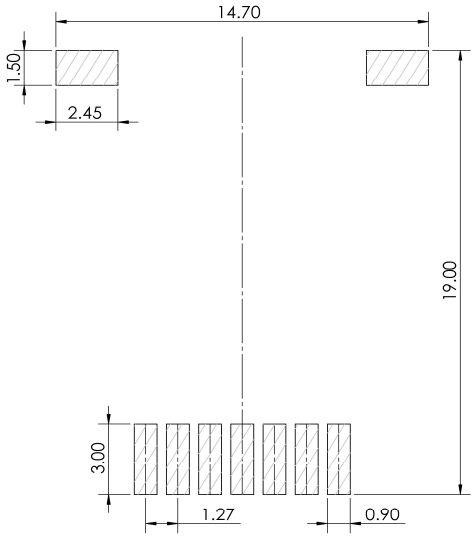
Figure 26. Reverse Recovery Definitions

Package Dimensions TCPAK



Sym	Millimeters		
	Min	Typ	Max
A	3.40	3.50	3.60
b	0.50	0.60	0.70
b2		0.70	
b3	0.80	0.90	1.00
c	0.40	0.50	0.60
c2	0.40	0.50	0.60
D	11.70	11.80	11.90
D1	8.80	9.00	9.10
E	13.90	14.00	14.10
E1	12.30	12.40	12.50
E2	7.75	7.80	7.85
e		1.27	
H	18.00	18.50	19.00
L	2.30	2.50	2.75
L1	-	3.05	-
L2		1.27	
L3	-	0.26	-
L4	0.00	0.10	0.25
L5	1.70	1.90	2.15
L6		2.20	
L7		4.50	
N1		3.90	
N2		0.30	
N3		0.90	
T		0.80	
T2		9.50	
V1		4°	
V2		4°	

Recommended Minimum Pad Dimensions



Warnings

AEC Qualification

Except as otherwise explicitly approved by SemiQ, Inc. in a written document signed by authorized representatives of SemiQ, SemiQ products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.

Notes

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented March, 2013. RoHS Declarations for this product can be obtained from the Product Documentation sections of www.SemiQ.com.

REACh Compliance

REACh substances of high concern (SVHC) information is available for this product. Since the European Chemicals Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future,please contact our office at SemiQ Headquarters in Lake Forest, California to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request. SemiQ Inc., reserves the right to make changes to the product specifications and data in this document without notice. SemiQ products are sold pursuant to SemiQ's terms and conditions of sale in place at the time of order acknowledgement.

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