

QSiC™ 1200V SiC MOSFET Power Module

Features

- High speed switching SiC MOSFETs
- All parts tested to greater than 1,400V
- Kelvin reference for stable operation
- Isolated backplate
- Avalanche tested to 800mJ

Benefits

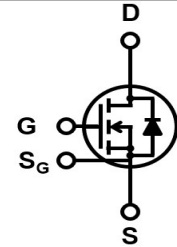
- Low switching losses
- Low junction to case thermal resistance
- Very rugged and easy mount
- Direct mounting to heatsink (isolated package)

Applications

- Photovoltaic Inverter
- Battery charger
- Server power supplies
- Energy storage system

V_{DS}	1200 V
$R_{DS,on}$	8.4 mΩ
I_D (TC=25°C)	188 A
$T_{J,max}$	175°C

Package



- (1) S_G (Driver Source)
 (2) G (Gate)
 (3) D (Drain)
 (4) S (Source)

Part #	Package	Marking
GCMX008C120S1-E1	SOT-227	GCMX008C120S1-E1



Absolute Maximum Ratings

Characteristics	Symbol	Conditions	Values	Unit
Drain-Source Voltage	V_{rated}	$V_{GS}=0V$, $I_{DS}=2\mu A$	1200	V
Continuous Drain Current	I_{DS}	$T_C=25^\circ C$, $T_J=175^\circ C$, $V_{GS}=18V$	188	A
		$T_C=100^\circ C$, $T_J=175^\circ C$, $V_{GS}=18V$	134	
Body Diode Drain Current	I_{SD}	$T_C=25^\circ C$, $T_J=175^\circ C$, $V_{GS}=-5V$	133	
Pulsed Drain Current	$I_{DS,pulse}^*$	$T_C=25^\circ C$, $V_{GS}=18V$	700	
Gate Source Voltage	V_{GSmax}		-8/22	V
	V_{GSop}	Recommended operational	-4.5/18	
Power Dissipation	P_{tot}	$T_C=25^\circ C$	536	W
Operating & Storage Temperature	T_J , $T_{storage}$	Continuous	-55...175	°C
Single Pulse Avalanche Energy	E_{AS}	$L=1.0mH$, $I_{AS}=40.0A$, $V=50V$	800	mJ

*Pulse width is limited by T_{Jmax}

QSiC™ 1200V SiC MOSFET Power Module**GCMX008C120S1-E1****Static Electrical Characteristics**, at $T_J=25^{\circ}\text{C}$, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Drain-Source Breakdown Voltage	BV_{DSS}	$I_{DS}=1\text{mA}$	1200	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=1200\text{V}$, $V_{GS}=0\text{V}$	-	0.2	2	μA
		$V_{DS}=1200\text{V}$, $V_{GS}=0\text{V}$, $T_J=175^{\circ}\text{C}$	-	2	-	
Gate-Source Leakage Current	I_{GSS+}	$V_{GS}=22\text{V}$, $V_{DS}=0\text{V}$	-	20	200	nA
	I_{GSS-}	$V_{GS}=-8\text{V}$, $V_{DS}=0\text{V}$	-	-20	-200	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}$, $I_{DS}=40\text{mA}$	1.8	3.4	4	V
		$V_{GS}=V_{DS}$, $I_{DS}=40\text{mA}$, $T_J=175^{\circ}\text{C}$	-	2.4	-	
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=18\text{V}$, $I_{DS}=100\text{A}$	-	8.4	12	m Ω
		$V_{GS}=18\text{V}$, $I_{DS}=100\text{A}$, $T_J=125^{\circ}\text{C}$	-	11.3	-	
		$V_{GS}=18\text{V}$, $I_{DS}=100\text{A}$, $T_J=175^{\circ}\text{C}$	-	14.1	-	
Transconductance	g_{fs}	$V_{DS}=20\text{V}$, $I_{DS}=100\text{A}$	-	50	-	S
Internal Gate Resistance	$R_{G(int)}$	$f=1\text{MHz}$, $V_{AC}=25\text{mV}$, D-S Short	-	1.5	-	Ω

AC Electrical Characteristics, at $T_J=25^{\circ}\text{C}$, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}$, $V_{DS}=1000\text{V}$, $f=200\text{kHz}$, $V_{AC}=25\text{mV}$	-	14067	-	pF
Output Capacitance	C_{OSS}		-	418	-	
Reverse Transfer Capacitance	C_{RSS}		-	32	-	
Coss Stored Energy	E_{OSS}^*		-	247	-	μJ
Turn-On Switching Energy	E_{ON}	$V_{DD}=800\text{V}$, $I_{DS}=100\text{A}$, $R_{G(ext)}=2.5\Omega$, $V_{GS}=-4.5/+18\text{V}$, $L=90\mu\text{H}$, FWD=GCMX008C120S1-E1	-	4.17	-	mJ
Turn-Off Switching Energy	E_{OFF}		-	0.82	-	
Turn-On Delay Time	$t_{D(on)}$		-	35	-	ns
Rise Time	t_R		-	36	-	
Turn-Off Delay Time	$t_{D(off)}$		-	89	-	
Fall Time	t_F		-	28	-	
Total Gate Charge	Q_G	$V_{DD}=800\text{V}$, $I_{DS}=100\text{A}$, $V_{GS}=-4.5/+18\text{V}$	-	506	-	nC
Gate to Source Charge	Q_{GS}		-	189	-	
Gate to Drain Charge	Q_{GD}		-	90	-	

* E_{OSS} is calculated from C_{OSS} curve**Body Diode Characteristics**, at $T_J=25^{\circ}\text{C}$, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Diode Forward Voltage	V_{SD}	$V_{GS}=-5\text{V}$, $I_{SD}=50\text{A}$	-	3.8	-	V
Reverse Recovery Time	t_{RR}	$I_{SD}=100\text{A}$, $V_R=800\text{V}$, $V_{GS}=-4.5/+18\text{V}$, $di_F/dt=2.5\text{A/ns}$	-	48	-	ns
Reverse Recovery Charge	Q_{RR}		-	776	-	nC
Peak Reverse Recovery Current	I_{RRM}		-	32	-	A
Reverse Recovery Energy	E_{RR}		-	0.20	-	mJ

Thermal and Package Characteristics, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Thermal resistance, junction-case	R_{thJC}		-	0.23	0.28	$^\circ\text{C/W}$
Mounting torque	M_d	M4-0.7 screws	1.1	-	1.5	N-m
Terminal connection torque	M_{dt}	M4-0.7 screws	-	1.1	1.3	
Package weight	W_t		-	32	-	g
Isolation voltage	V_{ISOL}	$I_{ISOL} < 1\text{mA}$, 50/60 Hz, 2 s	4000	-	-	V

Typical Performance

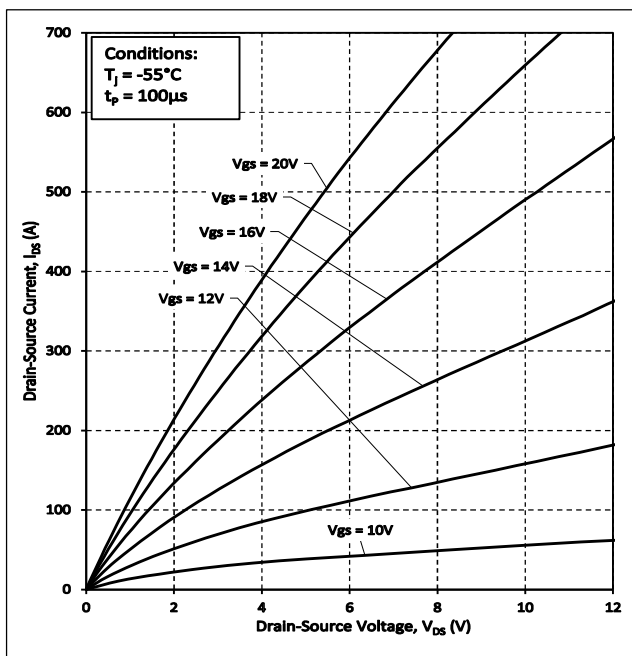


Figure 1. Output Characteristics $T_j = -55^\circ\text{C}$

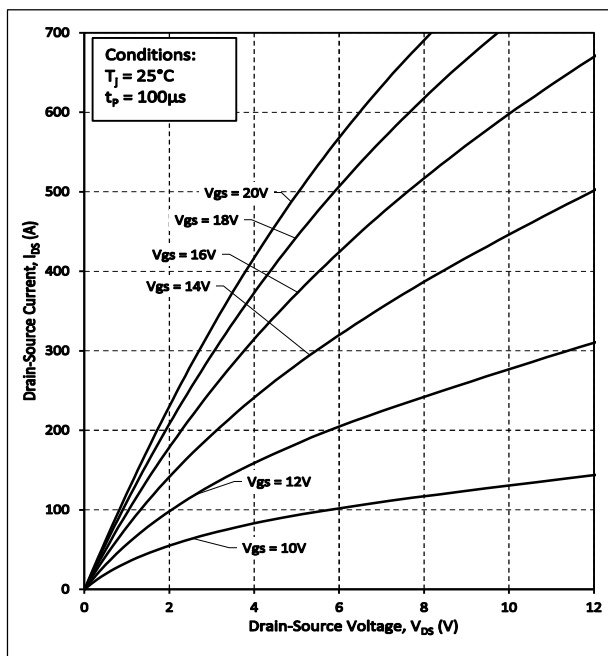


Figure 2. Output Characteristics $T_j = 25^\circ\text{C}$

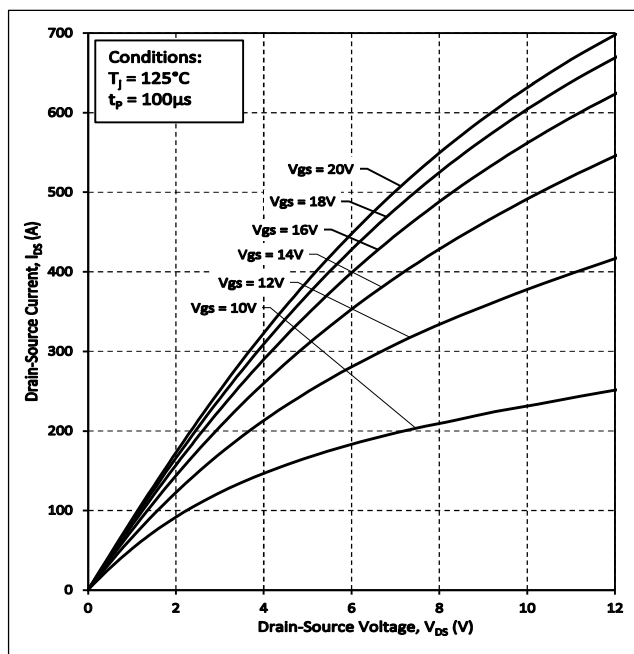


Figure 3. Output Characteristics $T_j = 125^\circ\text{C}$

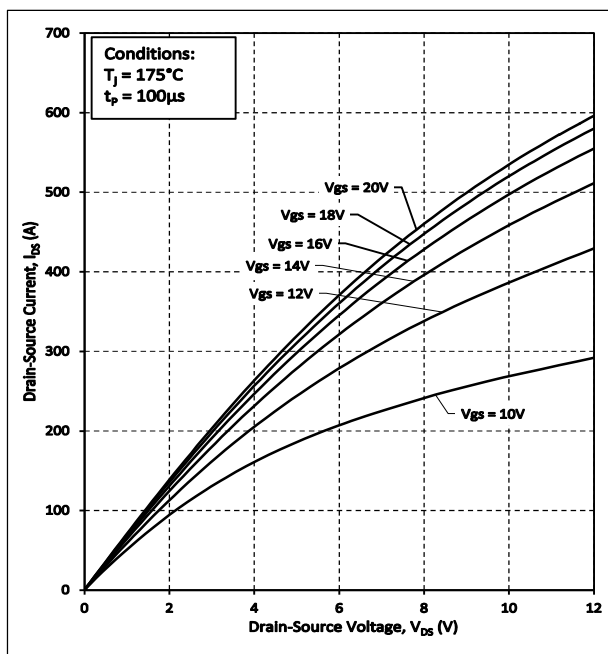


Figure 4. Output Characteristics $T_j = 175^\circ\text{C}$

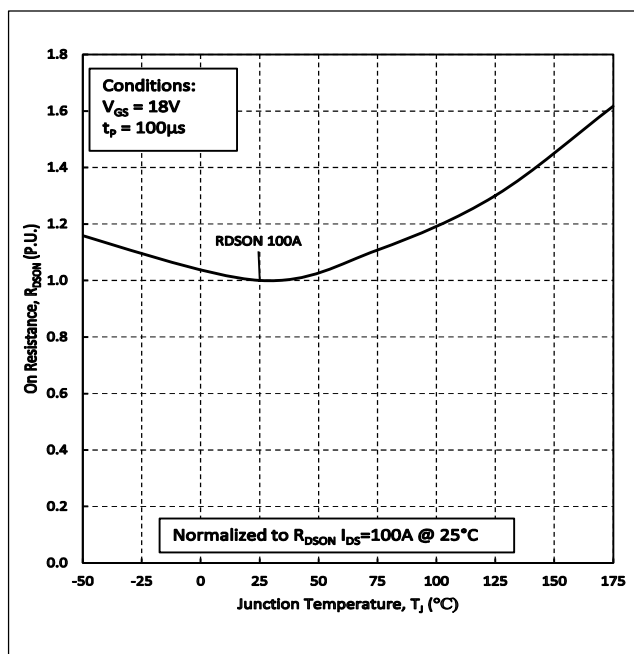


Figure 5. Normalized On-Resistance vs. Temperature

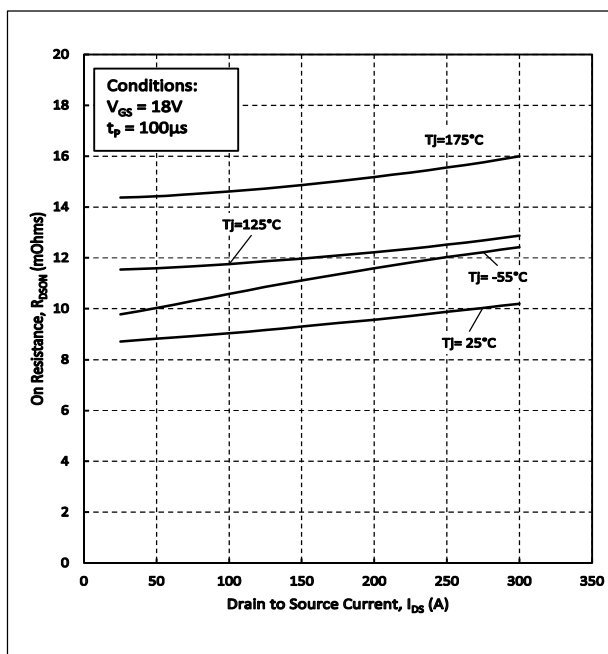


Figure 6. On-Resistance vs. Drain Current For Various Temperature

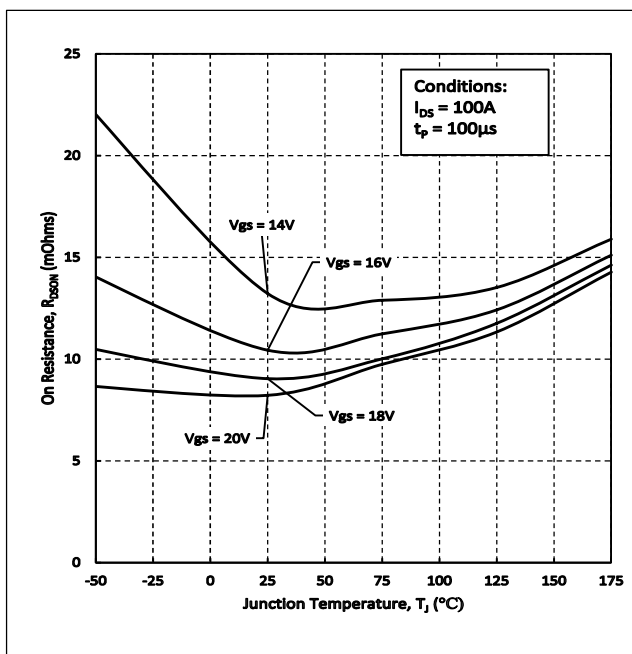


Figure 7. On-Resistance vs. Temperature For Various Gate Voltages

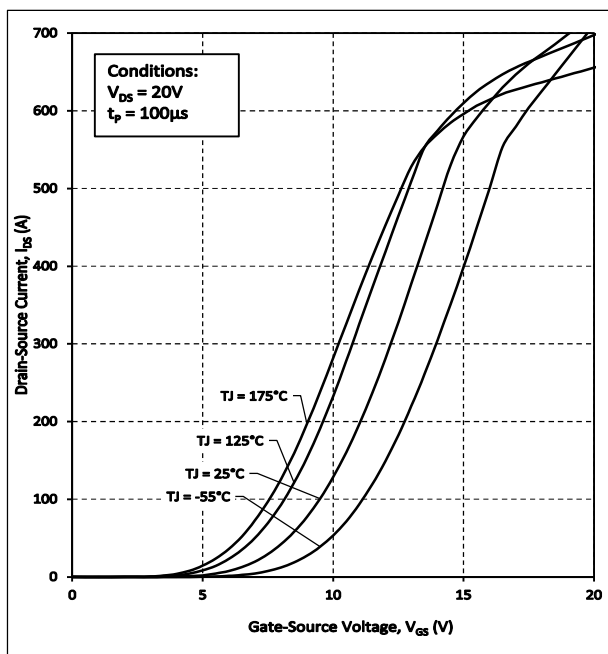


Figure 8. Transfer Characteristic for Various Junction Temperatures

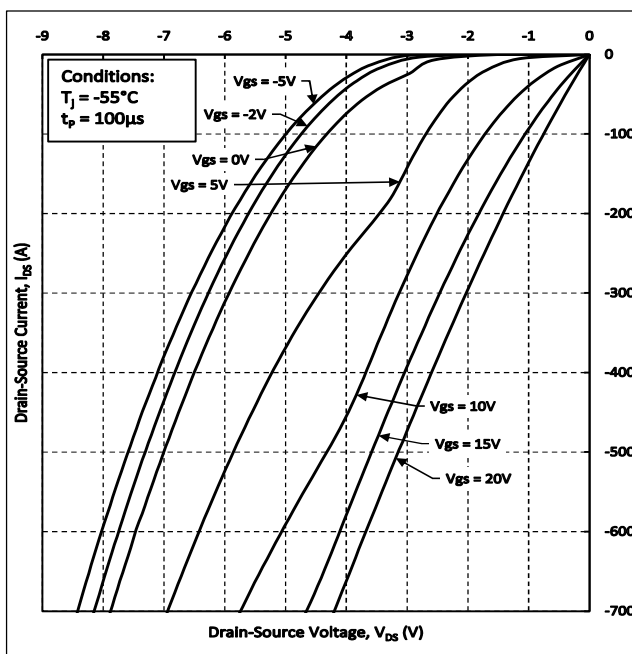


Figure 9. Body Diode Characteristics at $T_J = -55^\circ\text{C}$

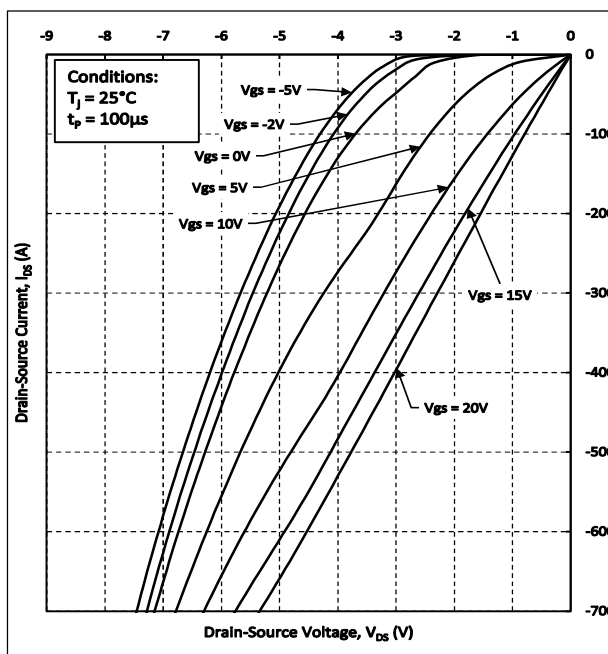


Figure 10. Body Diode Characteristics at $T_J = 25^\circ\text{C}$

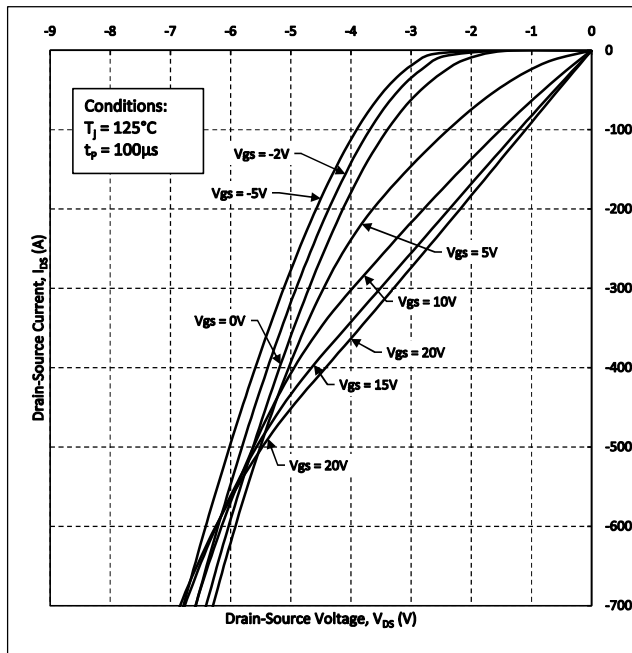


Figure 11. Body Diode Characteristics at $T_j = 125^\circ\text{C}$

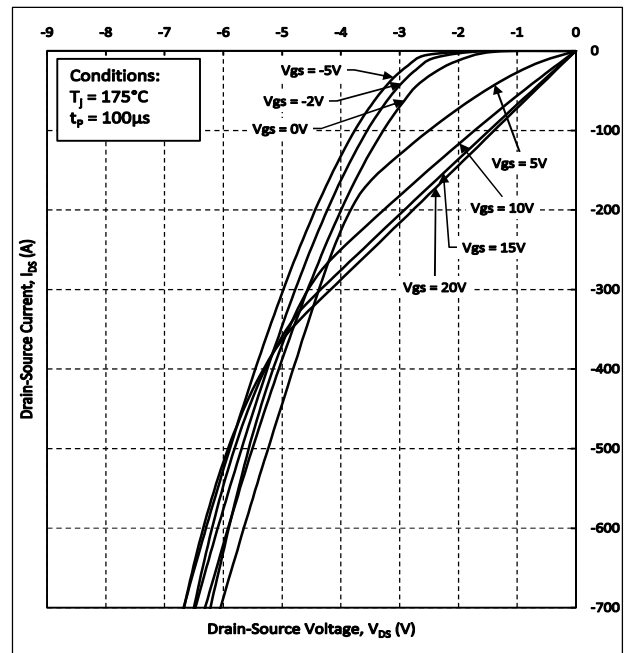


Figure 12. Body Diode Characteristics at $T_j = 175^\circ\text{C}$

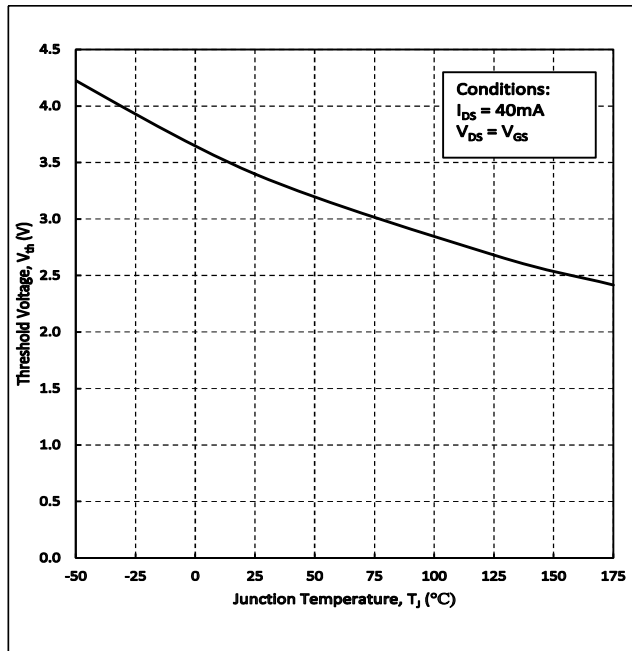


Figure 13. Threshold Voltage vs. Temperature

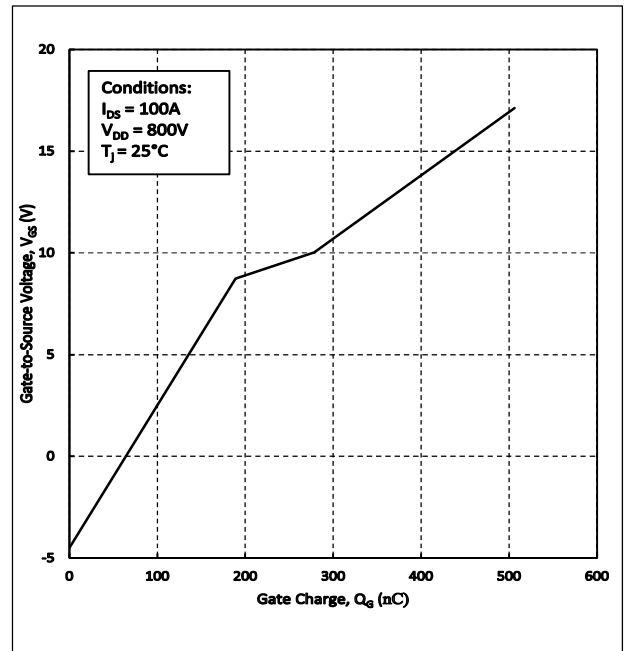


Figure 14. Gate Charge Characteristics

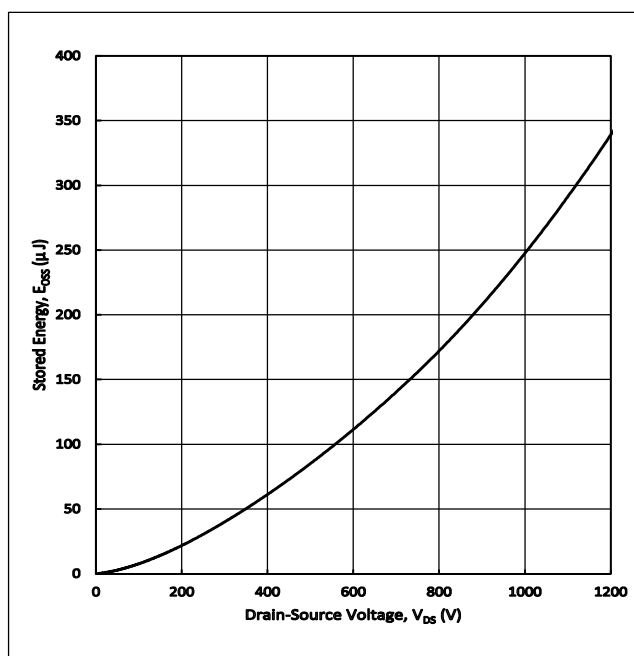


Figure 15. Output Capacitor Stored Energy

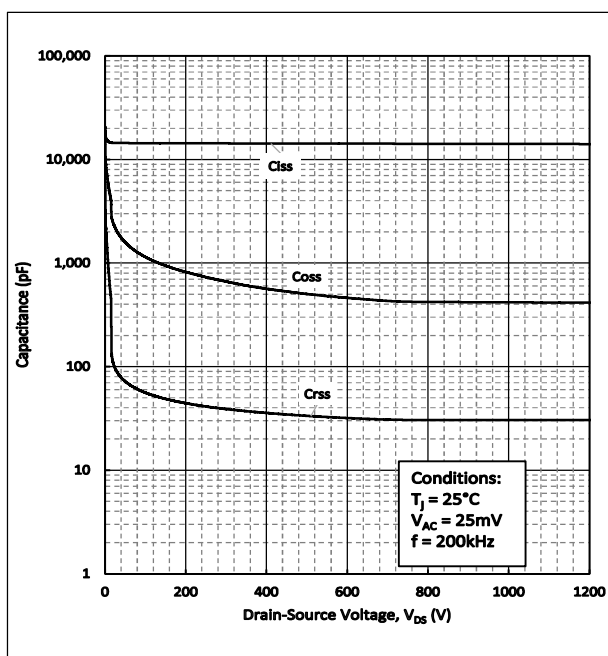


Figure 16. Capacitance vs Drain-Source Voltage

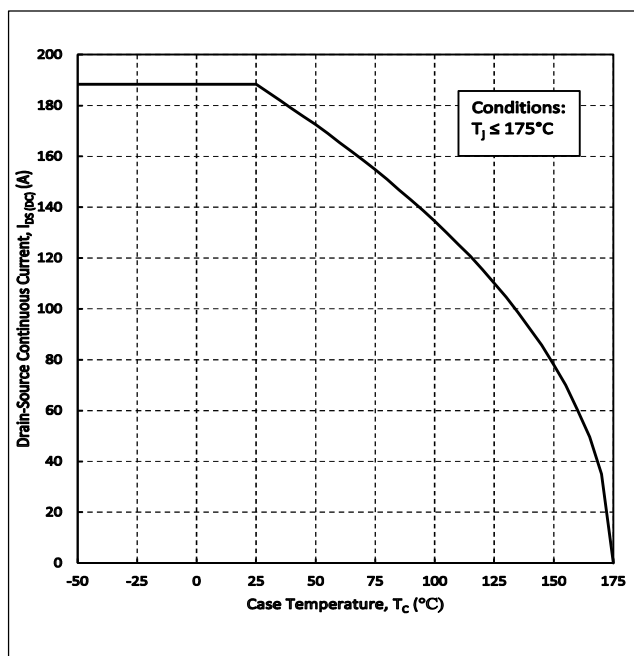


Figure 17. Continuous Drain Current Derating vs. Case Temperature

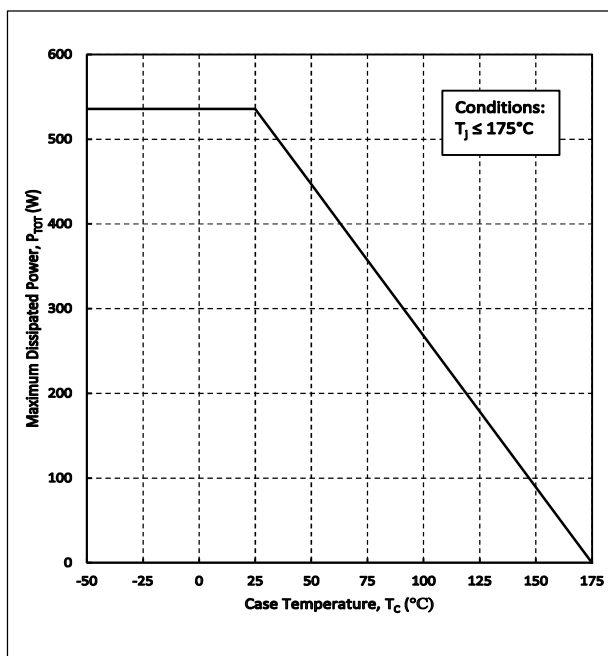


Figure 18. Maximum Power Dissipation Derating vs Case Temperature

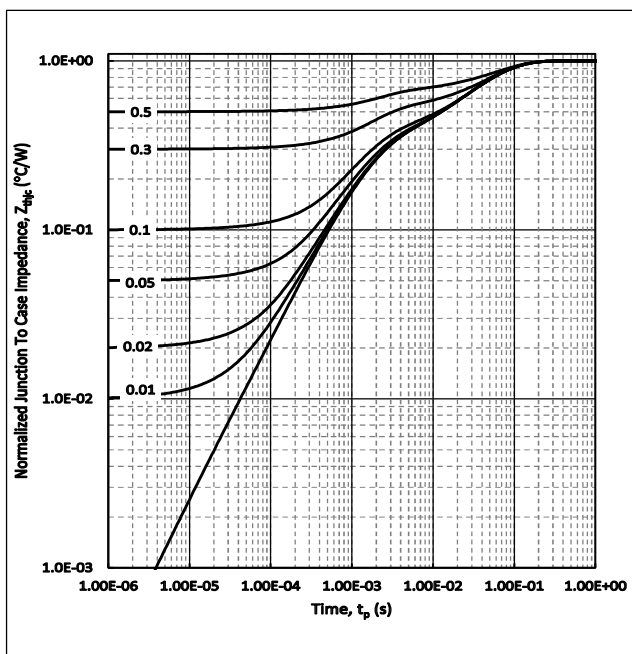


Figure 19. Transient Thermal impedance (Junction to Case)

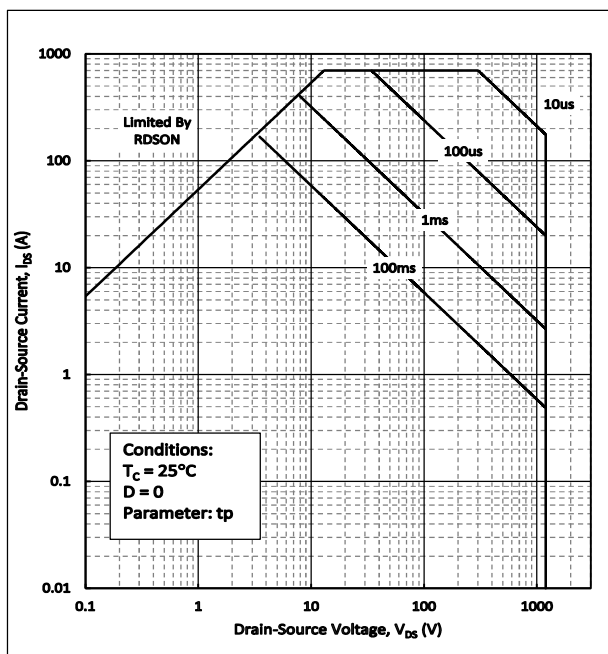


Figure 20. Safe Operating Area

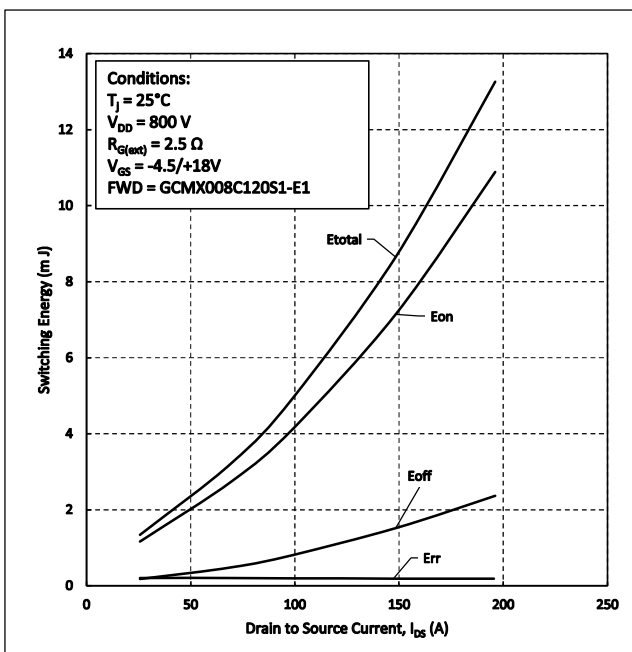


Figure 21. Clamped Inductive Switching Energy vs. Drain Current

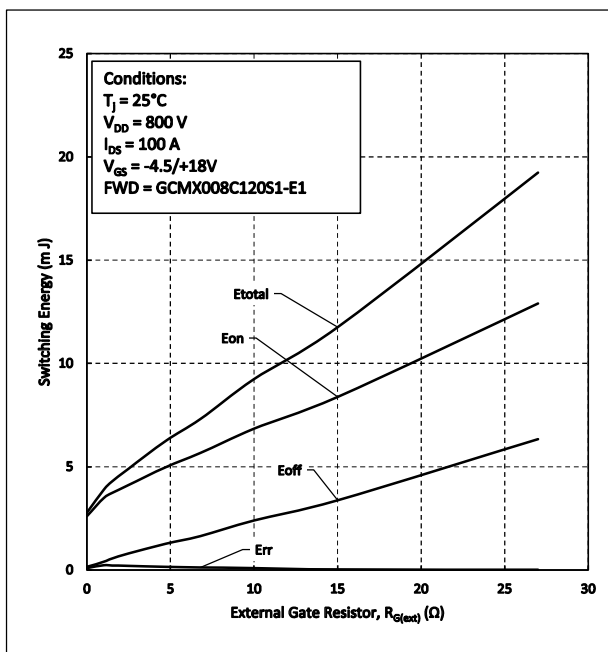


Figure 22. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

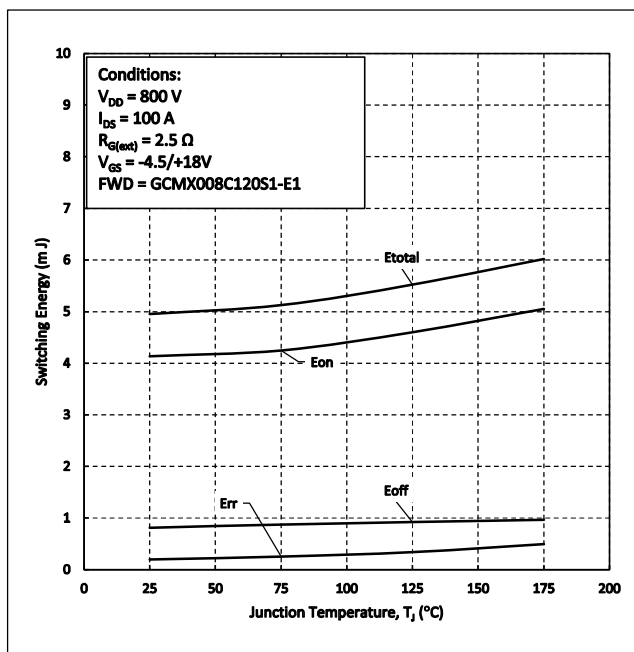


Figure 23. Clamped Inductive Switching Energy vs. Temperature

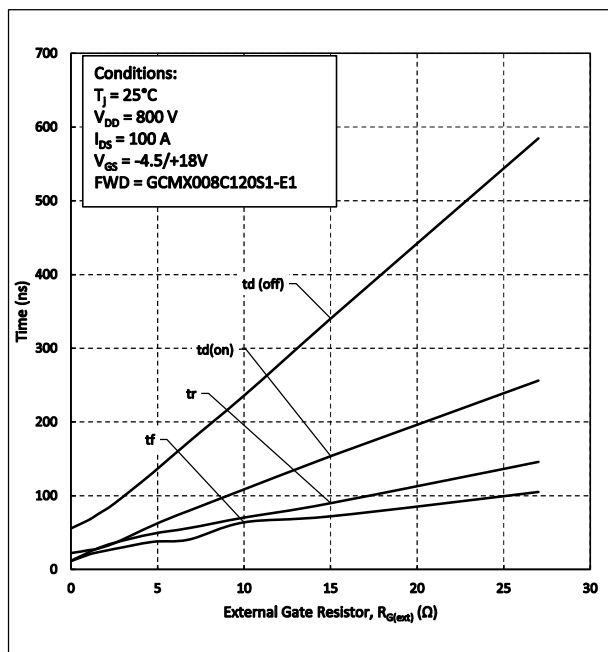


Figure 24. Switching Times vs $R_{G(ext)}$

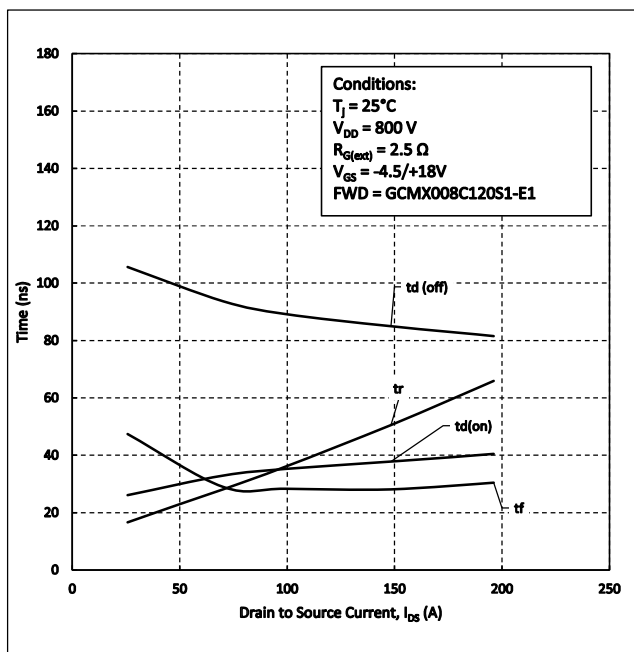


Figure 25. Switching Times vs. Drain Current

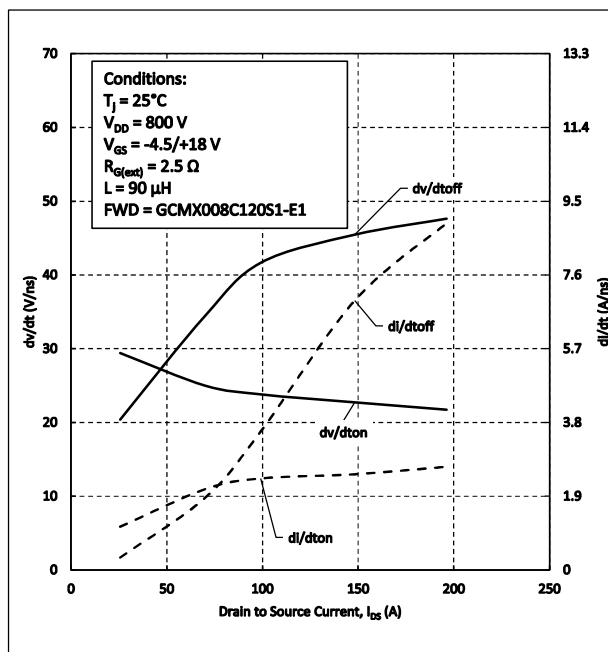


Figure 26. dv/dt and di/dt vs. Drain Current

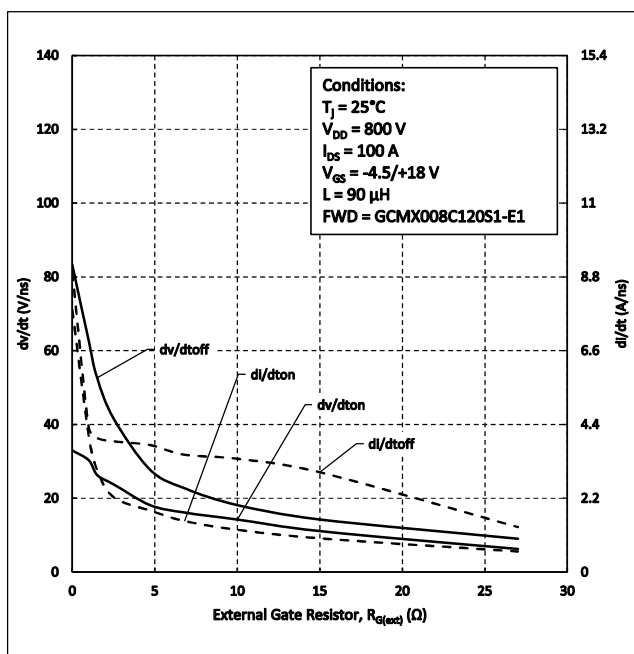


Figure 27. dv/dt and di/dt vs. $R_{G(\text{ext})}$

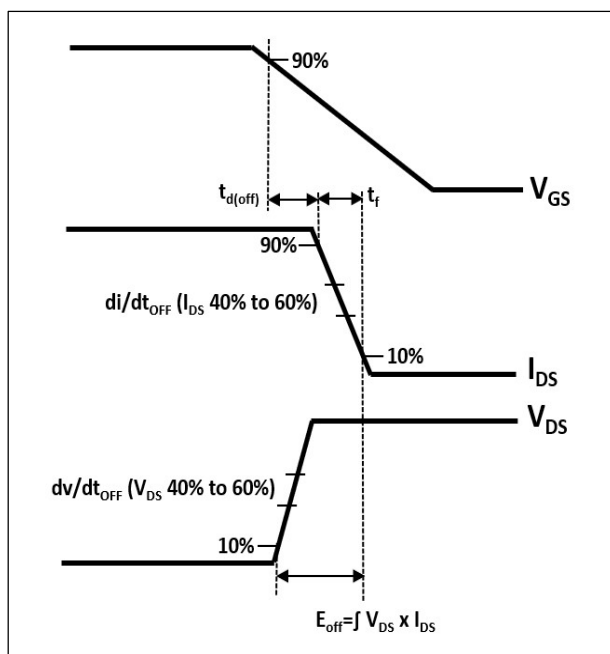


Figure 28. Turn-off Transient Definitions

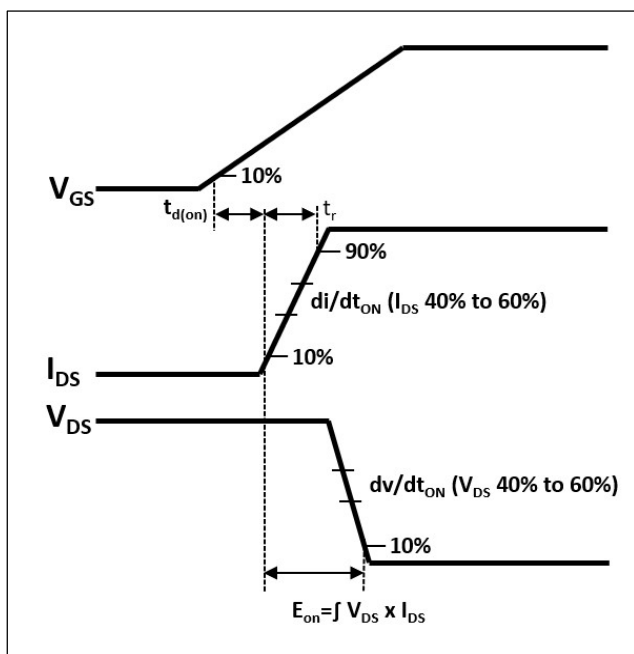


Figure 30. Turn-on Transient Definitions

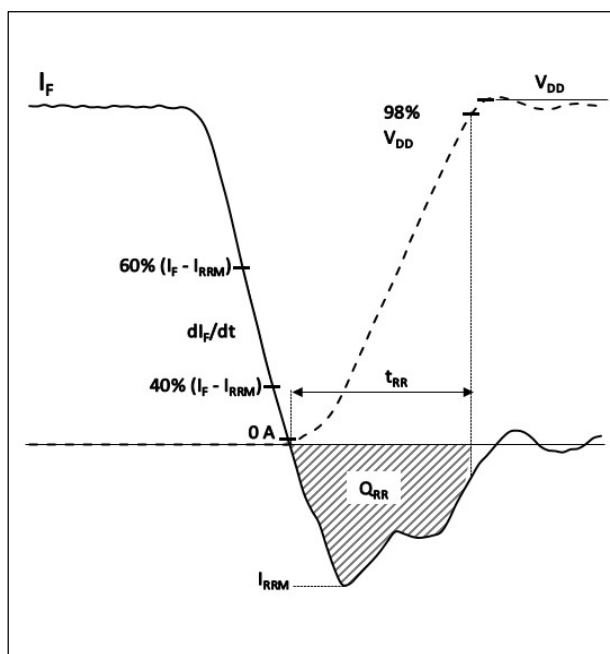
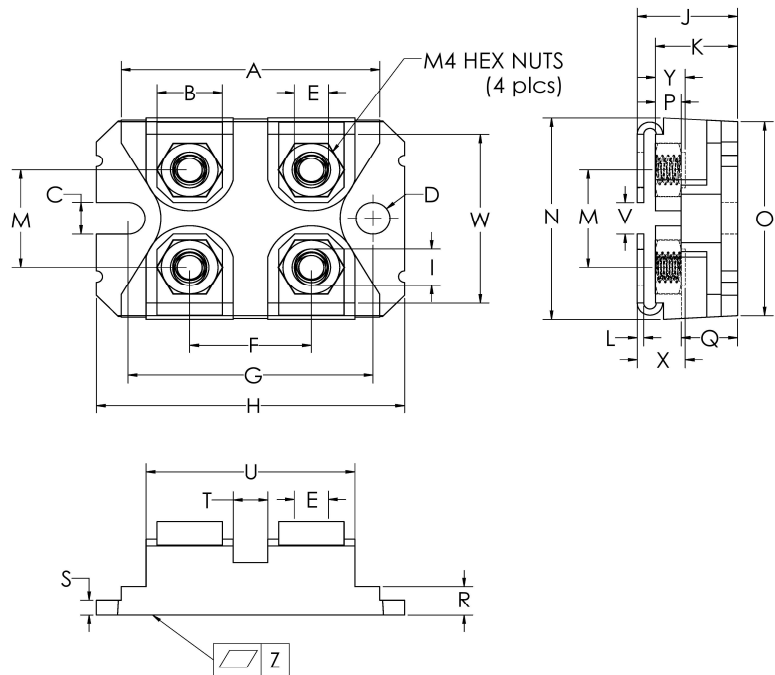


Figure 31. Reverse Recovery Definitions

QSiC™ 1200V SiC MOSFET Power Module

GCMX008C120S1-E1

Package Dimensions SOT-227



Sym	Millimeters		Inches	
	Min	Max	Min	Max
A	31.67	31.90	1.247	1.256
B	7.95	8.18	0.313	0.322
C	4.14	4.24	0.163	0.167
D	4.14	4.24	0.163	0.167
E	4.14	4.24	0.163	0.167
F	14.94	15.09	0.588	0.594
G	30.15	30.25	1.187	1.191
H	38.00	38.10	1.496	1.500
I	4.75	4.83	0.187	0.190
J	11.68	12.19	0.460	0.480
K	9.45	9.60	0.372	0.378
L	0.76	0.84	0.030	0.033
M	12.62	12.88	0.497	0.507
N	25.15	25.30	0.990	0.996
O	24.79	25.04	0.976	0.986
P	3.02	3.15	0.119	0.124
Q	6.71	6.96	0.264	0.274
R	4.17	4.42	0.164	0.174
S	2.08	2.13	0.082	0.084
T	3.28	3.63	0.129	0.143
U	26.75	26.90	1.053	1.059
V	3.86	4.24	0.152	0.167
W	20.55	26.90	0.809	0.814
X	5.45	5.85	0.215	0.230
Y	3.15	3.66	0.124	0.144
Z	0.00	0.13	0.000	0.005

Revision History		
Date	Revision	Notes
4/4/2025	1.0	Initial release

Notes

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented March, 2013. RoHS Declarations for this product can be obtained from the Product Documentation sections of www.SemiQ.com.

REACH Compliance

REACH substances of high concern (SVHC) information is available for this product. Since the European Chemicals Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact our office at SemiQ Headquarters in Lake Forest, California to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request. SemiQ Inc., reserves the right to make changes to the product specifications and data in this document without notice. SemiQ products are sold pursuant to SemiQ's terms and conditions of sale in place at the time of order acknowledgement.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control.

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Qualification

SemiQ qualification complies with JEDEC Standard conditions. This includes Temperature Cycle JESD22-A104 Condition G.