Powering the SiC evolution™

GCMX3P5B120S7B1

V_{DS}	1200 V
$R_{DS,on}$	3.6 mΩ
$I_{D\ (TC=25C)}$	361 A
T_J,max	175°C

QSiC™ 1200V SiC Half-Bridge Module

Features

- 62mm footprint with reduced package height (17 mm)
- High speed switching SiC MOSFETs
- All parts tested to greater than 1,350V
- Kelvin reference for stable operation
- Isolated baseplate

Package

Benefits

- Lower inductance from reduced package height
- Low switching losses
- Low junction to case thermal resistance
- Very rugged and easy mounting
- Direct mounting to heatsink (isolated package)

Part #	Package	Marking
GCMX3P5B120S7B1	S7	GCMX3P5B120S7B1

Applications

- Photovoltaic and Wind Inverter
- EV/Battery charger
- Energy storage system
- High voltage DC to DC converter
- Induction Heating
- SMPS and UPS

RoHS

Absolute Maximum Ratings, at T_J=25°C, unless otherwise specified

Characteristics	Symbol	Conditions	Values	Unit
Drain-Source Voltage	V_{rated}	V_{GS}=0V, I_{DS}=6μA	1200	V
Continuous Drain Current	I_{DS}	T_C=25°C, V_{GS}=18V, T_J=175 °C	361	A
		T_C=65°C, V_{GS}=18V, T_J=175 °C	310	
Body Diode Drain Current	I_{SD}	T_C=25°C, V_{GS}=-4.5V, T_J=175 °C	228	
Pulsed Drain Current	$I_{DS,pulse}$	T_C=25°C, V_{GS}=18V	900	
Gate Source Voltage	V_{GSmax}		-8/22	V
	V_{GSop}	Recommended operational	-4.5/18	
Power Dissipation	P_{tot}	T_C=25°C, T_J=175°C	882	W
Junction Temperature	T_J	Continuous	-40...175	°C
Case & Storage Temperature	T_C, $T_{storage}$	Continuous	-40...150	°C

Static Electrical Characteristics, at T_J=25°C, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_{DS}=1mA	1200	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=1200V, V_{GS}=0V	-	0.5	6	μA
		V_{DS}=1200V, V_{GS}=0V, T_J=150°C	-	2.9	600	
Gate-Source Leakage Current	I_{GSS+}	V_{GS}=22V, V_{DS}=0V	-	40	1000	nA
	I_{GSS-}	V_{GS}=-8V, V_{DS}=0V	-	-40	-1000	
Gate Threshold Voltage	$V_{GS(th)}$	V_{GS}=V_{DS}, I_{DS}=80mA	1.8	2.7	4	V
		V_{GS}=V_{DS}, I_{DS}=80mA, T_J=150°C	-	2	-	
Drain-Source On-Resistance	R_{DSon}*	V_{GS}=18V, I_{DS}=200A	-	3.6	5	mΩ
		V_{GS}=18V, I_{DS}=200A, T_J=150°C	-	5.6	-	
Transconductance	g_{fs}	V_{DS}=20V, I_{DS}=200A	-	100	-	S
		V_{DS}=20V, I_{DS}=200A, TJ=150°C	-	112	-	
Internal Gate Resistance	$R_{G(int)}$	f=1MHz, V_{AC}=25mV, D-S Short, including internal 0.7 ohm series gate resistor**	-	1.5	-	Ω

*R_{DSon} including package resistance

**Internal series gate resistor limits maximum switching frequency defined by Figure 31

AC Electrical Characteristics, at T_J=25°C, unless otherwise specified

Characteristics	Symbol	Conditions		Values			Unit
				min.	typ.	max.	
Input Capacitance	C_{ISS}	V_{GS}=0V, V_{DS}=800V, f=200kHz, V_{AC}=25mV		-	27.7	-	nF
Output Capacitance	C_{OSS}			-	1.02	-	
Reverse Transfer Capacitance	C_{RSS}			-	0.09	-	
Coss Stored Energy	E_{OSS}***			-	403	-	μJ
Turn-On Switching Energy	E_{ON}	T_J=25°C	V_{DD}=600V, I_{DS}=200A, $R_{G(ext)}$=1Ω, V_{GS}=-4.5/+18V, L=90μH,	-	3.08	-	mJ
		T_J=125°C		-	3.54	-	
		T_J=150°C		-	3.77	-	
Turn-Off Switching Energy	E_{OFF}	T_J=25°C		-	1.50	-	
		T_J=125°C		-	1.67	-	
		T_J=150°C		-	1.70	-	
Turn-On Delay Time	$t_{D(on)}$	V_{DD}=600V, I_{DS}=200A, $R_{G(ext)}$=1Ω, V_{GS}=-4.5/+18V, L=90μH,		-	52	-	ns
Rise Time	t_R			-	22	-	
Turn-Off Delay Time	$t_{D(off)}$			-	132	-	
Fall Time	t_F			-	29	-	
Total Gate Charge	Q_G	V_{DD}=800V, I_{DS}=200A, V_{GS}=-4.5/+18V		-	1079	-	nC
Gate to Source Charge	Q_{GS}			-	339	-	
Gate to Drain Charge	Q_{GD}			-	225	-	

***E_{OSS} is calculated from C_{OSS} curve

Freewheeling Diode Characteristics, at T_J=25°C, unless otherwise specified

Characteristics	Symbol	Conditions		Values			Unit
				min.	typ.	max.	
Diode Forward Voltage	V_{SD}	V_{GS}=-5V, I_{SD}=200A		-	4.3	-	V
		V_{GS}=-5V, I_{SD}=200A, T_J=150°C		-	3.8	-	
Reverse Recovery Time	t_{RR}	T_J=25°C	I_S=200A, V_R=600V, V_{GS}=-4.5V, di/dt=11.9A/ns	-	18	-	ns
Reverse Recovery Charge	Q_{RR}			-	1665	-	nC
Peak Reverse Recovery Current	I_{RRM}			-	144	-	A
Reverse Recovery Energy	E_{RR}	T_J=25°C	I_S=200A, V_R=600V, V_{GS}=-4.5/18V, $R_{G(ext)}$=1Ω	-	0.43	-	mJ
		T_J=125°C		-	0.87	-	
		T_J=150°C		-	1.06	-	

Thermal and Package Characteristics, at T_j=25 °C, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Thermal resistance, junction-case	R_{thJC}		-	0.150	0.170	°C/W
Mounting torque	M_d	M6-1.0 screws	-	-	5.0	N-m
Terminal connection torque	M_{dt}	M5-0.8 screws	-	-	5.0	N-m
Package weight	W_t		-	250	-	g
Isolation voltage	V_{ISOL}	I_{ISOL} < 1mA,50/60 Hz, 1 min	4000	-	-	V

NTC Characteristics, at T_j=25 °C, unless otherwise specified

Characteristics	Symbol	Conditions	Values			Unit
			min.	typ.	max.	
Rated resistance	R_{NTC}	T_{NTC} = 25°C	-	5.0	-	kΩ
Resistance tolerance	ΔR/R		-5	-	5	%
Beta Value (T_2 = 50°C)	$\beta_{25/50}$		-	3380	-	k
Beta Value (T_2 = 80°C)	$\beta_{25/80}$		-	3440	-	k
Power dissipation	P_{MAX}	T_{NTC} = 25°C	-	-	50	mW

Typical Performance

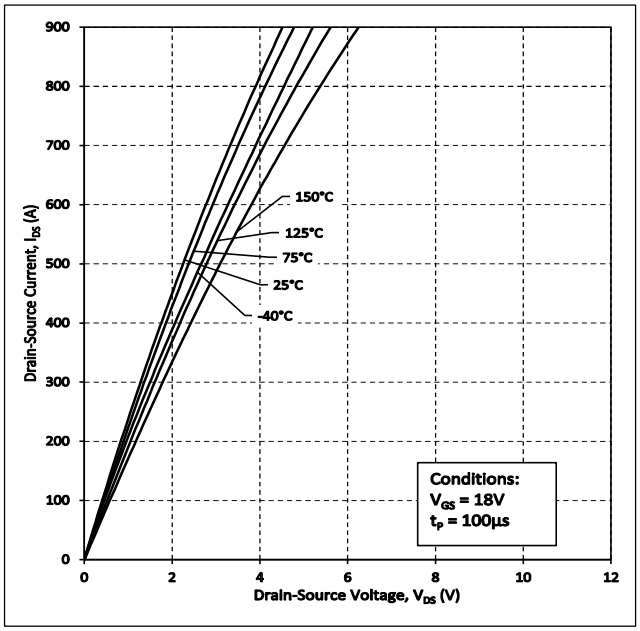


Figure 1. Output Characteristics for Various Temperatures

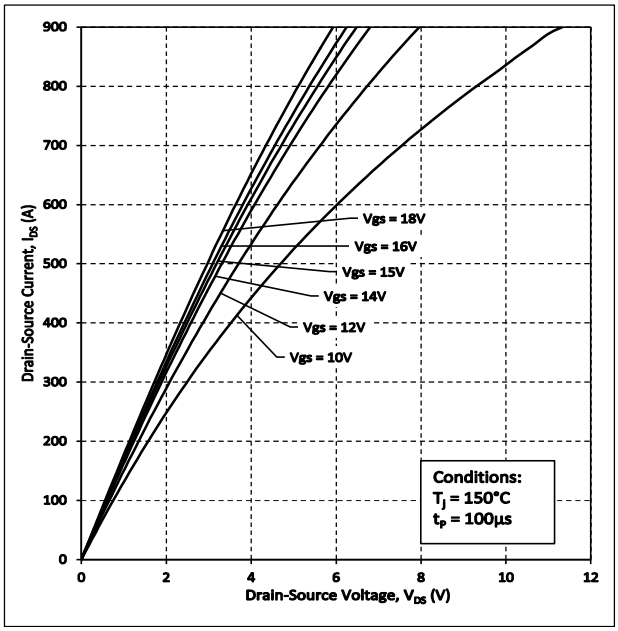


Figure 2. Output Characteristics T_J = 150°C

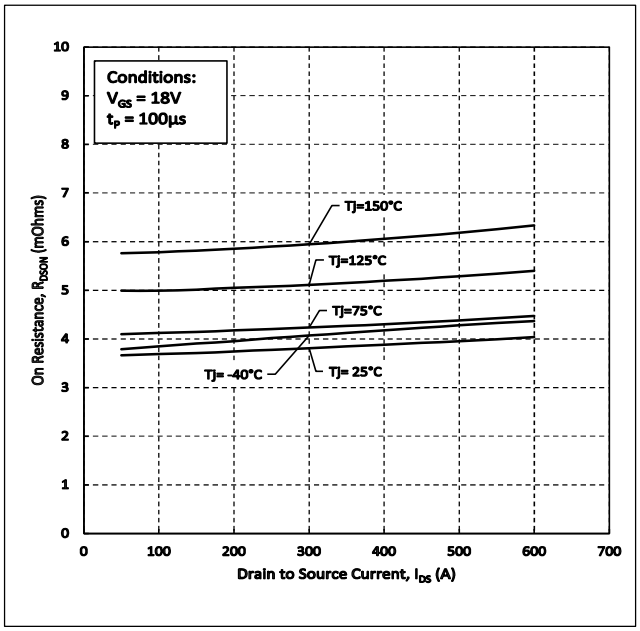


Figure 3. On-Resistance vs. Drain Current For Various Temperatures

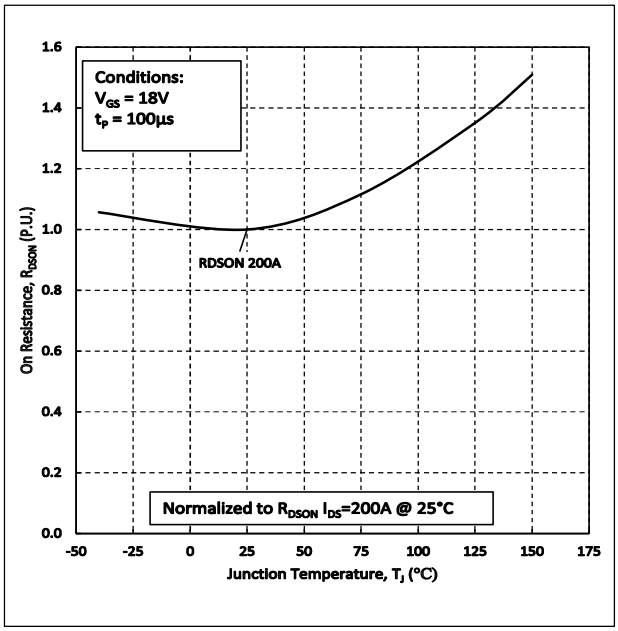


Figure 4. Normalized On-Resistance vs. Temperature

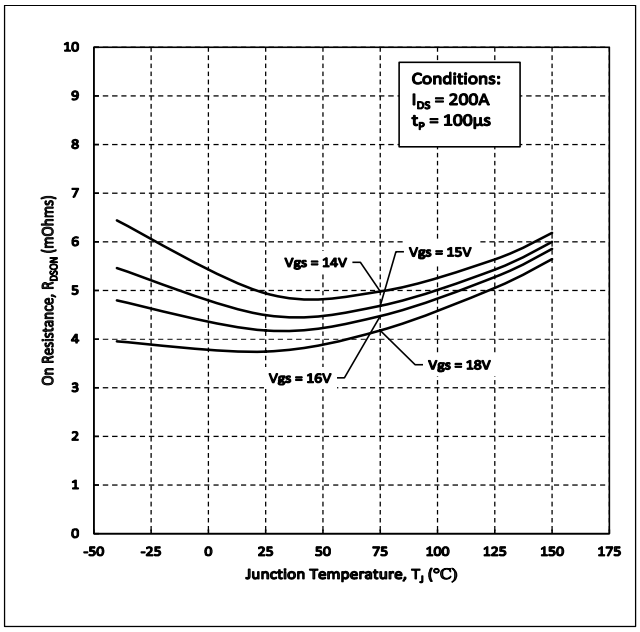


Figure 5. On-Resistance vs. Temperature For Various Gate Voltages

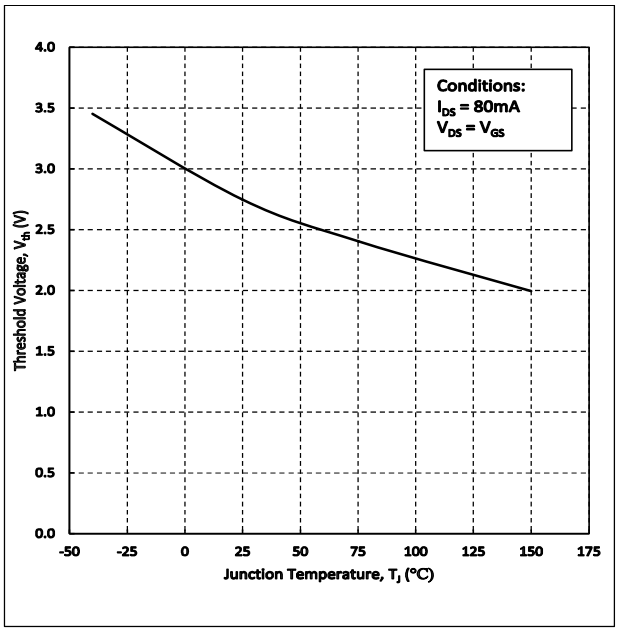


Figure 6. Threshold Voltage vs. Temperature

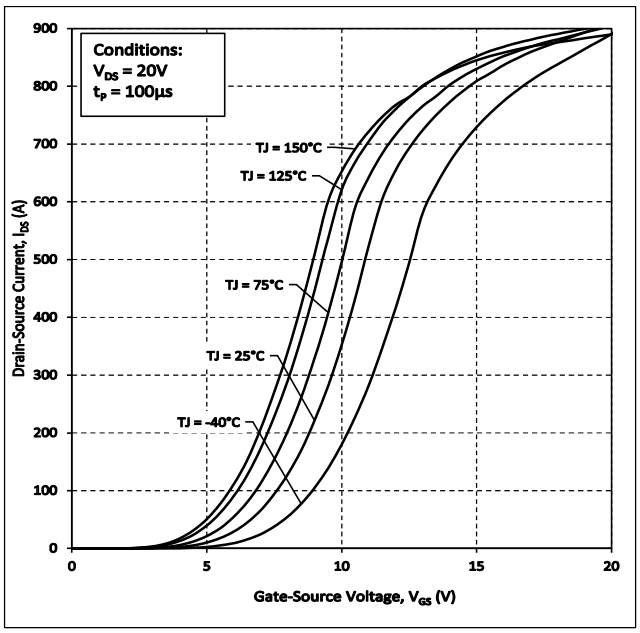


Figure 7. Transfer Characteristic for Various Junction Temperatures

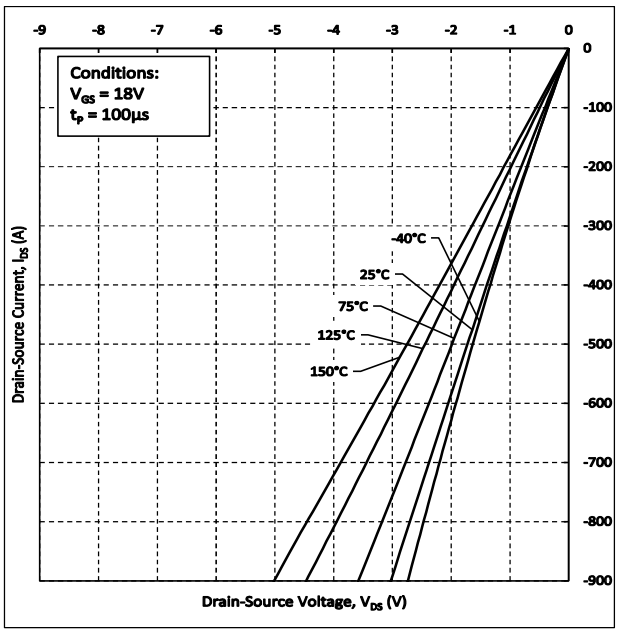


Figure 8. 3rd Quadrant Characteristics at V_{GS} = 18V

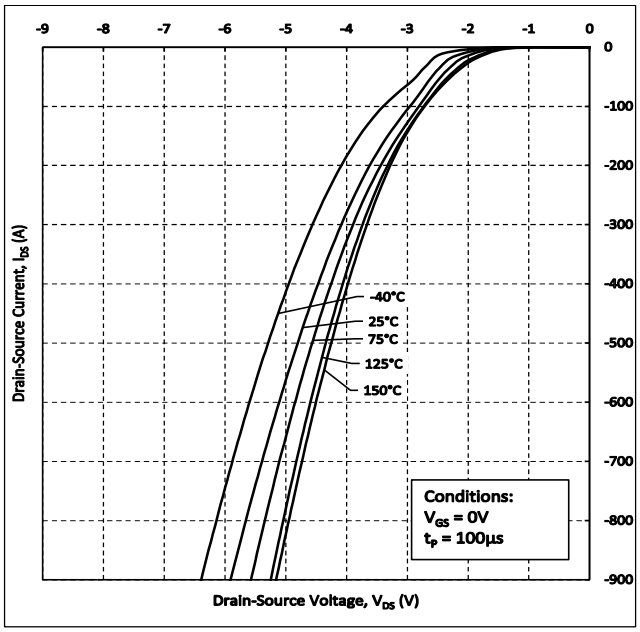


Figure 9. Body Diode Characteristics at V_{GS} = 0V

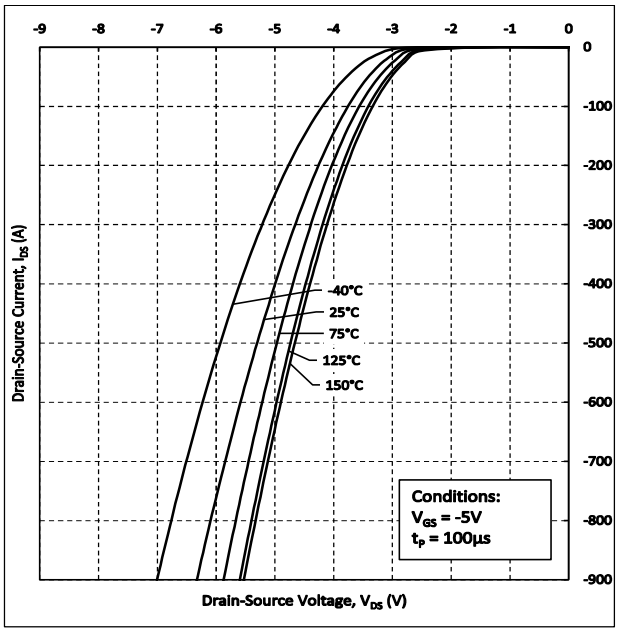


Figure 10. Body Diode Characteristics at V_{GS} = -5V

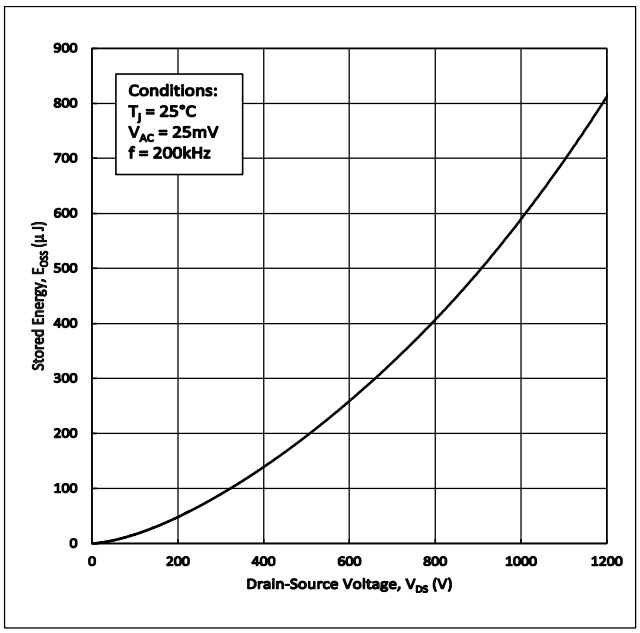


Figure 11. Output Capacitor Stored Energy

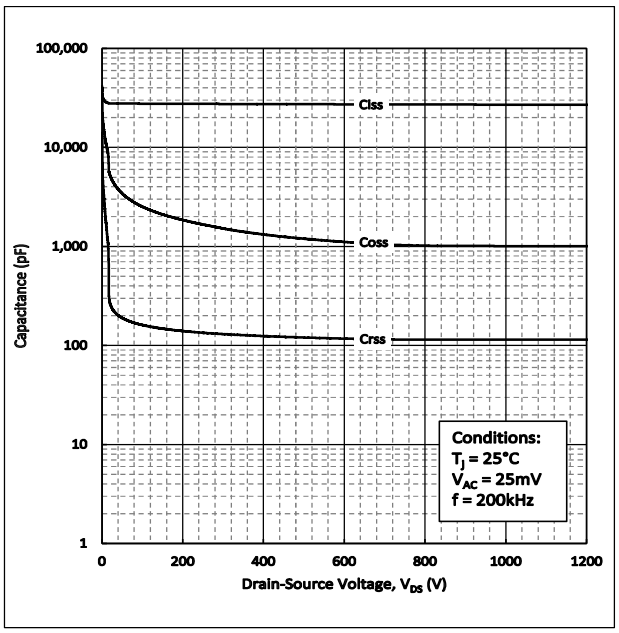


Figure 12. Capacitance vs. Drain-Source Voltage

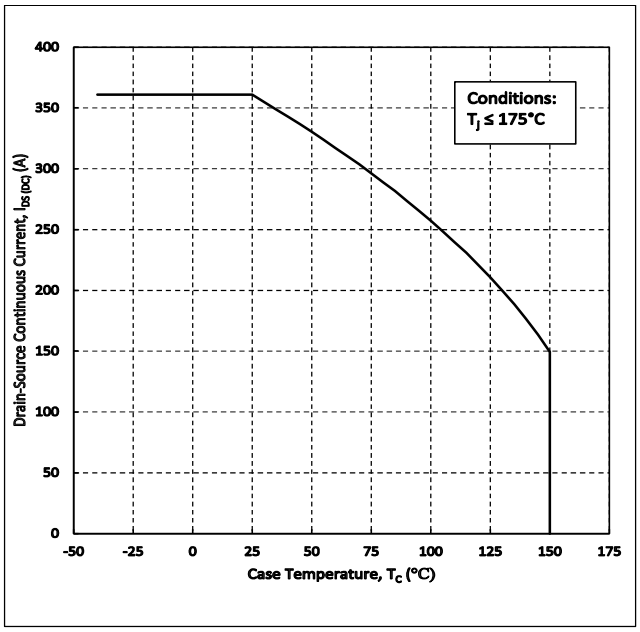


Figure 13. Continuous Drain Current Derating vs. Case Temperature

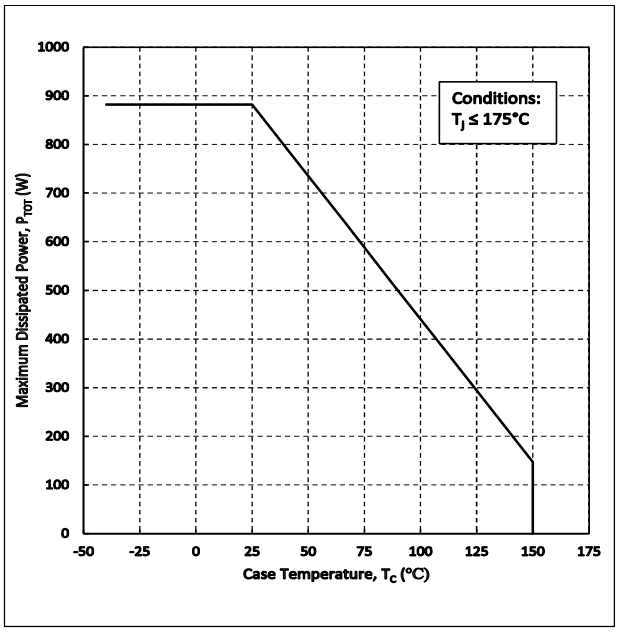


Figure 14. Maximum Power Dissipation Derating vs. Case Temperature

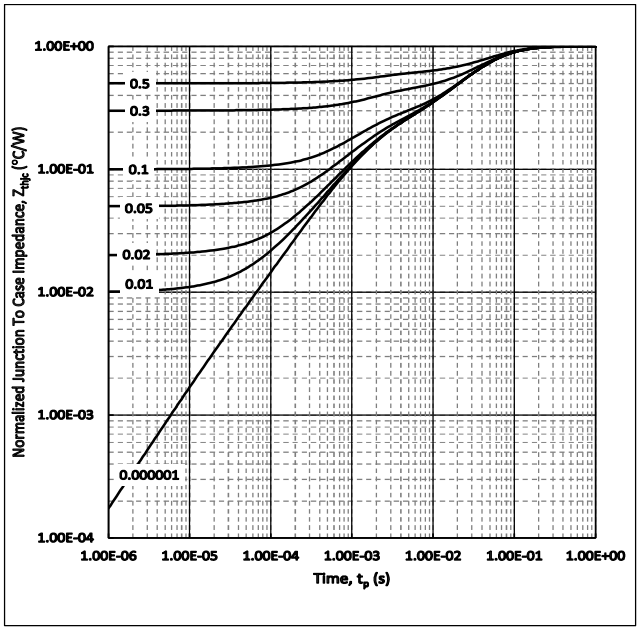


Figure 15. Transient Thermal impedance (Junction to Case)

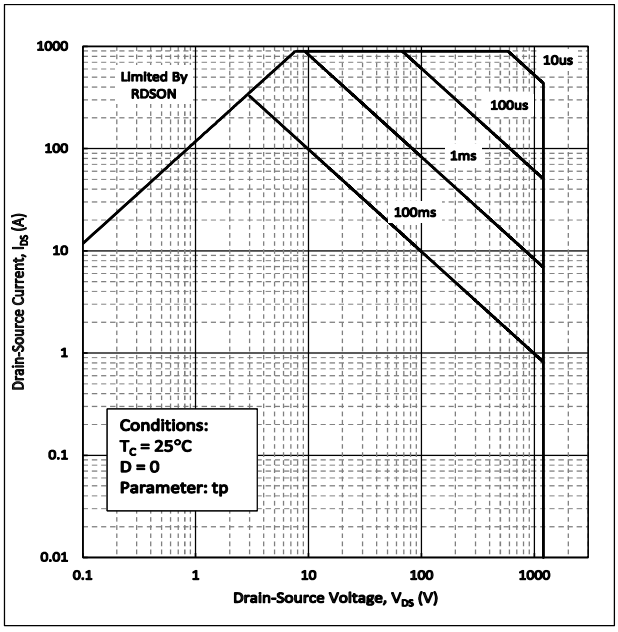


Figure 16. Safe Operating Area

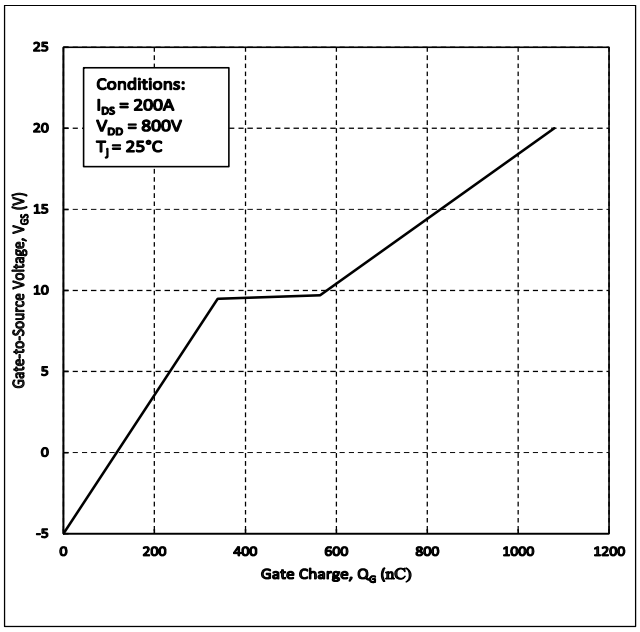


Figure 17. Gate Charge Characteristics

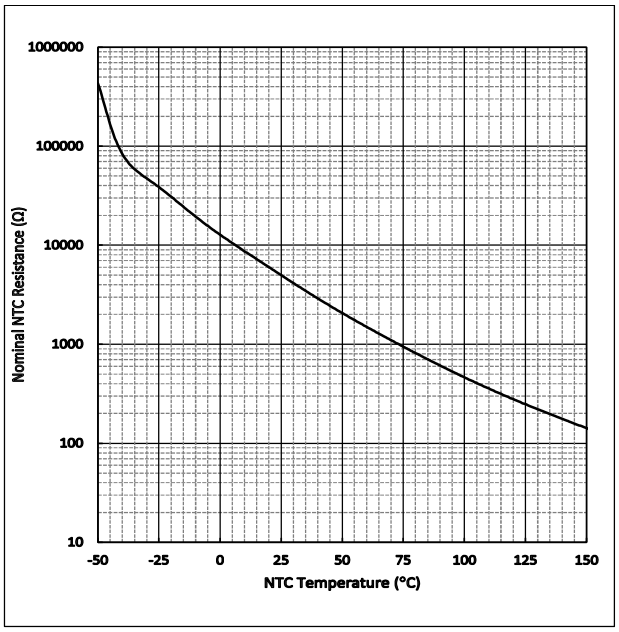


Figure 18. Nominal NTC Resistance vs. Temperature

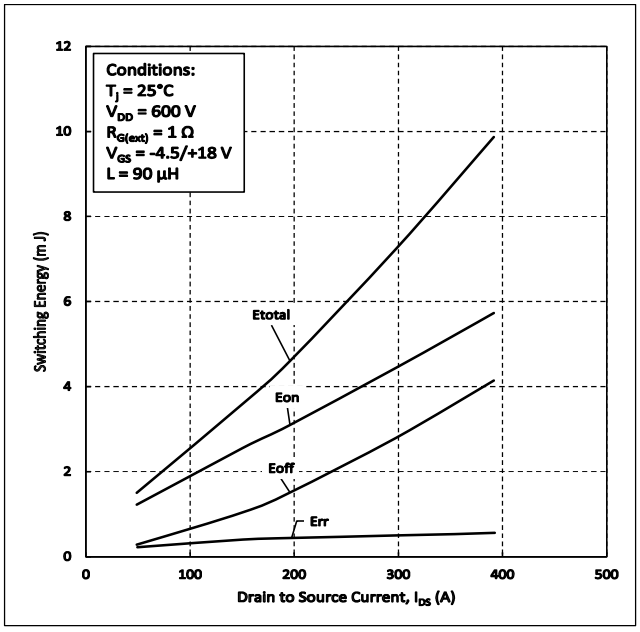


Figure 19. Clamped Inductive Switching Energy vs. Drain Current (600V)

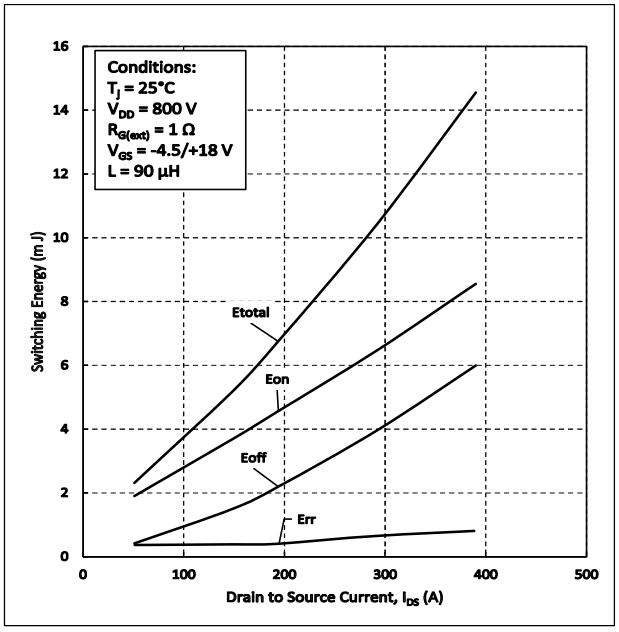


Figure 20. Clamped Inductive Switching Energy vs. Drain Current (800V)

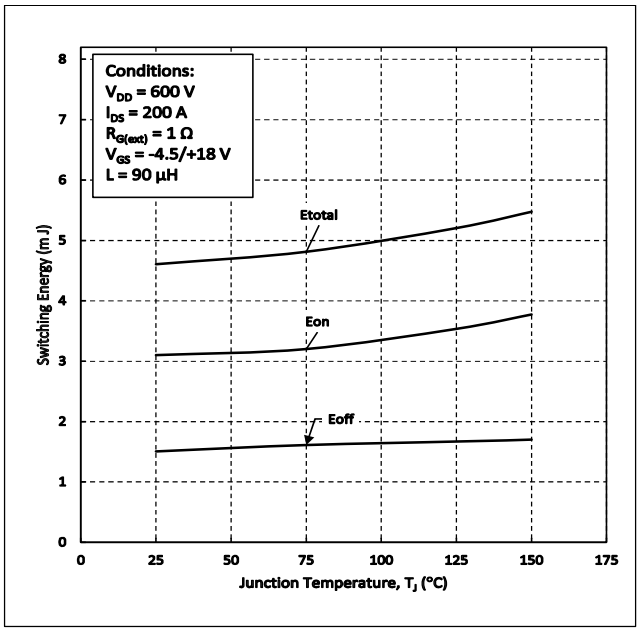


Figure 21. Clamped Inductive Switching Energy vs. Temperature

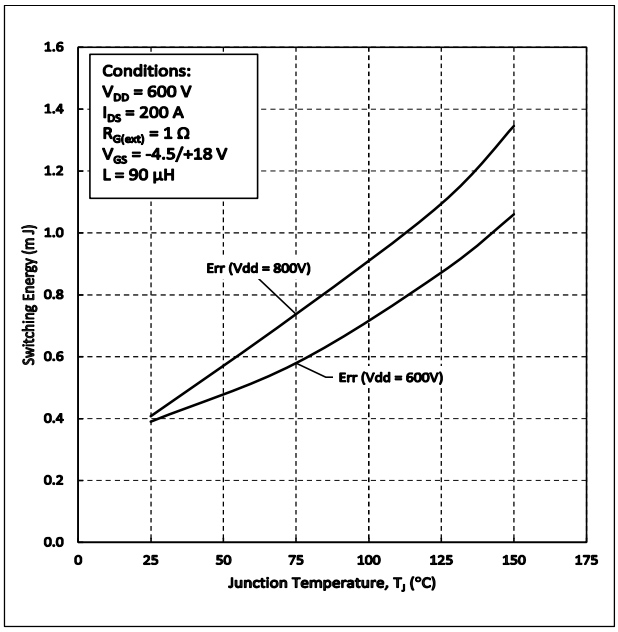


Figure 22. Reverse Recovery Energy vs. Temperature

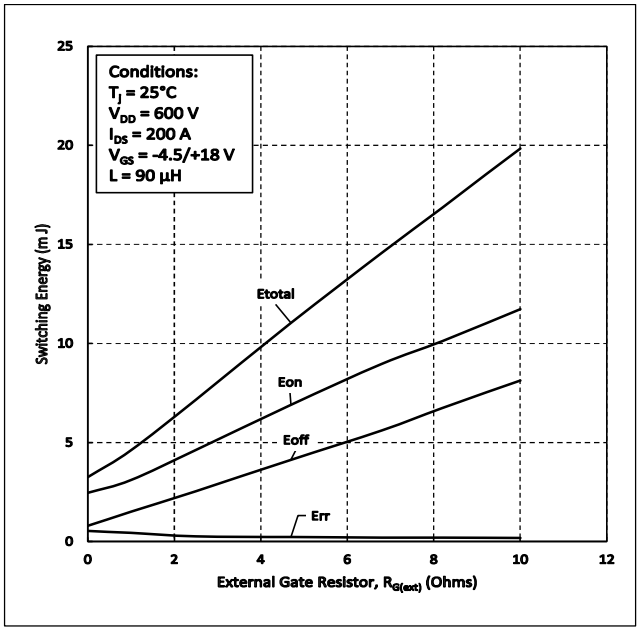


Figure 23. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

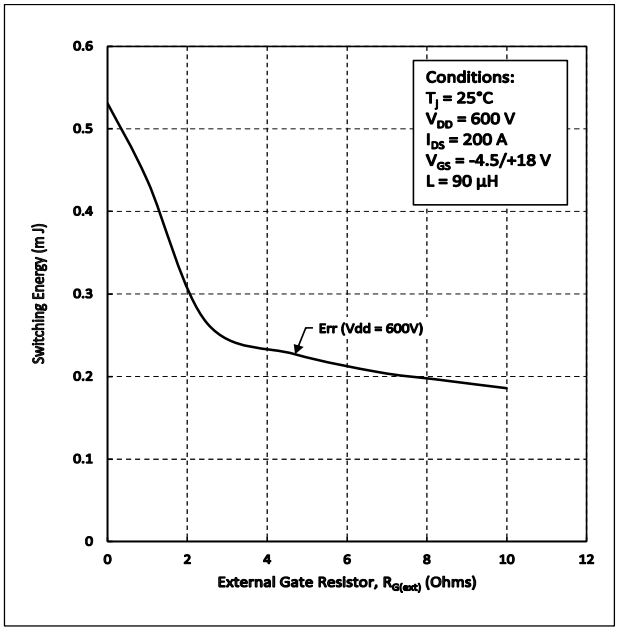


Figure 24. Reserve Recovery Energy vs. $R_{G(ext)}$

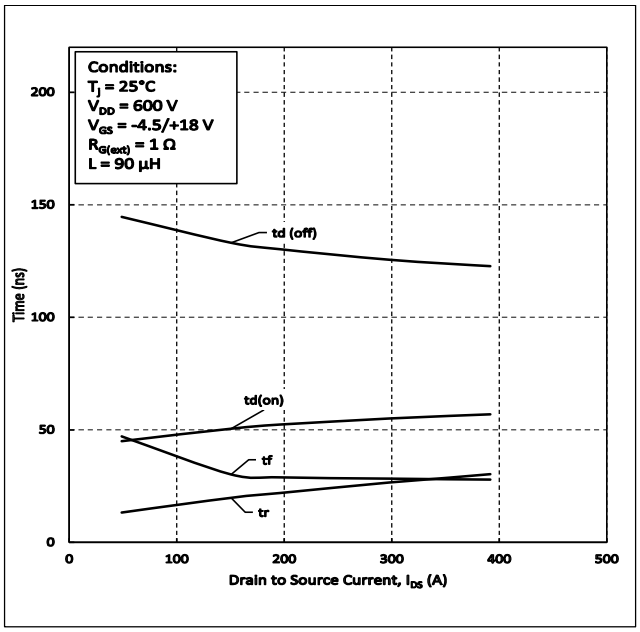


Figure 25. Switching Times vs. Drain Current

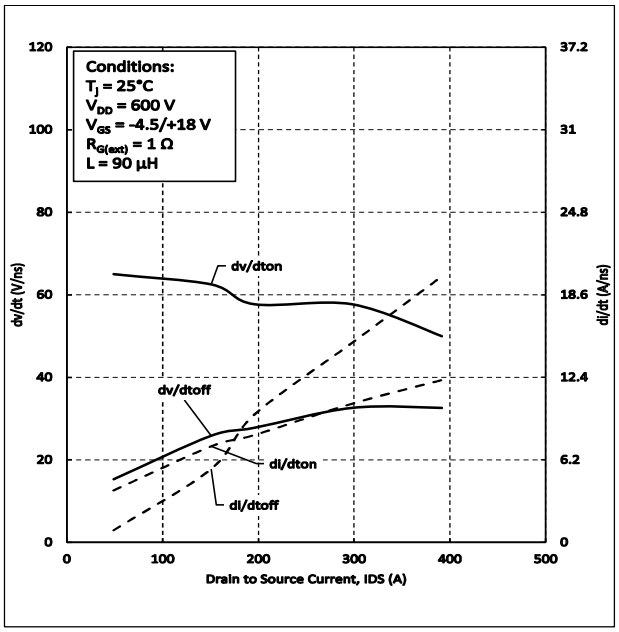


Figure 26. dv/dt and di/dt vs. Source Current

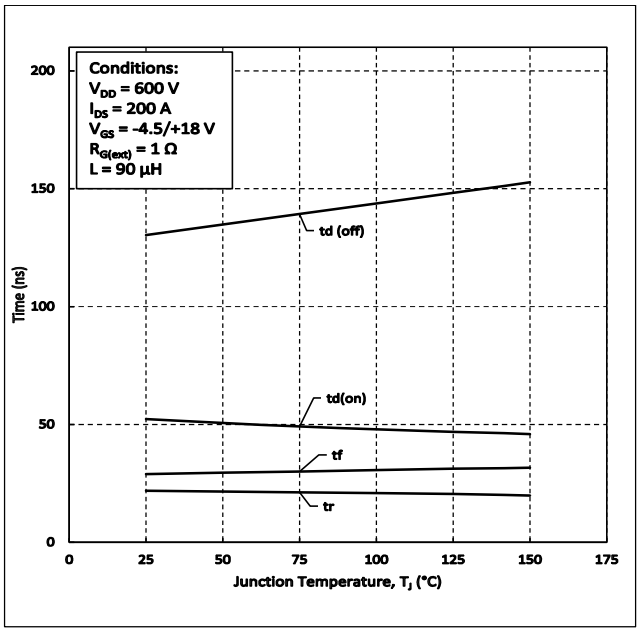


Figure 27. Switching Times vs. Temperature

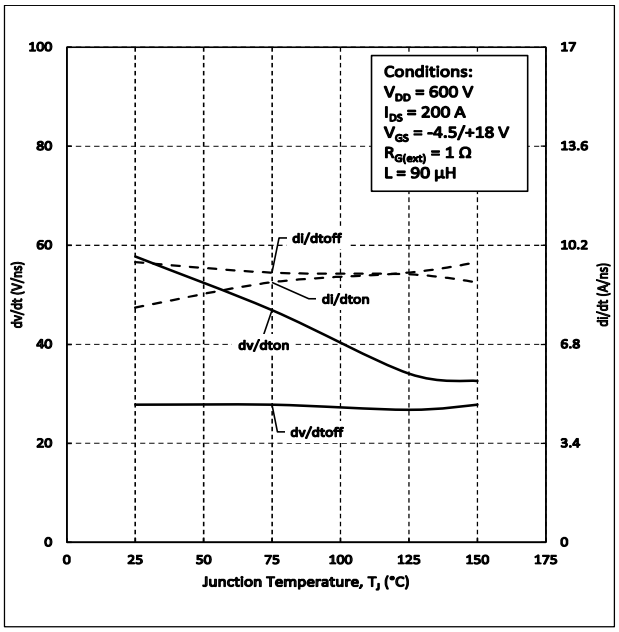


Figure 28. dv/dt and di/dt vs. Temperature

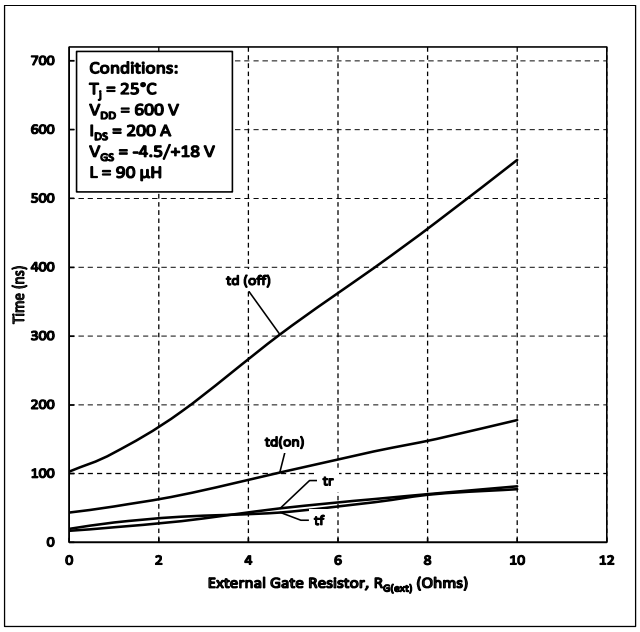


Figure 29. Switching Times vs. $R_{G(ext)}$

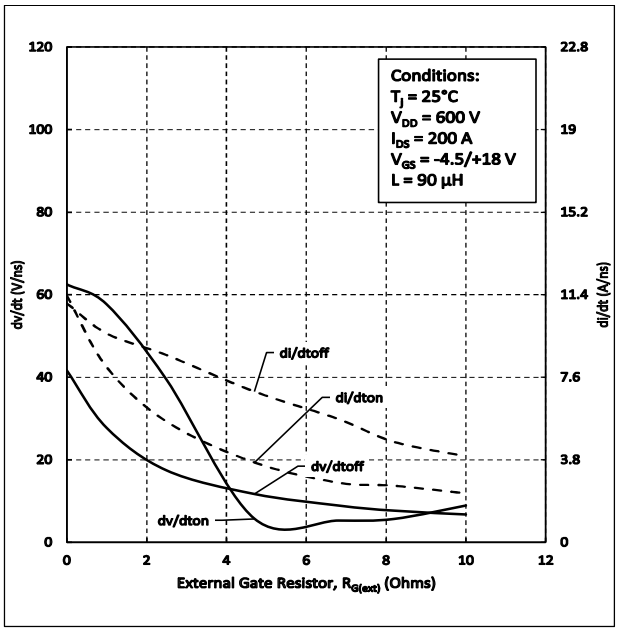


Figure 30. dv/dt and di/dt vs. $R_{G(ext)}$

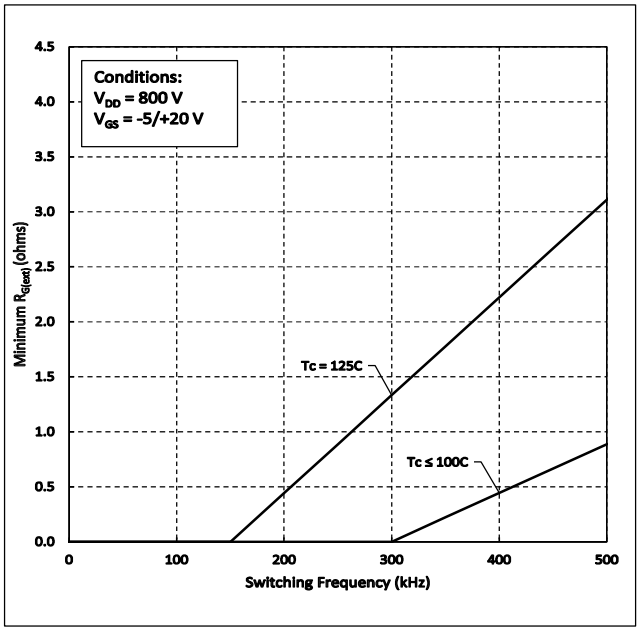


Figure 31. Frequency vs Minimum $R_{G(ext)}$

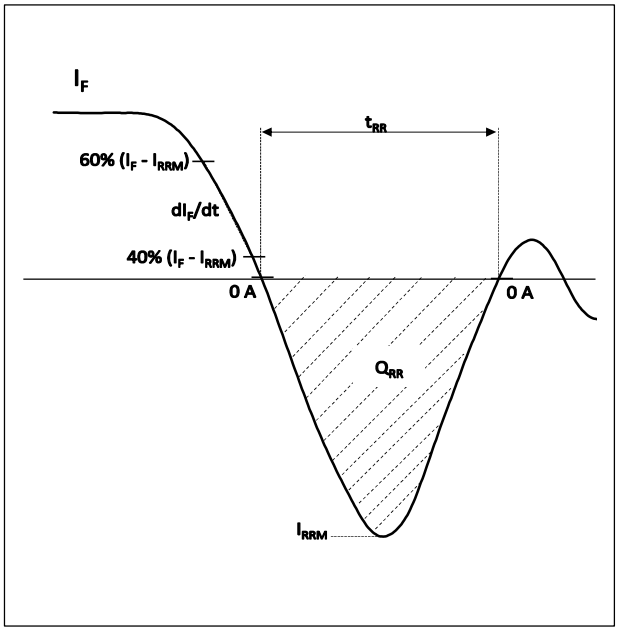


Figure 32. Reverse Recovery Definitions

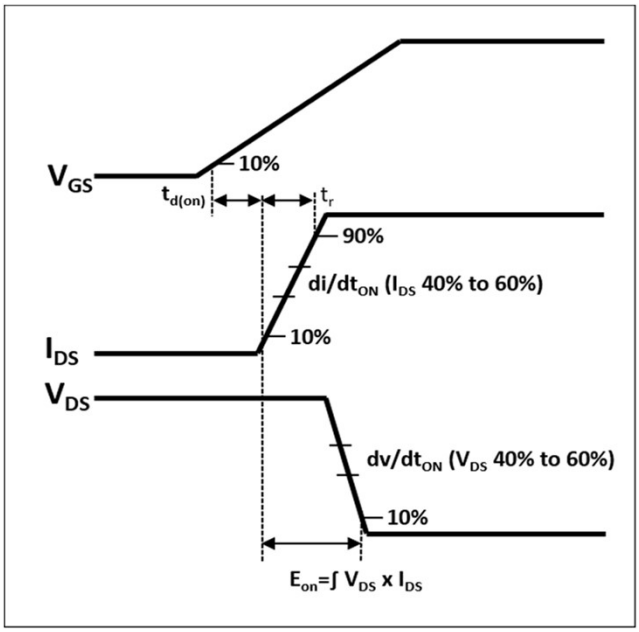


Figure 33. Turn-on Transient Definitions

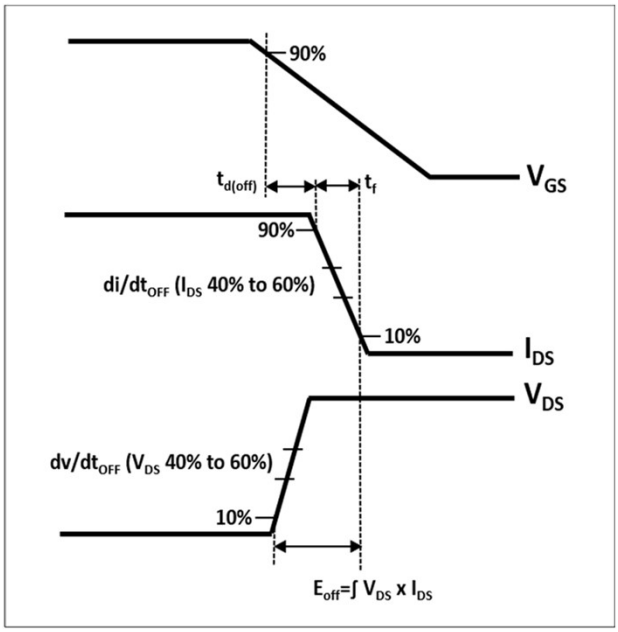
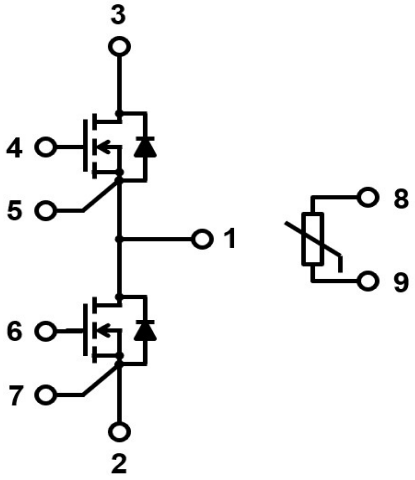
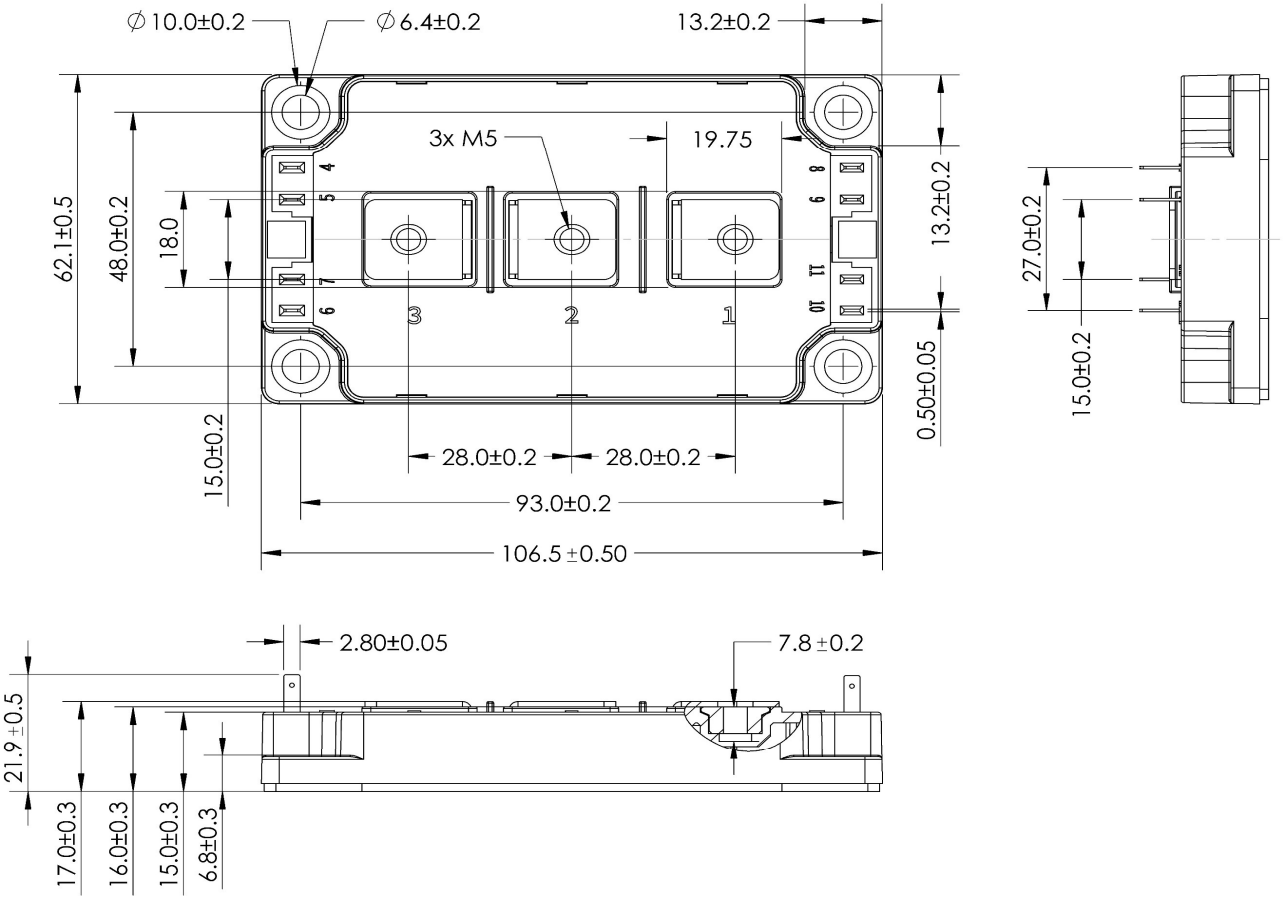


Figure 34. Turn-off Transient Definitions

Pinout and Circuit Diagram



Package Dimensions (mm)



Revision History		
Date	Revision	Notes
11/21/2025	1.0	Initial release

Notes

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2015/863 (RoHS3), as implemented July, 2019. RoHS Declarations for this product can be obtained from the Product Documentation sections of www.SemiQ.com.

REACh Compliance

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